

General Description

The AGM40P30A combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

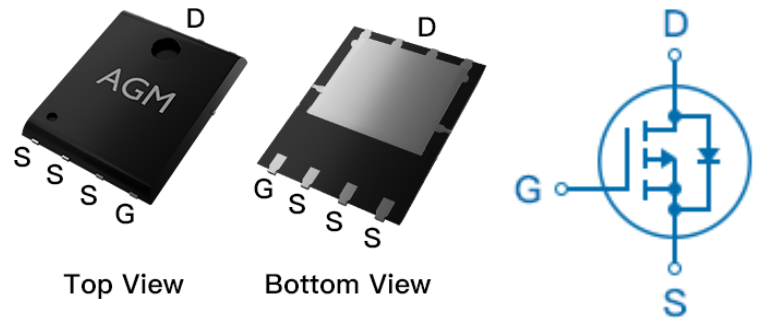
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
-40V	23mΩ	-38A

PDFN5*6 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM40P30A	AGM40P30A	PDFN5*6	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-40	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	-38	A
	Drain Current-Continuous(Tc=100°C)	-27	A
IDM (pluse)	Drain Current-Pulsed (Note 2)	-99	A
PD	Maximum Power Dissipation(Tc=25°C)	59	w
	Maximum Power Dissipation(Tc=100°C)	23	w
EAS	Avalanche energy (Note 3)	210	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	60	°C/W
RθJC	Thermal Resistance Junction-Case ¹	---	2.1	°C/W

Table 3. Electrical Characteristics (T_J=25°C unless otherwise noted)

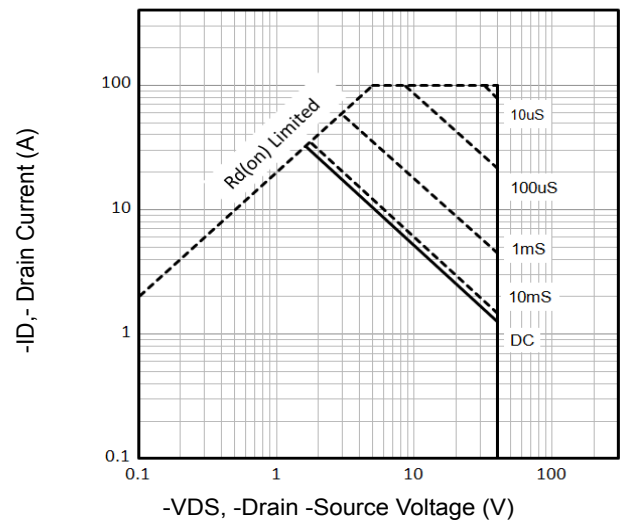
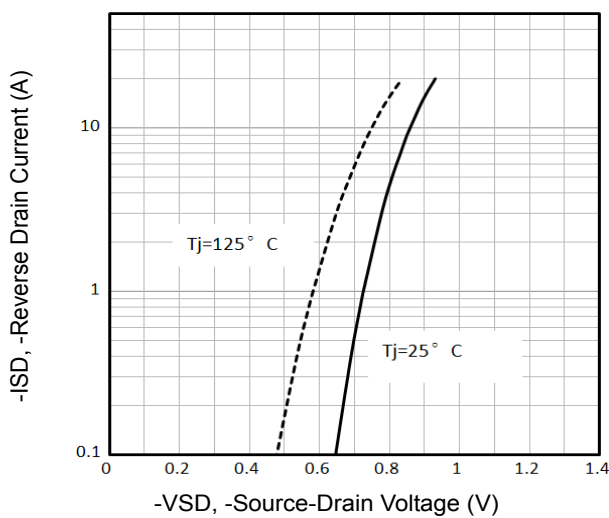
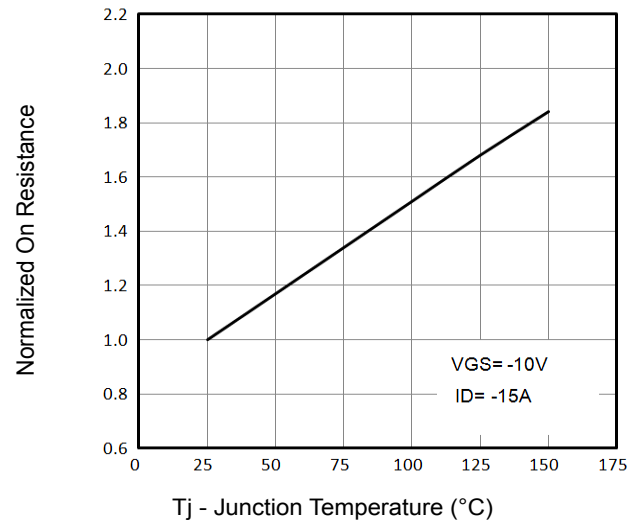
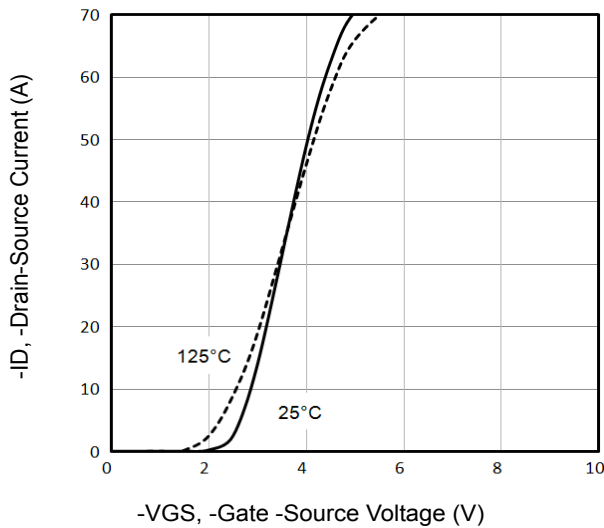
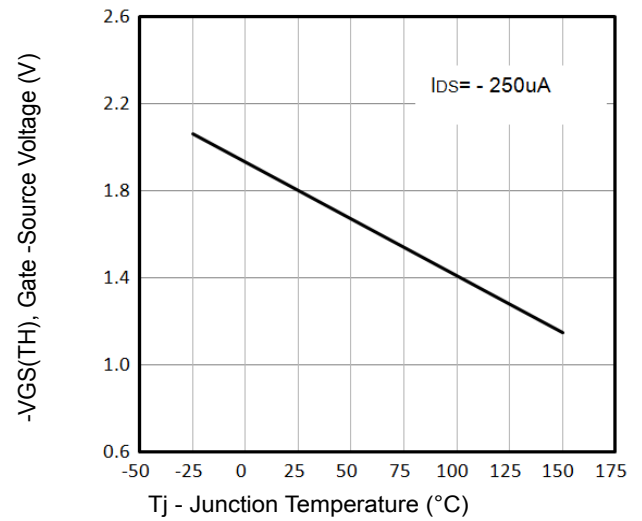
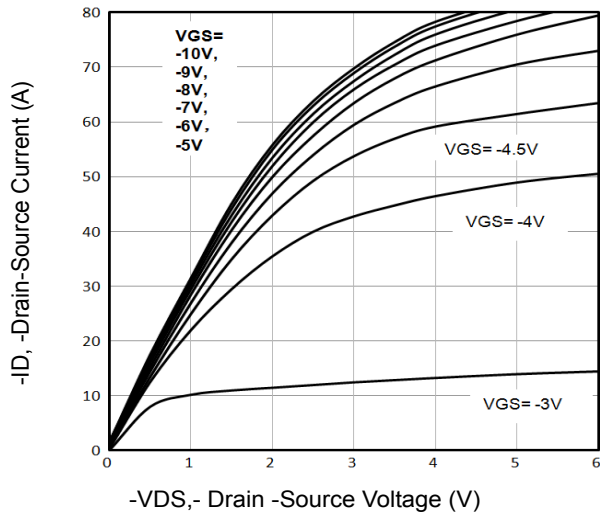
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-40V,VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.2	-1.6	-2.2	V
gFS	Forward Transconductance	VDS=-5V,ID=-10A	--	17	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-15A	--	23	32	mΩ
		VGS=-4.5V, ID=-10A	--	34	43	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-20V,VGS=0V ,F=1MHZ	--	1120	--	pF
Coss	Output Capacitance		--	120	--	pF
Crss	Reverse Transfer Capacitance		--	95	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	1.2	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-10V,VDS=-25V, ID=-10A,RGEN=3.3Ω	--	13.5	--	nS
tr	Turn-on Rise Time		--	18	--	nS
td(off)	Turn-Off Delay Time		--	36	--	nS
tf	Turn-Off Fall Time		--	25	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-25V, ID=-12A	--	27	--	nC
Qgs	Gate-Source Charge		--	7.3	--	nC
Qgd	Gate-Drain Charge		--	5.6	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-38	A
VSD	Forward on Voltage	VGS=0V,IS=-15A	--	--	-1.2	V
trr	Reverse Recovery Time	Isd=-15A ,	--	--	--	ns
Qrr	Reverse Recovery Charge	dI/dt=100A/μs , TJ=25℃	--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: T_J=25°C, V_{DD}=-25V, V_{gs}=-10V, ID=-29A, L=0.5mH, R_G=25ohm

Typical Characteristics



Typical Characteristics

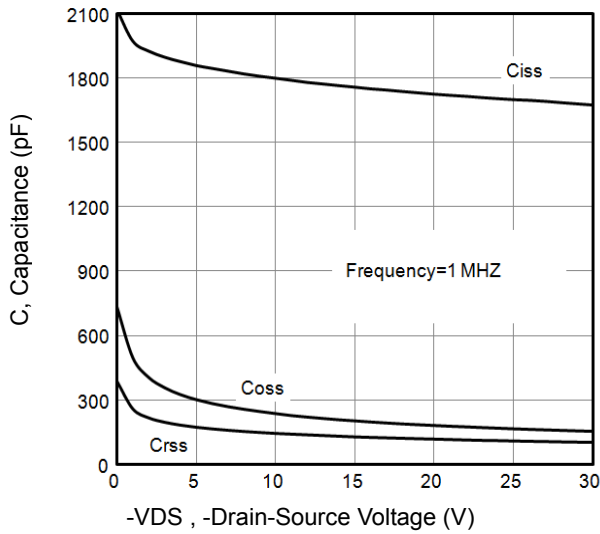


Fig7. Typical Capacitance Vs. Drain-Source Voltage

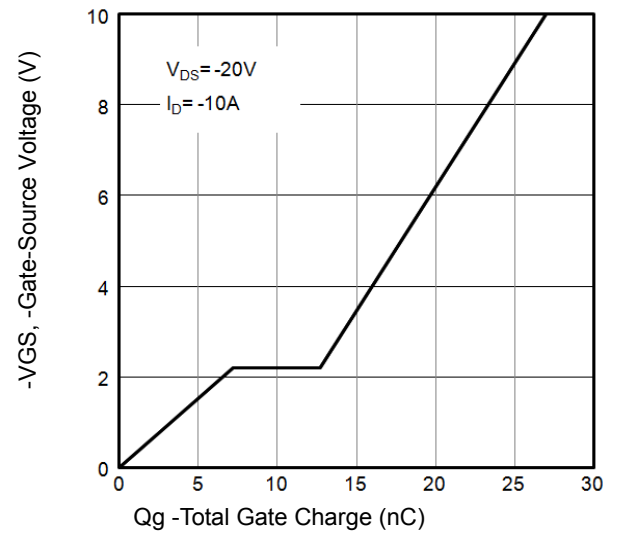


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

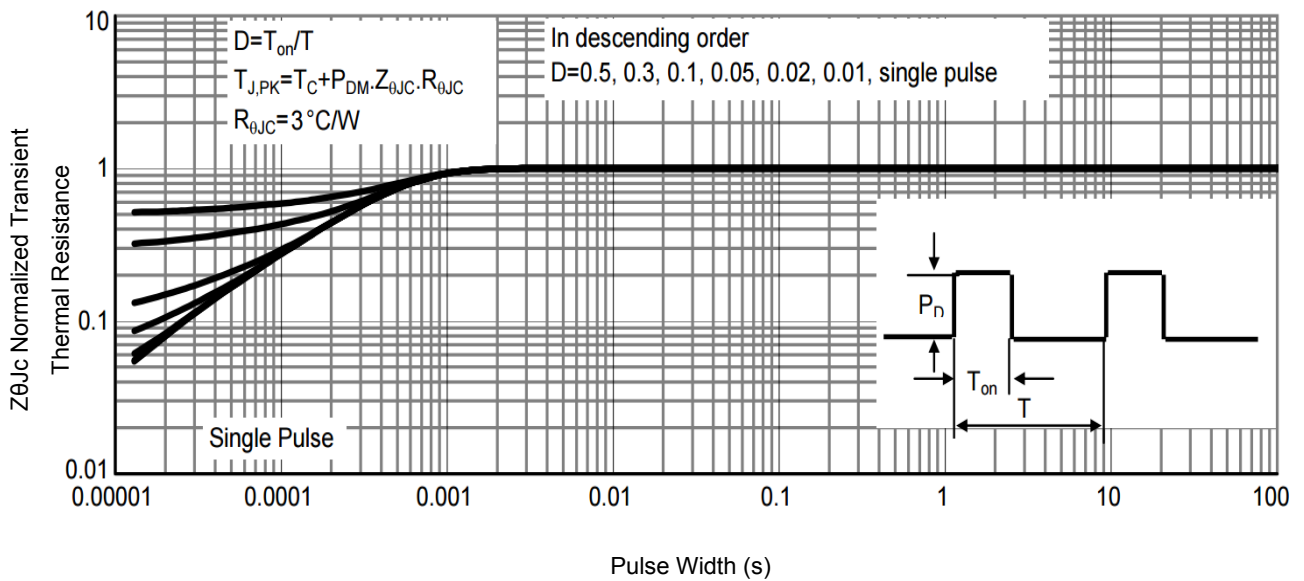


Fig9. Normalized Maximum Transient Thermal Impedance

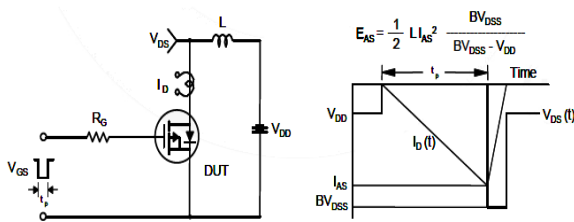


Fig10. Unclamped Inductive Test Circuit and Waveforms

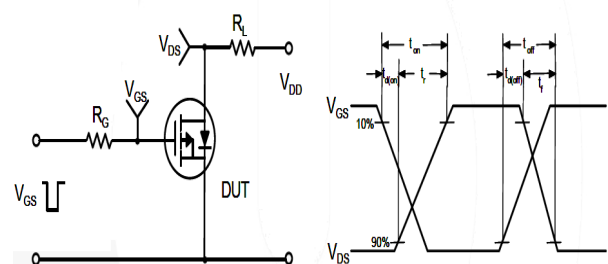


Fig11. Switching Time Test Circuit and waveforms

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