

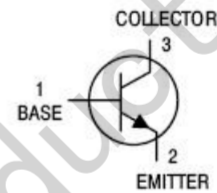
FEATURES

- Epitaxial planar die construction.
- Complementary PNP type available (MMBT3906T).
- Collector Current Capability $I_{CM} = 200\text{mA}$.
- Collector-emitter Voltage $V_{CEO} = 40\text{V}$.
- MSL 1


SOT-523 (SC-75A)

APPLICATIONS

- General switching and amplification



MAXIMUM RATING @ $T_a = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	Value	UNIT
V_{CBO}	collector-base voltage	open emitter	60	V
V_{CEO}	collector-emitter voltage	open base	40	V
V_{EBO}	emitter-base voltage	open collector	6	V
I_C	collector current (DC)		200	mA
I_{CM}	peak collector current		200	mA
I_{BM}	peak base current		100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$	250	mW
T_{stg}	storage temperature		-65 to +150	$^\circ\text{C}$
T_j	junction temperature		150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0; V_{CB} = 30\text{ V}$	-	50	nA
I_{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 6\text{ V}$	-	50	nA
h_{FE}	DC current gain	$V_{CE} = 1\text{ V};$ $I_C = 0.1\text{ mA}$	60	-	
		$I_C = 1\text{ mA}$	80	-	
		$I_C = 10\text{ mA}$	100	300	
		$I_C = 50\text{ mA}$	60	-	
		$I_C = 100\text{ mA}$	30	-	
$V_{CE(sat)}$	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 1\text{ mA}$	-	200	mV
		$I_C = 50\text{ mA}; I_B = 5\text{ mA}$	-	300	mV
$V_{BE(sat)}$	base-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 1\text{ mA}$	650	850	mV
		$I_C = 50\text{ mA}; I_B = 5\text{ mA}$	-	950	mV
C_{obo}	Output Capacitance	$I_E = I_e = 0; V_{CB} = 5\text{ V};$ $f = 1\text{ MHz}$	-	4	pF
C_{ibo}	Input Capacitance	$I_C = I_c = 0; V_{BE} = 500\text{ mV};$ $f = 1\text{ MHz}$	-	8	pF
f_T	transition frequency	$I_C = 10\text{ mA}; V_{CE} = 20\text{ V};$ $f = 100\text{ MHz}$	300	-	MHz
F	noise figure	$I_C = 100\text{ mA}; V_{CE} = 5\text{ V};$ $R_S = 1\text{ k}\Omega; f = 10\text{ Hz to } 15.7\text{ kHz}$	-	5	dB
Switching times (between 10% and 90% levels);					
t_d	delay time	$V_{CC} = 3\text{ Vdc}, V_{BE} = -0.5\text{ Vdc}$	-	35	ns
t_r	rise time	$I_C = 10\text{ mAdc}, I_{B1} = 1\text{ mAdc}$	-	35	ns
t_s	storage time	$V_{CC} = 3\text{ Vdc}, I_C = 10\text{ mAdc}$	-	200	ns
t_f	fall time	$I_{B1} = I_{B2} = 1\text{ mAdc}$	-	50	ns

 Note Pulse test: $t_p \leq 300\text{ ms}; d \leq 0.02$.

TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

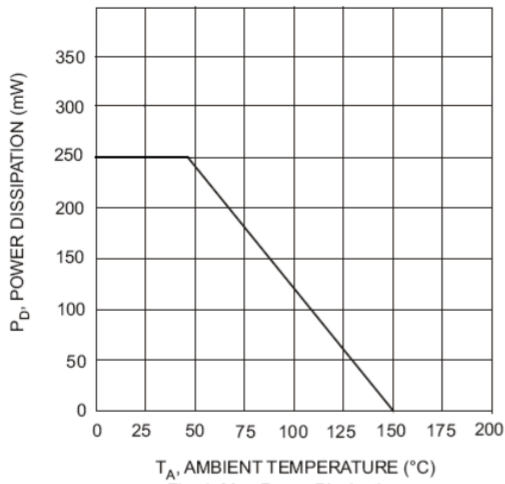


Fig. 1, Max Power Dissipation vs Ambient Temperature

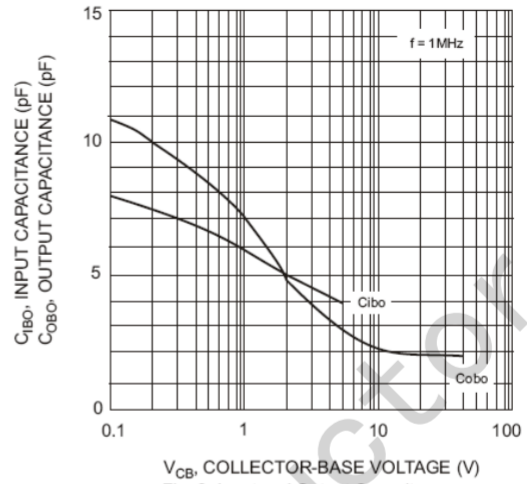


Fig. 2, Input and Output Capacitance vs. Collector-Base Voltage

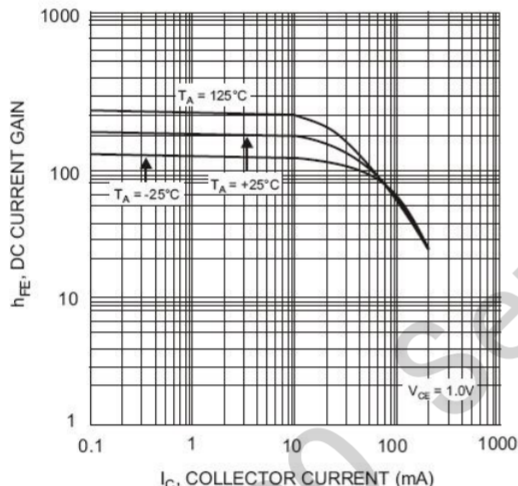


Fig. 3, Typical DC Current Gain vs Collector Current

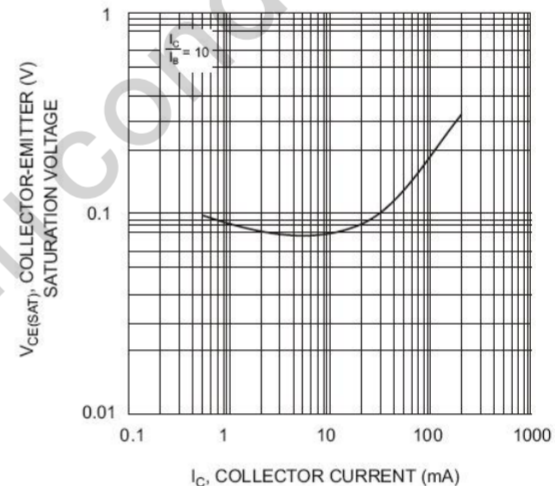


Fig. 4, Typical Collector-Emitter Saturation Voltage vs. Collector Current

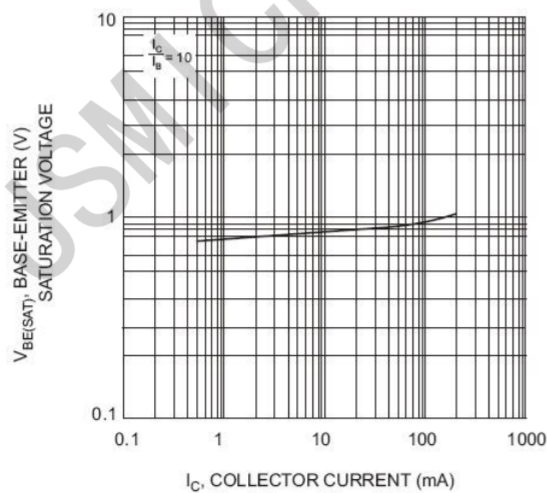
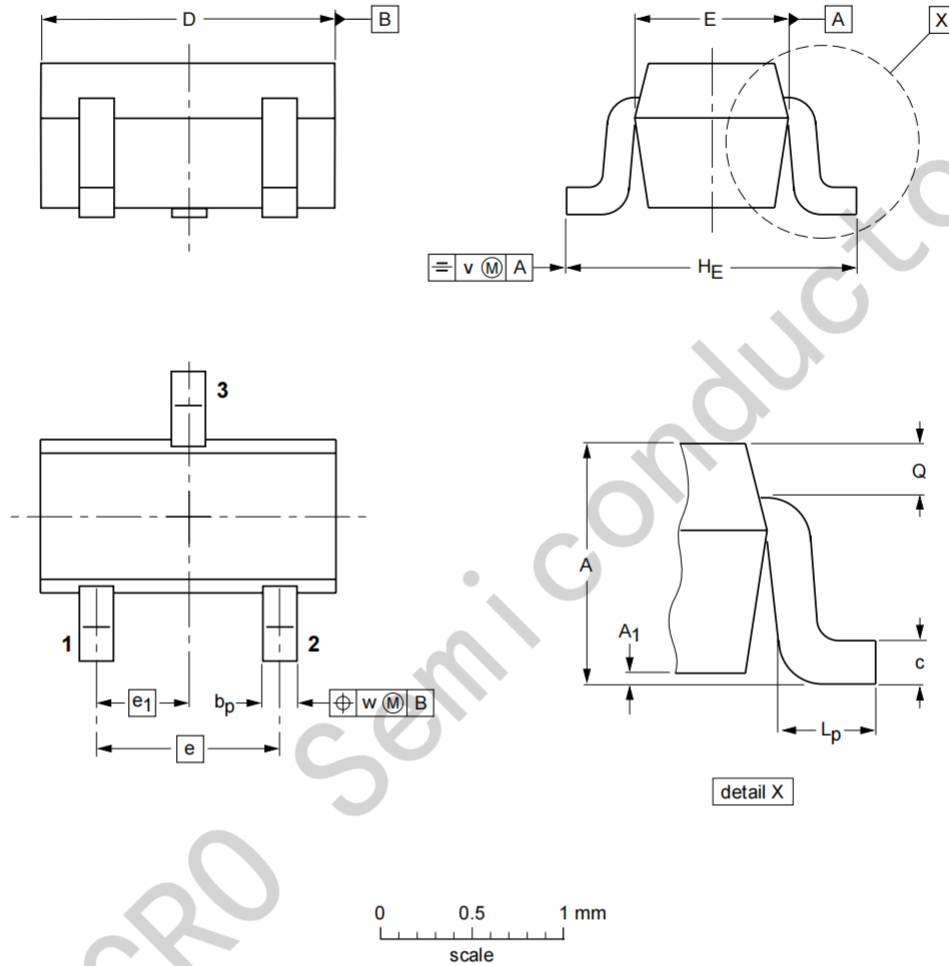


Fig. 5, Typical Base-Emitter Saturation Voltage vs. Collector Current

Package Information

SOT-523



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	0.95 0.60	0.1	0.30 0.15	0.25 0.10	1.8 1.4	0.9 0.7	1	0.5	1.75 1.45	0.45 0.15	0.23 0.13	0.2	0.2