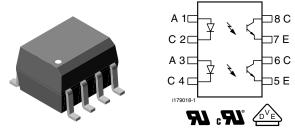
# ILD1206T, ILD1207T

### Vishay Semiconductors

## Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 Package, 100 °C Rated



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### LINKS TO ADDITIONAL RESOURCES



### DESCRIPTION

The 100 % rated ILD1206T and ILD1207T are optically coupled pairs with a Gallium Arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The ILD1206T and ILD1207T come in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through-holes requirements, this package conforms to standards for surface mounted devices.

A specified minimum and maximum CTR allows a narrow tolerance in the electrical design of the adjacent circuits. The high  $BV_{CEO}$  of 70 V gives a higher safety margin compared to the industry standard of 30 V.

### FEATURES

### • Two channel coupler

- SOIC-8 surface mountable package
- Standard lead spacing of 0.05"
- Available only on tape and reel option (conforms to EIA standard 481-2)
- Isolation test voltage, 4000 V<sub>RMS</sub>
- Compatible with dual wave, vapor phase and IR reflow soldering
- Operating temperature from 55 °C to + 110 °C
- Lead (Pb)-free component
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- AC adapters
- PLCs
- · Switch mode power supplies
- DC/DC converters
- Microprocessor I/O interfaces
- · General impedance matching circuits

### AGENCY APPROVALS

- <u>UL</u> / <u>cUL</u> 1577
- DIN EN 60747-5-5 (VDE 0884), available with option 1

ORDERING INFORMATION					
	1 2 0 #	T SOIC-8			
	PART NUMBER	6.1 mm			
AGENCY CERTIFIED / PACKAGE	CTR	(%)			
AGENCY CERTIFIED / PACKAGE	10 mA				
UL, cUL, VDE	63 to 125	100 to 200			
SOIC-8	ILD1206T	ILD1207T			

#### Note

• For additional information on the available options refer to option information

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RoHS

COMPLIANT

# ILD1206T, ILD1207T



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<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT				-		
Peak reverse voltage		V <sub>R</sub>	6	V		
Peak pulsed voltage	1 µs, 300 pps		1	А		
Continuous forward current per channel			30	mA		
Power dissipation		P <sub>diss</sub>	50	mW		
Derate linearly from 25 °C			0.5	mW/°C		
OUTPUT				-		
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V		
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7	V		
Power dissipation per channel		P <sub>diss</sub>	125	mW		
Derate linearly from 25 °C			1.25	mW/°C		
COUPLER						
Isolation test voltage	t = 1 min	V <sub>ISO</sub>	3333	V <sub>RMS</sub>		
Total package dissipation ambient (2 LEDs and 2 detectors, 2 channels)		P <sub>tot</sub>	300	mW		
Derate linearly from 25 °C			4	mW/°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature		T <sub>amb</sub>	-55 to +110	°C		
Soldering time from 260 °C		T <sub>sld</sub>	10	S		

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum rating for extended periods of the time can adversely affect reliability.

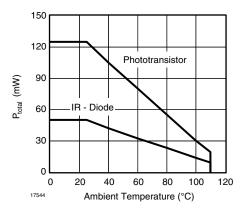


Fig. 1 - Power Dissipation vs. Ambient Temperature



## Vishay Semiconductors

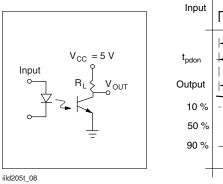
<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>	-	1.2	1.55	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	-	0.1	100	μA
Capacitance	V <sub>R</sub> = 0 V		Co	-	25	-	pF
OUTPUT							
Collector emitter breakdown voltage	I <sub>C</sub> = 10 μA		BV <sub>CEO</sub>	70	-	-	V
Emitter collector breakdown voltage	I <sub>E</sub> = 10 μA		BV <sub>ECO</sub>	7	-	-	V
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$		I <sub>CEO</sub>	-	5	50	nA
Collector emitter capacitance	$V_{CE} = 0 V$		C <sub>CE</sub>	-	10	-	pF
Collector emitter saturation voltage	$I_{\rm F}$ = 10 mA, $I_{\rm C}$ = 2.5 mA		V <sub>CEsat</sub>	-	-	0.4	V
COUPLER							
Capacitance (input to output)			CIO	-	0.5	-	pF
Resistance (input to output)			R <sub>IO</sub>	-	100	-	GΩ

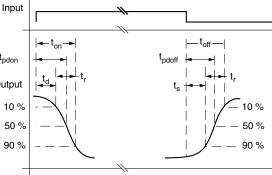
#### Note

• Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = 10 \text{ mA}$	ILD1206T	CTR <sub>DC</sub>	63	-	125	%
		ILD1207T	CTR <sub>DC</sub>	100	-	200	%

SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_C$ = 2 mA, $R_L$ = 100 $\Omega$ , $V_{CC}$ = 5 V	t <sub>on</sub>	5	-	-	μs
Turn-off time	$I_C=2~mA,~R_L=100~\Omega,~V_{CC}=5~V$	t <sub>off</sub>	4	-	-	μs











### **Vishay Semiconductors**

<b>SAFETY AND INSULATION RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		55 / 100 / 21		
Pollution degree	According to DIN VDE 0109		2		
Comparative tracking index	Insulation group Illa	CTI	175		
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	3333	V <sub>RMS</sub>	
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	6000	V <sub>peak</sub>	
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	560	V <sub>peak</sub>	
Isolation resistance	$T_{amb} = 25 \text{ °C}, V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
Isolation resistance	$T_{amb} = 100 \text{ °C}, V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω	
Output safety power		P <sub>SO</sub>	350	mW	
Input safety current		I <sub>SI</sub>	150	mA	
Input safety temperature		Τ <sub>S</sub>	165	°C	
Creepage distance			≥ 4	mm	
Clearance distance			≥ 4	mm	
Insulation thickness		DTI	≥ 0.2	mm	

Note

As per IEC 60747-5-5, §7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

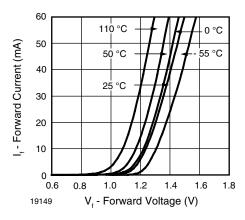


Fig. 3 - Forward Current vs. Forward Voltage

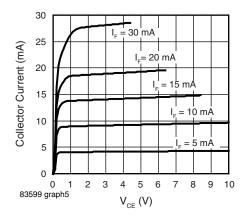


Fig. 4 - V<sub>CE</sub> vs. I<sub>C</sub>, (Non-Saturated)

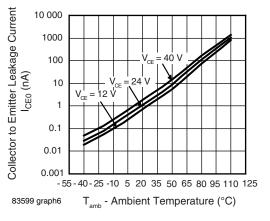


Fig. 5 - Collector to Emitter Leakage Current vs. Ambient Temperature

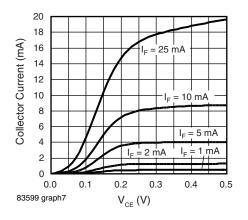


Fig. 6 - V<sub>CE</sub> vs. I<sub>C</sub>, (Saturated)

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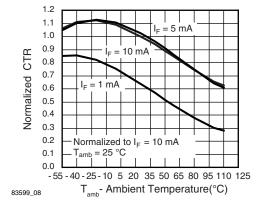


Fig. 7 - Normalized CTR vs. Ambient Temperature (Saturated,  $V_{CE} = 0.4 V$ )

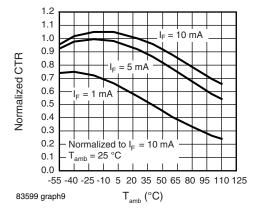
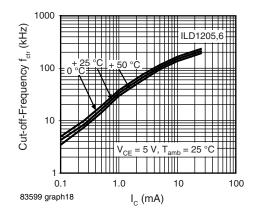


Fig. 8 - Normalized CTR vs. Ambient Temperature (Non-Saturated,  $V_{CE} = 5 \text{ V}$ )



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Fig. 9 - Cut-off-Frequency (- 3 dB) vs. Collector Current

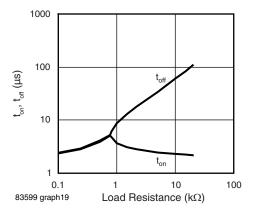


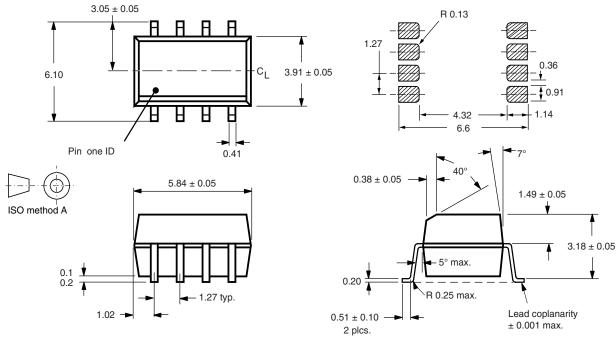
Fig. 10 -  $t_{on}$ ,  $t_{off}$  vs. Load Resistance (100  $\Omega$  to 20 000  $\Omega$ )



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### **PACKAGE DIMENSIONS** (in millimeters)



i178020

### PACKAGE MARKING (example)

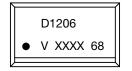


Fig. 11 - Example of ILD1206T

#### Notes

- XXXX = LMC (lot marking code)
- Tape and reel suffix (T) is not part of the package marking



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