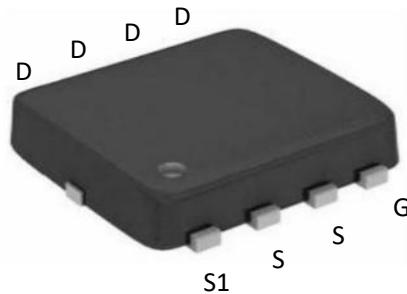


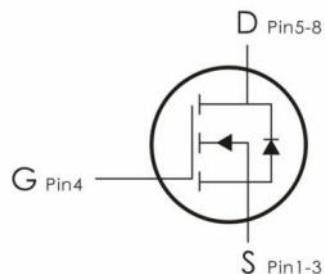
Description:

This N-Channel MOSFET uses advanced trench technology to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.



Features:

- 1) $V_{DS}=30V, I_D=30A, R_{DS(ON)}<13m\Omega @ V_{GS}=10V$
- 2) Improved dv/dt capability
- 3) Fast switching
- 4) 100% EAS Guaranteed
- 5) Green Device Available.



Absolute Maximum Ratings: ($T_J=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current-Continuous- $T_C=25^\circ C$	30	A
	Continuous Drain Current-Continuous- $T_C=100^\circ C$	22	
I_{DM}	Drain Current – Pulsed ^{note1}	110	A
EAS	Single Pulse Avalanche Energy ^{note2}	12	mJ
P_D	Power Dissipation - $T_C=25^\circ C$	24	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	5.2	°C/W

Package Marking and Ordering Information:

Part NO.	Marking	Package
DOZ30N03	30N03	DFN3*3-8

Electrical Characteristics: ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	30	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=30\text{V}, T_J=25^\circ\text{C}$	---	---	1.0	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1.0	1.5	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance ^{note3}	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	10	13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	---	16	22.5	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	550	---	pF
C_{oss}	Output Capacitance		---	105	---	
C_{rss}	Reverse Transfer Capacitance		---	95	---	
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=10\text{V}, R_G=3.0 \Omega, I_{\text{D}}=18\text{A}$	---	5	---	ns
t_r	Rise Time		---	8	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	21	---	ns
t_f	Fall Time		---	7	---	ns
Q_g	Total Gate Charge	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	15	---	nC
Q_{gs}	Gate-Source Charge		---	4.7	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	3.6	---	nC
Drain-Source Diode Characteristics						
I_s	Continuous Source Current1,5	VG=VD=0V , Force Current	---	---	18	A
I_{SM}	Pulsed Source Current2,5		---	---	72	A

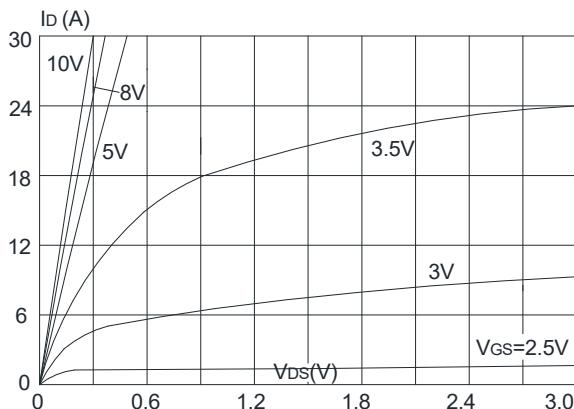
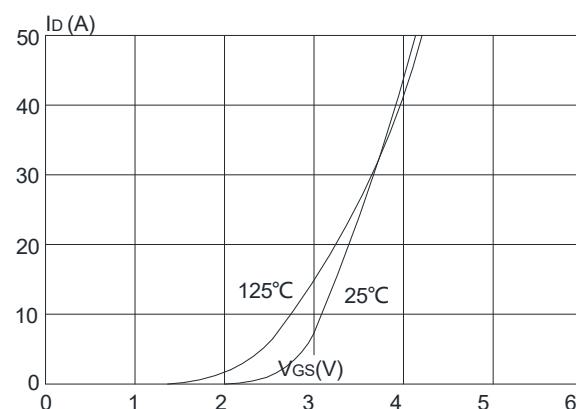
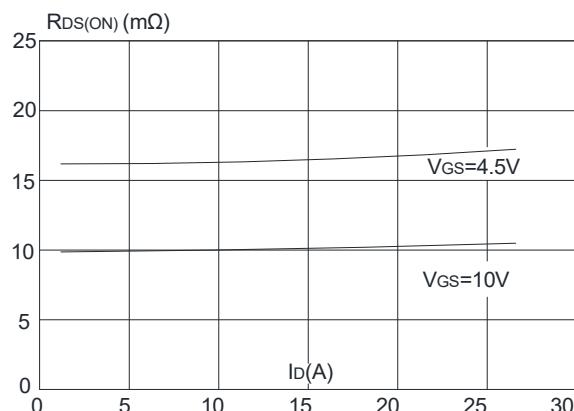
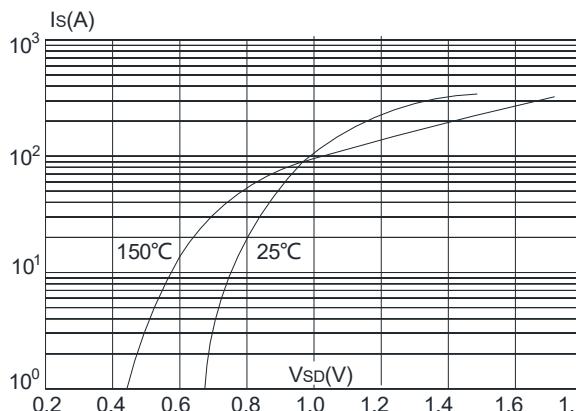
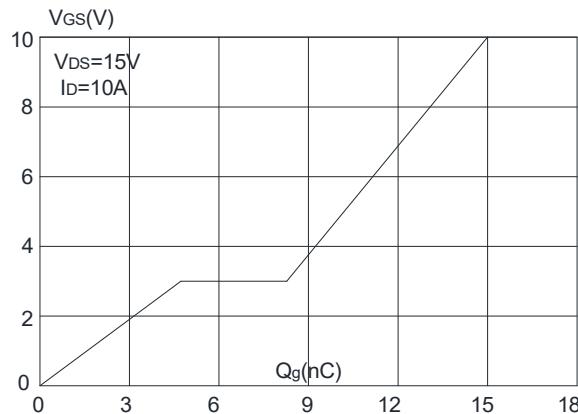
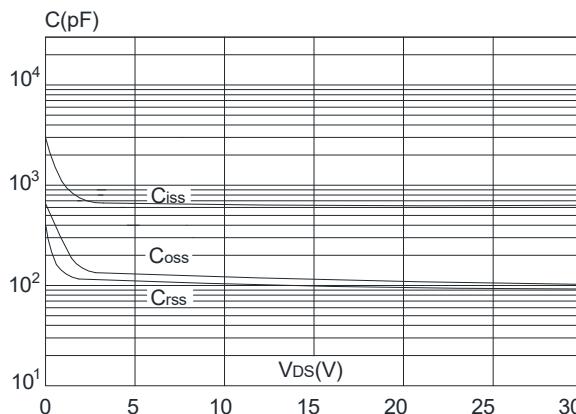


V_{SD}	Forward on voltage	$I_{SD}=18A, T_J=25^{\circ}C, V_{GS}=0V$	---	---	1.2	V
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Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: $T_J=25^{\circ}C$, $V_{GS}=10V$, $R_G=25\Omega$, $L=0.5mH$, $I_{AS}=8A$
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_c=25^{\circ}C$ unless otherwise noted)

**Figure 1:** Out put Characteristics**Figure 2:** Typical Transfer Characteristics**Figure 3:** On-resistance vs. Drain Current**Figure 4:** Body Diode Characteristics**Figure 5:** Gate Charge Characteristics**Figure 6:** Capacitance Characteristics

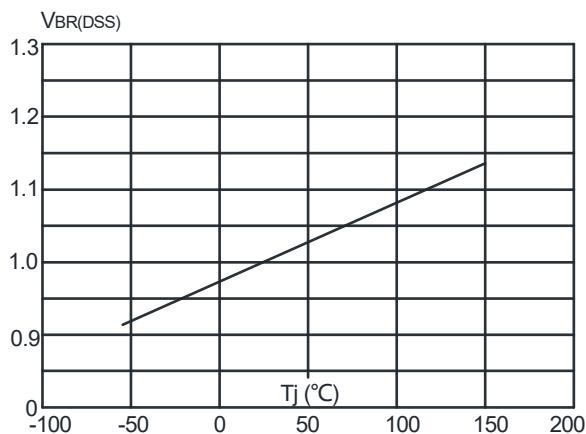


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

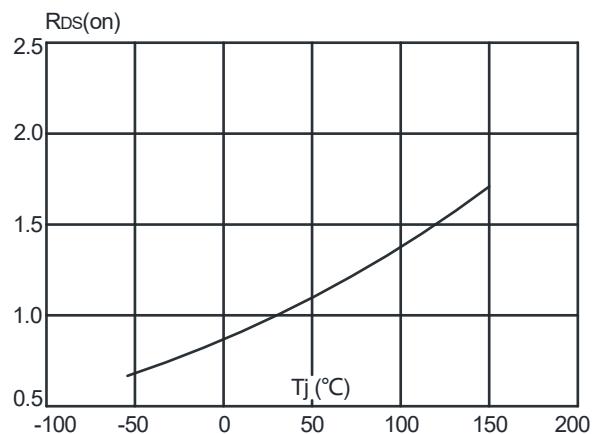


Figure 8: Normalized on Resistance vs. Junction Temperature

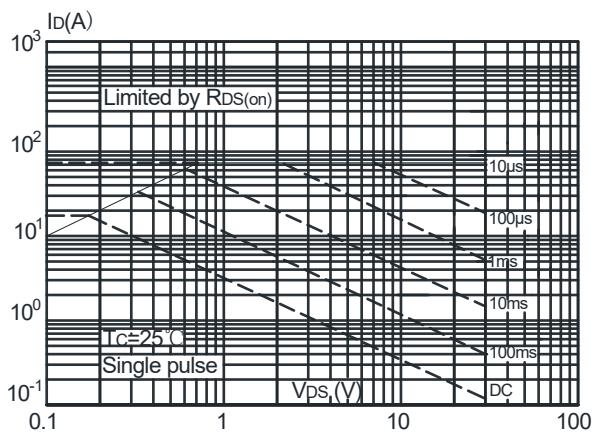


Figure 9: Maximum Safe Operating Area

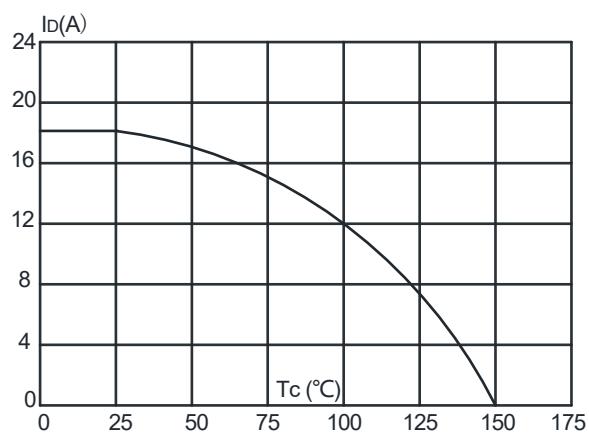


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

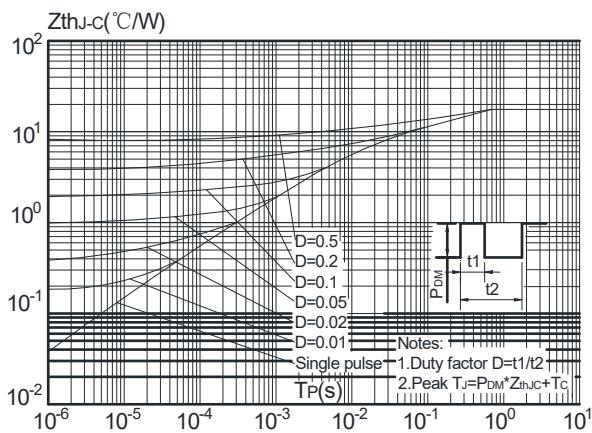


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case