

Description

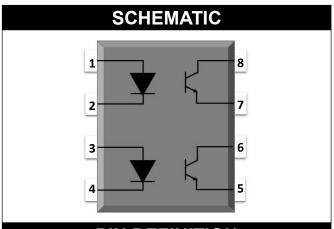
The TLP521-2 series provide two channel operation, and each combines an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon planar phototransistor detector in a plastic DIP8 package with different lead forming options. With the robust coplanar double mold structure, TLP521-2 series provide the most stable isolation feature.

Features

- High isolation 5000 VRMS
- DC input with transistor output
- Operating temperature range 55 °C to 110 °C
- REACH compliance
- Halogen free (Optional)
- MSL class 1
- Regulatory Approvals
 - UL UL1577
 - VDE EN60747-5-5(VDE0884-5)
 - CQC GB4943.1, GB8898
 - cUL- CSA Component Acceptance
 Service Notice No. 5A

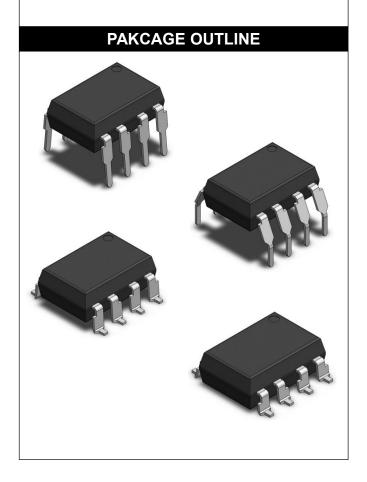
Applications

- Computer peripheral interface
- Microprocessor system interface



PIN DEFINITION

- 1. Anode
- 8. Collector
- 2. Cathode
- 7. Emitter
- 3. Anode
- 6. Collector
- 4. Cathode
- 5. Emitter





ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	VALUE	UNIT	NOTE		
INPUT						
Forward Current	I _F	60	mA			
Peak Forward Current	I _{FP}	1	Α	1		
Reverse Voltage	V _R	6	V			
Input Power Dissipation	Pı	100	mW			
OUTPUT						
Collector - Emitter Voltage	V _{CEO}	80	V			
Emitter - Collector Voltage	V _{ECO}	6	V			
Collector Current	Ic	50	mA			
Output Power Dissipation	Po	150	mW			
COMMON						
Total Power Dissipation	Ptot	200	mW			
Isolation Voltage	Viso	5000	Vrms	2		
Operating Temperature	Topr	-55~110	°C			
Storage Temperature	Tstg	-55~125	°C			
Soldering Temperature	Tsol	260	°C			

Note 1. 100μs pulse, 100Hz frequency

Note 2. AC For 1 Minute, R.H. = $40 \sim 60\%$



	ELECT	RICAL OF	PTICA	L CHA	ARAC	TER	ISTICS at Ta=25°C		
PARAME	ETER	SYMBOL	MIN	TYP.	MAX.	UNIT	TEST CONDITION	NOTE	
	INPUT								
Forward V	oltage/	V _F	-	1.24	1.4	V	IF=10mA		
Reverse Current		I _R	-	-	10	μA	VR=6V		
Input Capa	Input Capacitance		-	10	-	pF	V=0, f=1kHz		
				OUT	PUT				
Collector Dar	k Current	I _{CEO}	-	-	100	nA	VCE=20V, IF=0		
Collector-I	Emitter	BV _{CEO}	80			V	IC-0.4mA IF-0		
Breakdown	Voltage	D A CEO	80	_	_	V	IC=0.1mA, IF=0		
Emitter-Co	ollector	BV _{ECO}	6			V	IE=0.1mA, IF=0		
Breakdown	Voltage	D A ECO		_	_	V	IE-0. IIIA, IF-0		
		TR	RANSFE	ER CHA	RACT	TERIS	TICS		
Current	521-2	CTR	50		600	%	IF=5mA, VCE=5V		
Transfer				_					
Ratio	521-2GB	CTR	100	-	600	%			
Collector-Emitter		V _{CE(sat)}	_	0.06	0.2	V	IF=20mA, IC=1mA		
Saturation	Voltage	V CE(Sat)		0.00	0.2	•	11 –2011/4, 10–111/4		
Isolation Re	sistance	R _{ISO}	10^12	10^14	-	Ω	DC500V, 40 ~ 60% R.H.		
Floating Capacitance		C _{IO}	-	0.4	1	pF	V=0, f=1MHz		
Response Time (Rise)		tr	-	6	18	μs	VCE=2V, IC=2mA	3	
Response Time (Fall)		tf	-	8	18	μs	RL=100Ω	3	
Cut off Fraguerov		fc	8	80	_	kHz	VCE=2V, IC=2mA	4	
Cut-off Frequency		10	_	00	_	KI IZ	RL=100Ω,-3dB	+	

Note 3. Fig.14

Note 4. Fig.12&13



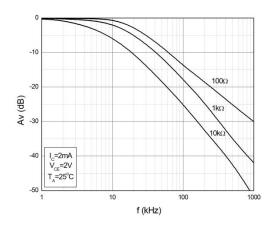
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DIP8, DC Input, Phototransistor Photo Coupler

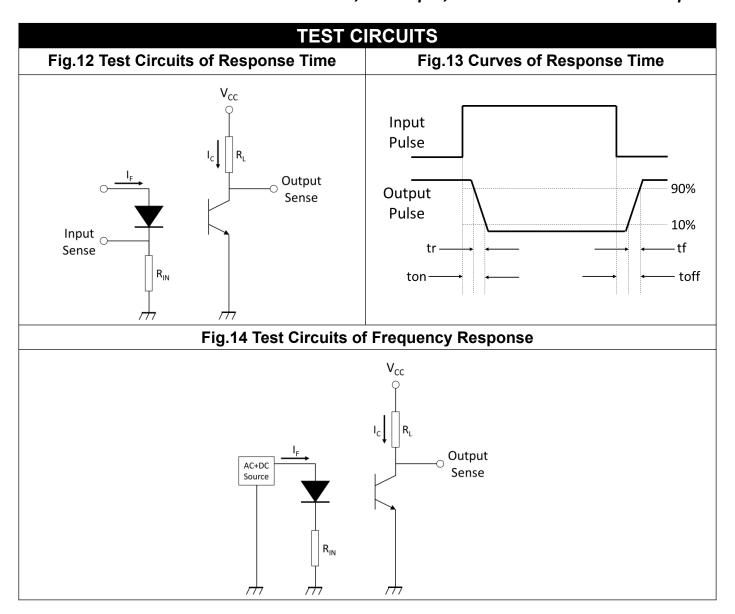
CHARACTERISTIC CURVES Fig.1 Forward Current **Fig.2 Collector Power Dissipation** vs. Ambient Temperature vs. Ambient Temperature 140 120 100 (mW) 60 20 40 20 -40 -20 40 60 80 120 -40 -20 40 60 80 100 120 TA (°C) TA(°C) Fig.3 Forward Current **Fig.4 Collector Dark Current** vs. Forward Voltage vs. Ambient Temperature 100 10000 1000 V_{CE}=20\ I_F (mA) I_{CEO} (nA) V_{CE}=10V -55°C 0.9 1.0 1.1 1.2 1.3 1.4 1.5 1.6 100 $V_{F}(V)$ T_A (°C) **Fig.5 Collector Current Fig.6 Collector Current** vs. Collector-emitter Voltage vs. Collector-emitter Voltage T_A=25°C T_A=25°C I_F=10mA _=50mA 50) I_F=30mA) I_F=20mA I_c=5mA PC=150mW I_c (mA) I_=2mA I_F=1mA I_F=0.5mA I_=5mA 0.0 V_{CE} (V) V_{CE} (V) Rev: V02 Release Date: 2021/06/22



CHARACTERISTIC CURVES Fig.7 Normalized Current Transfer Ratio **Fig.8 Normalized Current Transfer Ratio** vs. Forward Current vs. Ambient Temperature 1.2 V_{CE}=5V 1.0 Normalized CTR Normalized CTR 0.8 =0.4 0.6 0.4 I_F=5mA Normalized to I_z=5mA 0.2 Normalized to T_A=25°C T_A=25°C 0.0 T_A (°C) $I_F(mA)$ Fig.9 Collector-emitter Saturation Voltage Fig.10 Switching Time vs. Ambient Temperature vs. Load Resistance 0.14 I_c =2mA V_{ce} =2V T_a =25°C 0.12 Response Time (µs) € 0.08 O.06 0.04 0.02 0.1 -40 20 40 60 80 100 Load Resistance ($k\Omega$) T_A (°C) Fig.11 Frequency Response









PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated) Standard DIP - Through Hole (DIP Type) 6.60±0.20 9.76±0.20 7.62±0.30 1.30±0.10 3.50±0.20 4.50±0.30 Typ.2.80 Typ.0.25 5°~15° Typ.0.50 Typ.2.54 7.62~9.50 Gullwing (400mil) Lead Forming – Through Hole (M Type) 6.60±0.20 9.76±0.20 7.62±0.30 1.30±0.10 3.50±0.20 4.58±0.30 Typ.2.20 Typ.0.25 10.16±0.30 Typ.0.50 Typ.2.54



PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated) **Surface Mount Lead Forming (S Type)** 6.60±0.20 9.76±0.20 7.62±0.30 1.30±0.10 3.50±0.20 | Typ.0.25 4.30±0.30 Typ.0.80 Typ.0.80 10.15±0.30 Typ.0.50 Typ.2.54 Surface Mount (Low Profile) Lead Forming (SL Type) 6.60±0.20 9.76±0.20 7.62±0.30 1.30±0.10 3.50±0.20 Typ.0.25 3.60±0.30 Тур.0.10 Typ.0.80 10.15±0.30 Typ.0.50 Typ.2.54

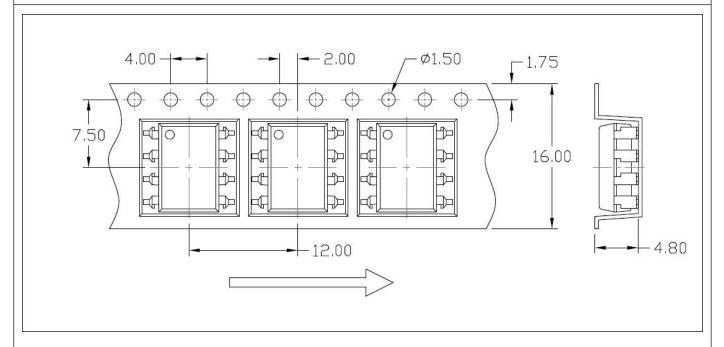


RECOMMENDED SOLDER MASK (Dimensions in mm unless otherwise stated) Surface Mount Lead Forming & Surface Mount (Low Profile) Lead Forming 1.60 2.54 862

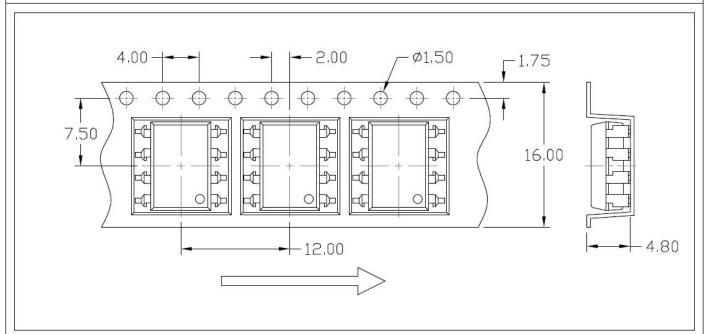


Carrier Tape Specifications (Dimensions in mm unless otherwise stated)

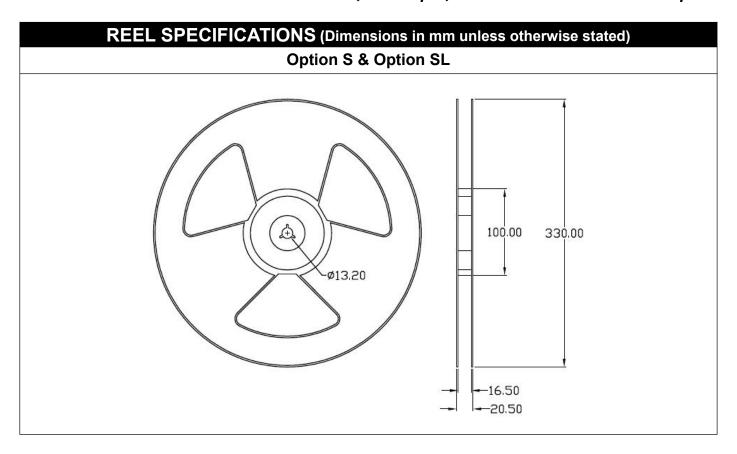
Option S(T1) & SL(T1)

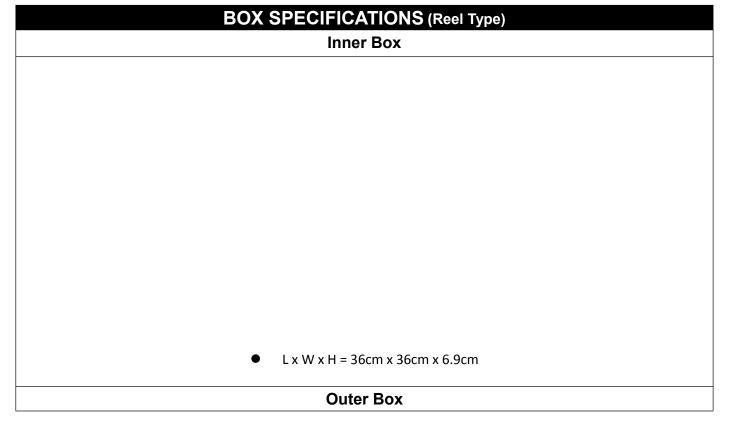


Option S(T2) & SL(T2)





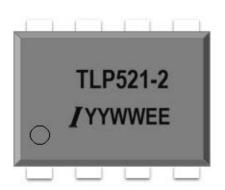






ORDERING AND MARKING INFORMATION

MARKING INFORMATION



TLP: Series Abbr. 521-2: Part Number Y: Fiscal Year

WW : Work Week

EE : Manufacturing Code

ORDERING INFORMATION

TLP521-2(X)L(Z)

TLP - Series Abbr.

521-2 - Part Number

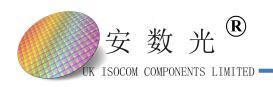
XXX - Rank (None/XGR/XBL/XGB)

L – Lead Form Option (SM/S/SL/None)

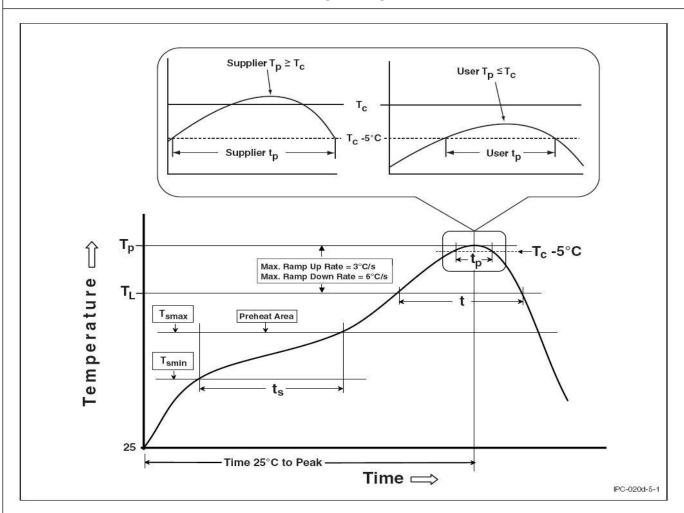
Z – Tape and Reel Option (T/R1 or T/R2)

LABEL INFORMATION

PACKING QUANTITY				
Option	Quantity	Quantity – Inner box	Quantity – Outer box	
None	50 Units/Tube	16 Tubes/Inner box	10 Inner box/Outer box = 8k Units	
SM	50 Units/Tube	16 Tubes/Inner box	10 Inner box/Outer box = 8k Units	
SMT/R1	1000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 10k Units	
SMT/R2	1000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 10k Units	
SLT/R1	1000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 10k Units	
SLT/R2	1000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 10k Units	

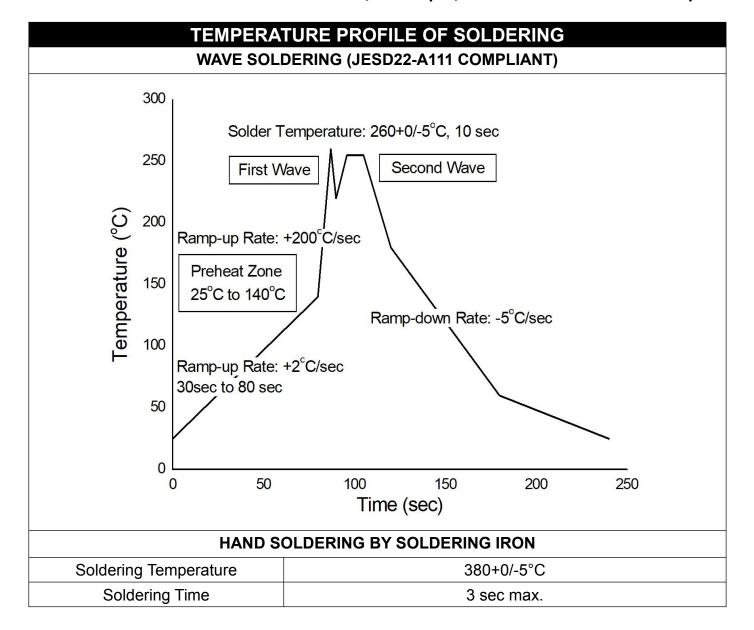


REFLOW INFORMATION REFLOW PROFILE



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	100	150°C
Temperature Max. (Tsmax)	150	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds	60-120 seconds
Ramp-up Rate (tL to tP)	3°C/second max.	3°C/second max.
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.





- One time soldering is recommended for all soldering method.
- Do not solder more than three times for IR reflow soldering.



DISCLAIMER

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- Please contact ASG sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
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