

Features <ul style="list-style-type: none"> ➤ Super Low Gate Charge ➤ Green Device Available ➤ Excellent CdV/dt effect decline ➤ Advanced high cell density Trench technology 	Bvdss	Rdson	ID
	-60V	70mΩ	-8A
Application <ul style="list-style-type: none"> ➤ DC-DC Converters ➤ Power management function ➤ Synchronous-rectification applications 			
Package <div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;"> <p>1. Marking and pin assignment</p> </div> <div style="text-align: center;"> <p>2. SOP8 top view</p> </div> <div style="text-align: center;"> <p>3. Schematic diagram</p> </div> </div>			

Package Marking and Ordering Information

Device Marking	Device	Device Package	Quantity
9958	9958	SOP8	3000

Absolute Maximum Ratings ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, $V_{GS} @ -10\text{V}$ (1)	$T_C = 25^{\circ}\text{C}$	I_D	-8
	$T_C = 70^{\circ}\text{C}$	I_D	-6.2
Pulsed Drain Current (2)	I_{DM}	-16.2	A
Single Pulsed Avalanche Energy (3)	E_{AS}	69.7	mJ
Avalanche Current	I_{AS}	44.4	W
Total Power Dissipation(4)	$T_C = 25^{\circ}\text{C}$	P_D	6.1
Junction Temperature	T_J	-55~+150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^{\circ}\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Junction to case	$R_{\theta JC}$	36	$^{\circ}\text{C}/\text{W}$
Thermal Resistance Junction-Ambient (1)	$R_{\theta JA}$	85	$^{\circ}\text{C}/\text{W}$



Ordering Information

Ordering Number	Package	Pin Assignment						Packing
		G1	G2	S1	S2	D1	D2	
HL9958	SOP8	2	4	1	3	7,8	5,6	Tape Reel

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain to Source Breakdown Voltage	$V_{(br)dss}$	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	-60	-	-	V
BVDSS Temperature Coefficient	$\Delta B_{V_{DSS}}/\Delta T_J$	Reference to 25°C , $I_D = -1\text{mA}$	-	-0.03	-	V/C
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = -48\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 25^{\circ}\text{C}$	-	-	1	μA
		$V_{DS} = -48\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 55^{\circ}\text{C}$	-	-	5	
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}$, $V_{DS} = 0\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS} = V_{DS}$, $I_D = -250\mu\text{A}$	-1.2	-	-2.5	V
VGS(th) Temperature Coefficient	$\Delta V_{GS(th)}$		-	4.56	-	mV/C
Static Drain-Source On-Resistance(2)	$R_{DS(on)}$	$V_{GS} = -10\text{V}$, $I_D = -3\text{A}$	-	70	90	m Ω
		$V_{GS} = -4.5\text{V}$, $I_D = -2\text{A}$	-	90	115	
Forward Transconductance	g_{fs}	$V_{DS} = -5\text{V}$, $I_D = -3\text{A}$	-	8.7	-	S
Gate Resistance	R_g	$V_{DS} = 0\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	-	15	-	Ω
Input Capacitance	C_{iss}	$V_{DS} = -15\text{V}$, $V_{GS} = 0\text{V}$, $f = 1\text{MHz}$	-	1080	-	pF
Output Capacitance	C_{oss}		-	73	-	pF
Reverse Transfer Capacitance	C_{rss}		-	50	-	pF
Turn-ON Delay Time	$t_{d(on)}$	$V_{DS} = -15\text{V}$, $V_{GS} = -10\text{V}$, $RG = 3.3$, $I_D = -1\text{A}$	-	8.8	-	ns
Rise Time	t_r		-	19.6	-	ns
Turn-OFF Delay Time	$t_{d(off)}$		-	47.2	-	ns
Fall Time	t_f		-	9.6	-	ns
Total Gate Charge	Q_g	$V_{DS} = -48\text{V}$, $V_{GS} = -4.5\text{V}$, $I_D = -3\text{A}$	-	11.8	-	nC
	Q_{gs}		-	1.9	-	nC
	Q_{gd}		-	6.5	-	nC
Maximum Pulsed Drain to Source Diode Forward Current(2,5)	I_{SM}	$V_G = V_D = 0\text{V}$, Force Current	-	-	-16.2	A
Drain to Source Diode Forward Voltage(2)	V_{SD}	$V_{GS} = 0\text{V}$, $I_S = -1\text{A}$, $T_J = 25^{\circ}\text{C}$	-	-	-1.2	V
Continuous Diode Forward Current(1,5)	I_S	$V_G = V_D = 0\text{V}$, Force Current	-	-	-8	A

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper. 2.The data tested by pulsed , pulse width \cong 300us , duty cycle \cong 2%
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-24.4A$
- 4.The power dissipation is limited by 150C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

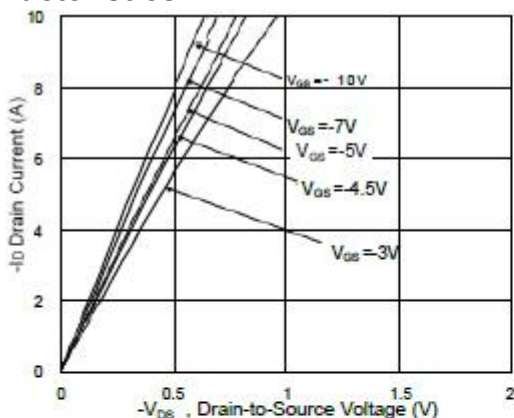


Fig.1 Typical Output Characteristics

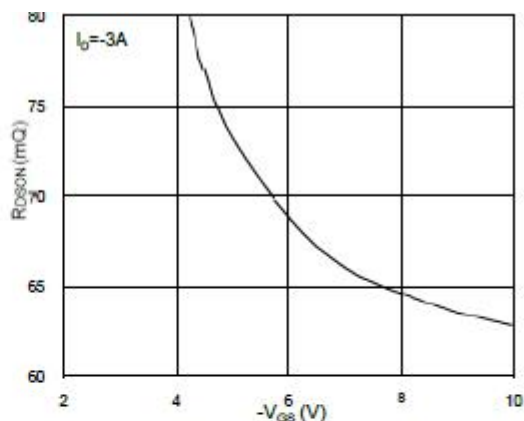


Fig.2 On-Resistance v.s Gate-Source

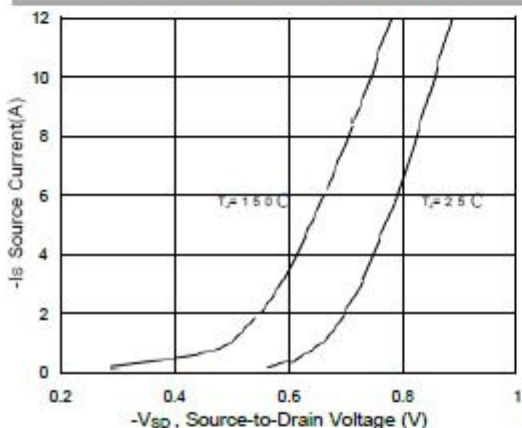


Fig.3 Forward Characteristics of Reverse

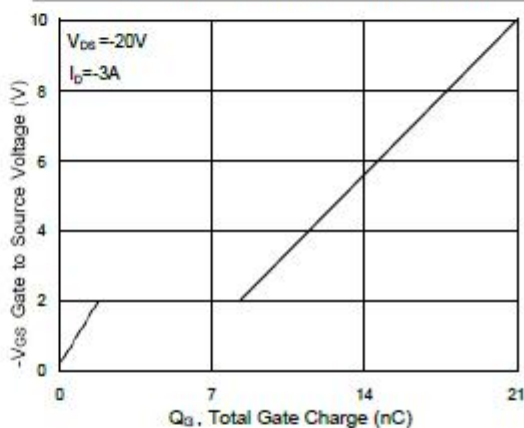


Fig.4 Gate-Charge Characteristics

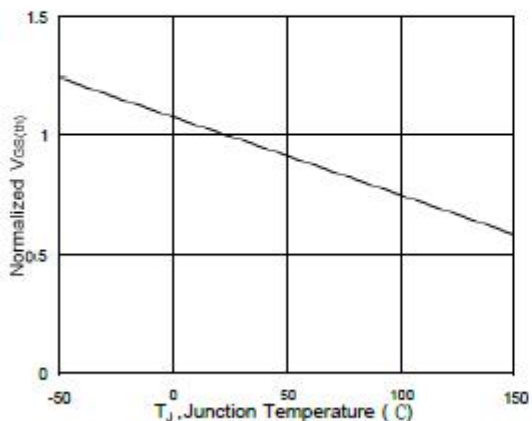


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

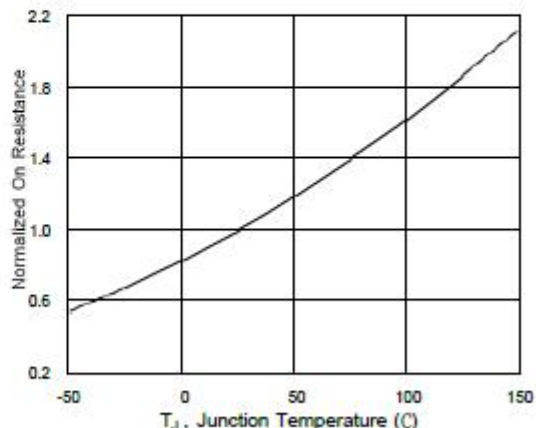


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

Typical Characteristics

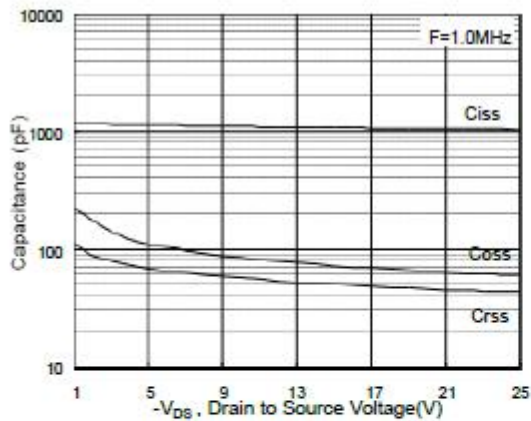


Fig. 7 Capacitance

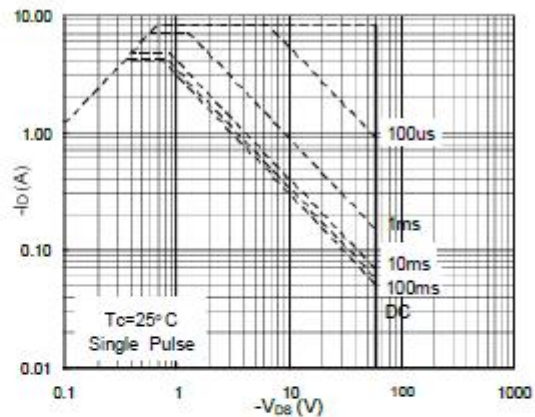


Fig. 8 Safe Operating Area

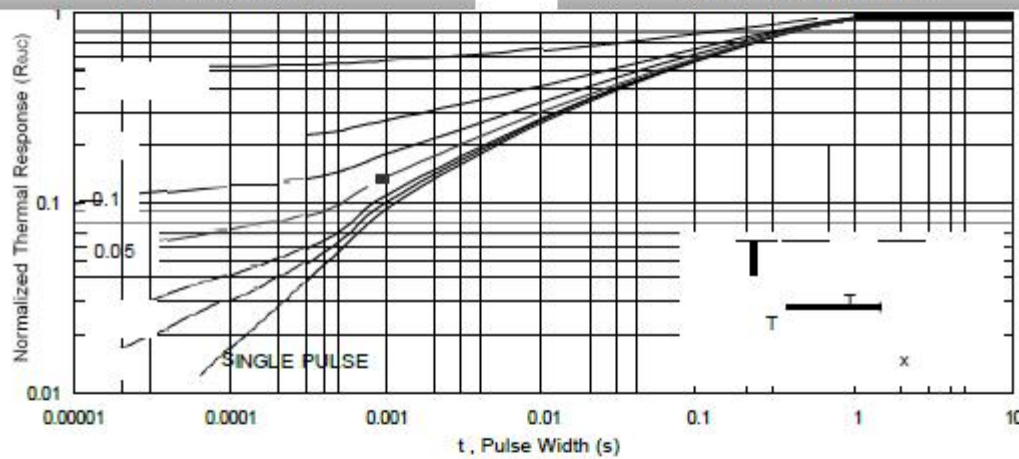


Fig. 9 Normalized Maximum Transient Thermal Impedance

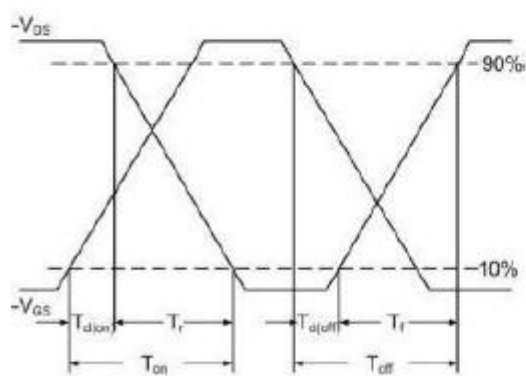


Fig. 10 Switching Time Waveform

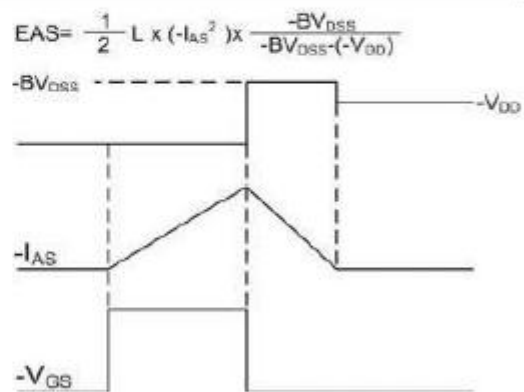
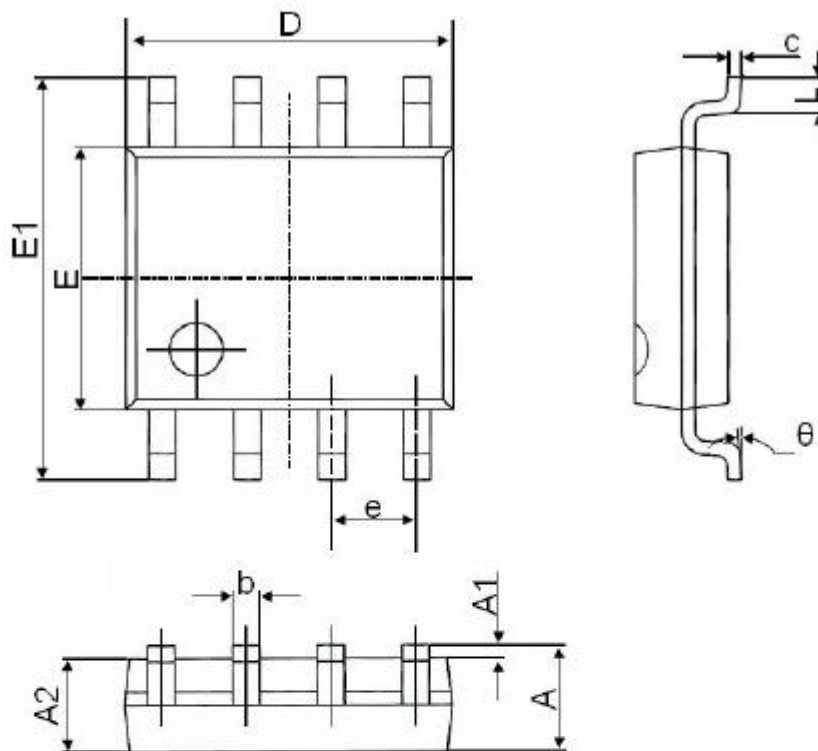


Fig. 11 Unclamped Inductive Waveform

Package Dimensions SOP8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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