

High Efficiency 1MHz, 2.5A Continuous, 3A Peak, Synchronous Step Down Regulator

General Description

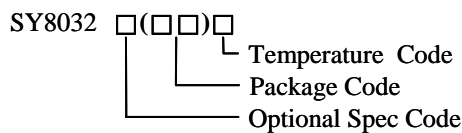
The SY8032 is a high-efficiency, high frequency synchronous step-down DC/DC regulator capable of delivering up to 2.5A output current. The SY8032 operates over a wide input voltage range from 2.7V to 5.5V and integrate main switch and synchronous switch with very low $R_{DS(ON)}$ to minimize the conduction loss.

Low output voltage ripple and small external inductor and capacitor sizes are achieved with greater than 1MHz switching frequency.

Features

- Low $R_{DS(ON)}$ for Internal Switches (Top/Bottom): 100m Ω /80m Ω , 2.5A
- 2.7-5.5V Input Voltage Range
- 2.5A Continuous, 3A Peak Load Current Capability
- High Switching Frequency Minimizes the External Components: 1MHz
- Internal Softstart Limits the Inrush Current
- 100% Dropout Operation
- RoHS Compliant and Halogen Free
- Compact Package: SOT23-6. Other Packages are Available Upon Requests

Ordering Information



Ordering Number	Package type	Note ^①
SY8032ABC	SOT23-6	----

Applications

- LCD TV
- Set Top Box
- Net PC
- Mini-Notebook PC
- Access Point Router

Typical Applications

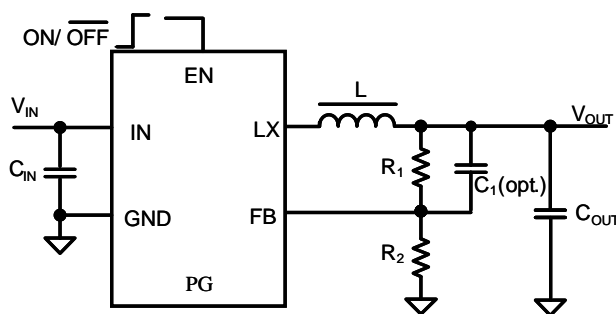


Figure 1. Schematic diagram

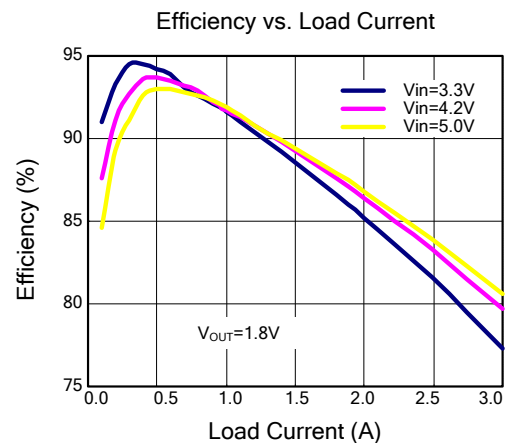
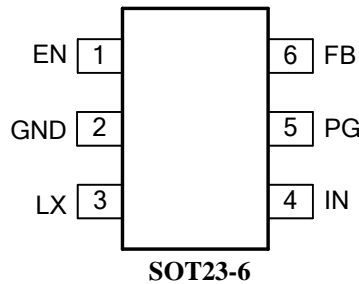


Figure 2. Efficiency vs. Load Current

Pinout (top view)



Top Mark: FKxyz (device code: FK, x=year code, y=week code, z=lot number code)

Pin Name	Pin Number	Pin Description
EN	1	Enable control. Pull high to turn on. Do not float.
GND	2	Ground pin
LX	3	Inductor pin. Connect this pin to the switching node of inductor.
IN	4	Input pin. Decouple this pin to GND pin with at least 10uF ceramic cap.
PG	5	Power good indicator (open drain output). Low if the output < 90% of regulation voltage; High otherwise. Connect a pull-up resistor to the input.
FB	6	Output feedback pin. Connect this pin to the center point of the output resistor divider (as shown in Figure 1) to program the output voltage: $V_{OUT}=0.6 \times (1+R_1/R_2)$.

Absolute Maximum Ratings (Note 1)

IN	-----	6.0V
LX	-----	-0.3V ^(*1) to 6V ^(*2)
All Other Pins	-----	V _{IN} + 0.6V
Power Dissipation, P _D @ T _A = 25 °C, SOT23-6	-----	0.6W
Package Thermal Resistance (Note 2)		
θ _{JA}	-----	140 °C/W
θ _{JC}	-----	70 °C/W
Junction Temperature Range	-----	-150 °C
Lead Temperature (Soldering, 10 sec.)	-----	260 °C
Storage Temperature Range	-----	-65 °C to 150 °C
(*1) LX Voltage Tested Down to -3V<10ns		
(*2) LX Voltage Tested Up to +7V<50ns		

Recommended Operating Conditions (Note 3)

IN	-----	2.7V to 5.5V
Junction Temperature Range	-----	-40 °C to 125 °C
Ambient Temperature Range	-----	-40 °C to 85 °C

Electrical Characteristics

($V_{IN} = 5V$, $V_{OUT} = 2.5V$, $L = 2.2\mu H$, $C_{OUT} = 10\mu F$, $T_A = 25^\circ C$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage Range	V_{IN}		2.7		5.5	V
Quiescent Current	I_Q	$I_{OUT}=0$, $V_{FB}=V_{REF} \cdot 105\%$		80		μA
Shutdown Current	I_{SHDN}	EN=0		0.1	1	μA
Feedback Reference Voltage	V_{REF}		0.58 8	0.6	0.612	V
FB Input Current	I_{FB}	$V_{FB}=V_{IN}$	-50		50	nA
PFET RON	$R_{DS(ON),P}$			100		$m\Omega$
NFET RON	$R_{DS(ON),N}$			80		$m\Omega$
PFET Current Limit	I_{LIM}		3.5			A
EN rising threshold	V_{ENH}		1.5			V
EN falling threshold	V_{ENL}				0.4	V
Input UVLO threshold	V_{UVLO}				2.7	V
UVLO hysteresis	V_{HYS}			0.1		V
Turn On Delay	$t_{ON,DLY}$	from EN high to LX start switching		100		μs
Soft-Start Time	t_{SS}			0.8		ms
Oscillator Frequency	F_{OSC}	$I_{OUT}=500mA$	0.8	1	1.2	MHz
Min ON Time				50		ns
Max Duty Cycle			100			%
Thermal Shutdown Temperature	T_{SD}			160		$^\circ C$

Note 1: Stresses beyond “Absolute Maximum Ratings” may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2: θ_{JA} is measured in the natural convection at $T_A = 25^\circ C$ on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Pin 2 of SOT23-6 packages is the case position for θ_{JC} measurement.

Note 3: The device is not guaranteed to function outside its operating conditions.

Function Block

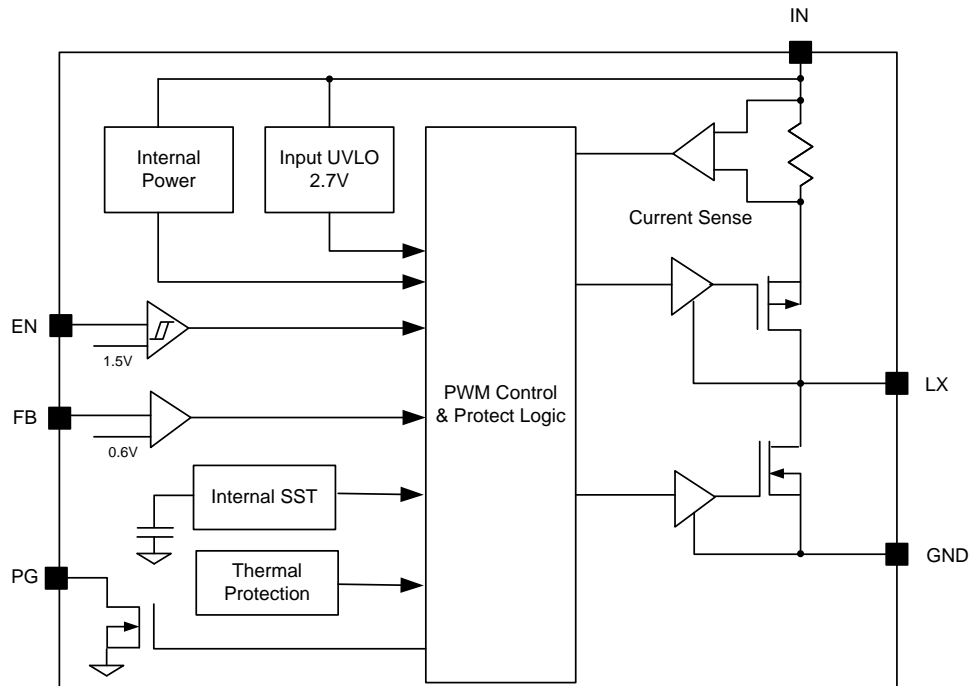
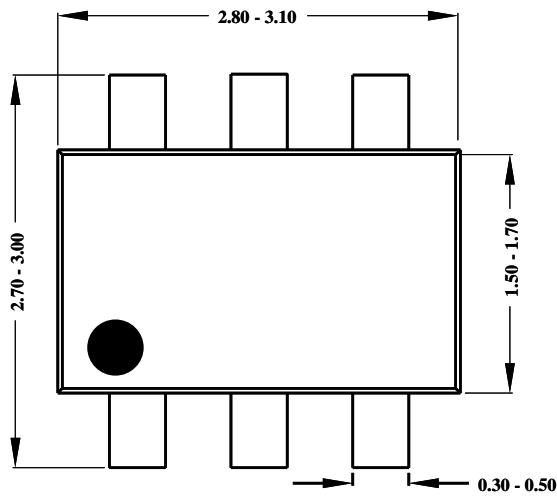
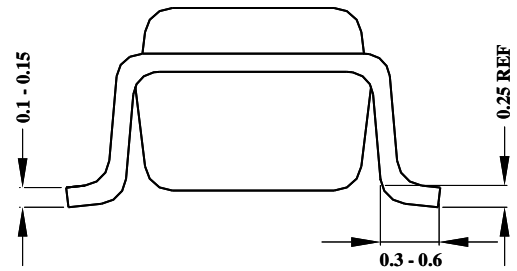


Figure3. Functional Block

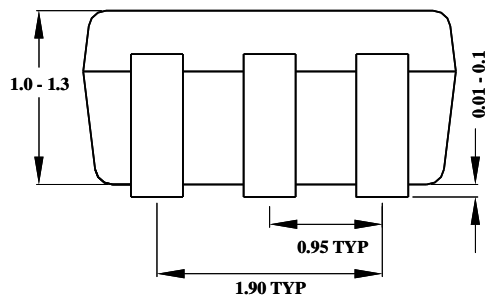
SOT23-6 Package Outline & PCB layout



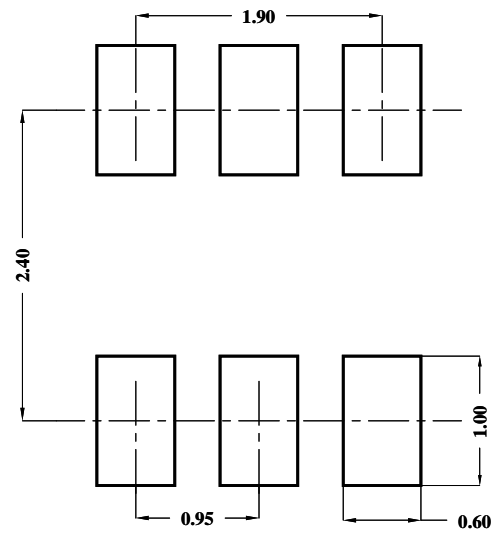
Top View



Side View A



Side View B



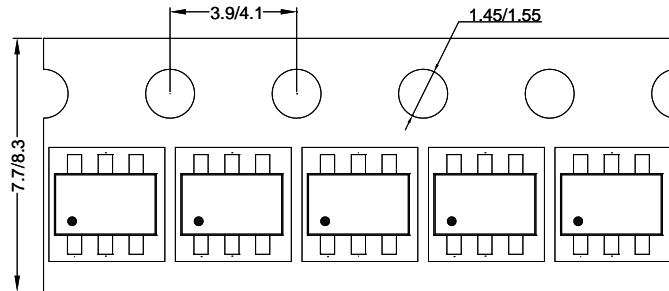
Recommended Pad Layout

Notes: **All dimension in millimeter.**
 All dimension don't not include mold flash & metal burr.

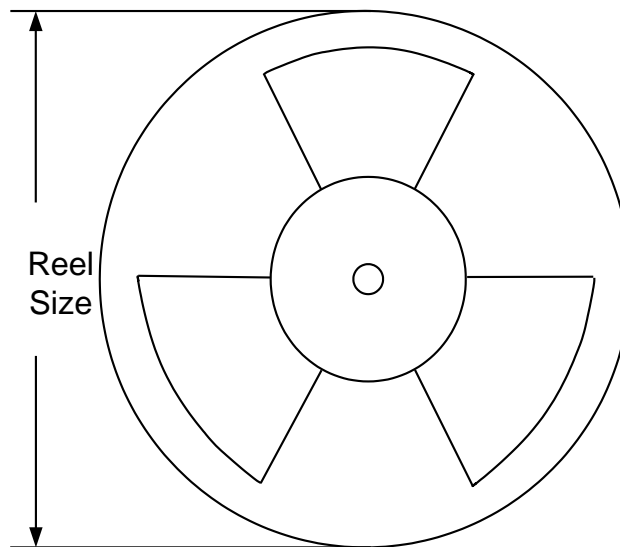
Taping & Reel Specification

1. Taping orientation

SOT23-6



2. Carrier Tape & Reel specification for packages



Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
SOT23-6	8	4	7"	280	160	3000

3. Others: NA