

**SuperMOS – SOT-23 -40V  $BV_{DSS}$ , 68m $\Omega$   $R_{DS(on)}$ , P-channel MOSFET**

**1. Description**

The WST4041-ES is P-Channel enhancement MOS Field Effect Transistor. Uses advanced technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product WST4041-ES is Pb-free.

**2. Features**

- -40V,  $R_{DS(ON)}$ =68m $\Omega$ (TYP.) @ $V_{GS}$ =-10V
- $R_{DS(ON)}$ =84m $\Omega$ (TYP.) @ $V_{GS}$ =-4.5V
- Fast Switching
- High density cell design for low  $R_{DS(on)}$
- Material: Halogen free
- Reliable and rugged
- Avalanche Rated
- Low leakage current

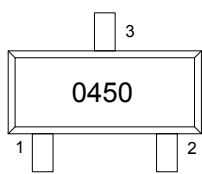
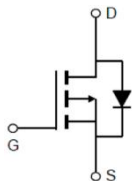
**3. Applications**

- PWM applications
- Load switch
- Power management in portable/desktop PCs
- DC/DC conversion

**4. Ordering Information**

Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
WST4041-ES	SOT-23	0450	Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

**5. Pin Configuration and Functions**

Pin	Function	Outline	Circuit Diagram
1	Gate		
2	Source		
3	Drain		

## 6. Specification

### Absolute Maximum Rating & Thermal Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$BV_{DSS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	-5	A
	$T_A=25^\circ\text{C}$		
Maximum Power Dissipation	$P_D$	1.4	W
Pulsed Drain Current	$I_{DM}$	-20	A
Operating Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{stg}$	-55 to +150	°C

### Thermal resistance ratings

Single Operation				
Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ( $t \leq 10\text{s}$ )	$R_{\theta JA}$	75	90	°C/W

## Electrical Characteristics

At TA = 25°C unless otherwise specified

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-40			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-40V, V_{GS}=0V$			-1	$\mu A$
Gate-to-source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.5	-2.5	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-5 A$		68	85	m $\Omega$
		$V_{GS}=-4.5V, I_D=-4A$		84	125	
<b>CHARGES, CAPACITANCES AND GATE RESISTANCE</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=-20V$ $f=1MHz$		550		pF
Output Capacitance	$C_{OSS}$			50		
Reverse Transfer Capacitance	$C_{RSS}$			40		
Total Gate Charge	$Q_G$	$V_{GS}=-10V, V_{DS}=-20V$ $I_D=-5 A$		12		nC
Gate-to-Source Charge	$Q_{GS}$			2.2		
Gate-to-Drain Charge	$Q_{GD}$			3		
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{d(ON)}$	$V_{GS}=-10V, V_{DS}=-20V$ $I_D=-5A, R_G=3\Omega$		7		ns
Rise Time	$t_r$			6.5		
Turn-Off Delay Time	$t_{d(OFF)}$			25		
Fall Time	$t_f$			8		
<b>BODY DIODE CHARACTERISTICS</b>						
Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=-5A$			-1.5	V

7. Typical Characteristic

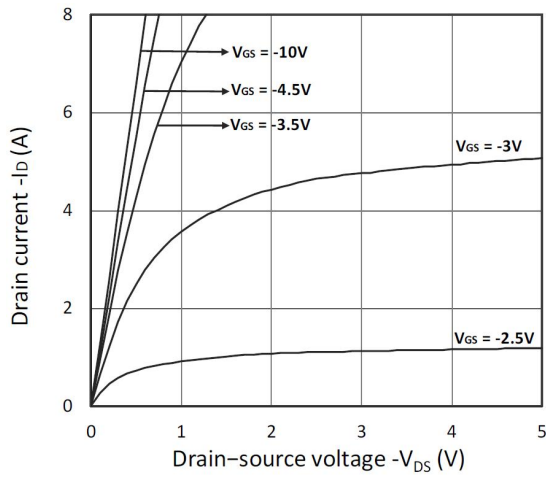


Figure 1. Output Characteristics

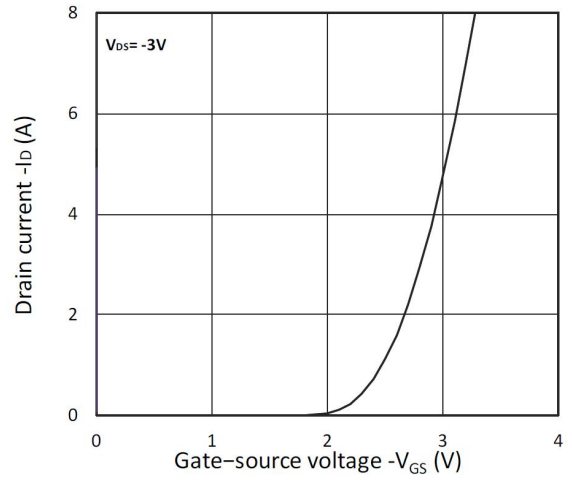


Figure 2. Transfer Characteristics

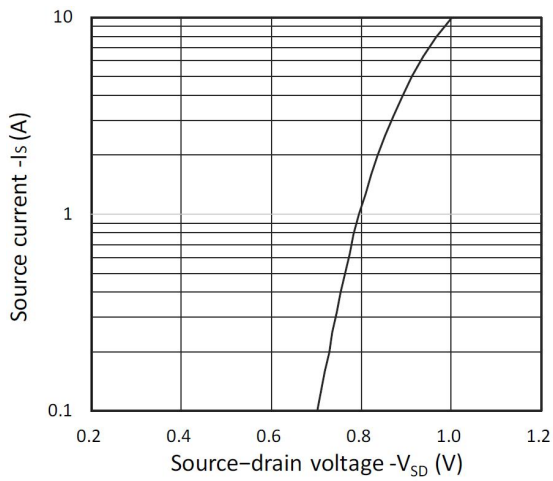


Figure 3. Forward Characteristics of Reverse

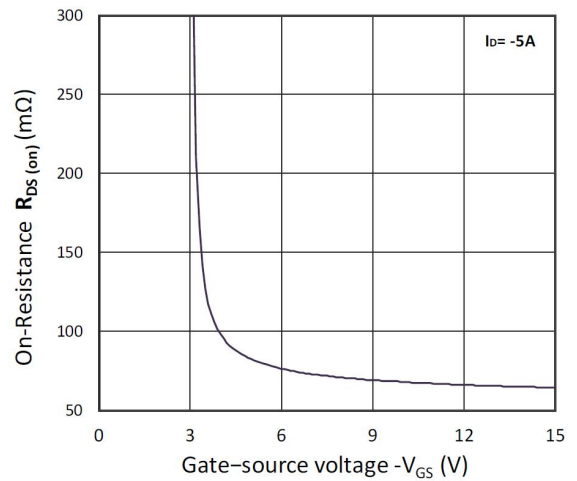


Figure 4.  $R_{DS(ON)}$  vs.  $V_{GS}$

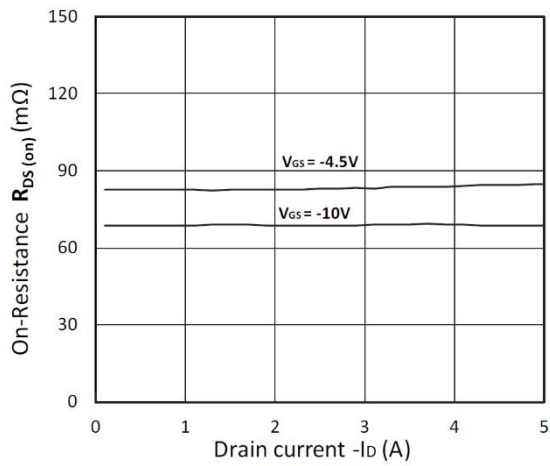


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

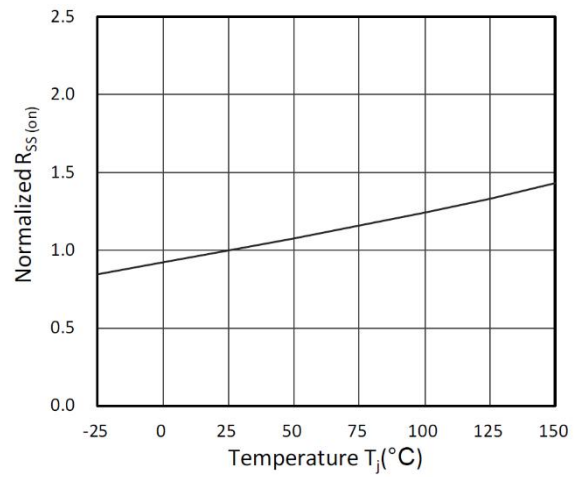


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature

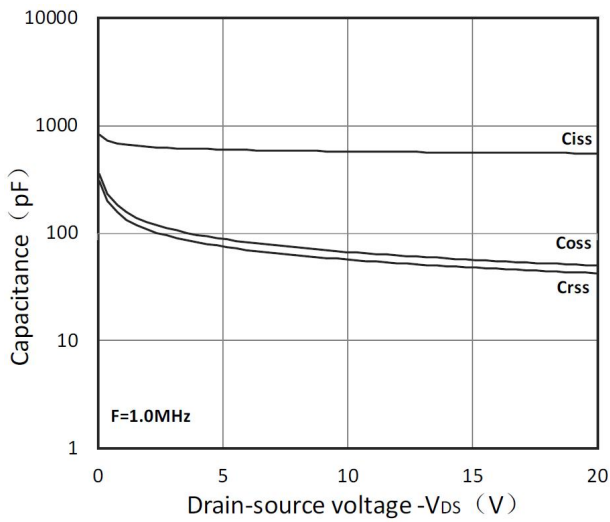


Figure 7. Capacitance Characteristics

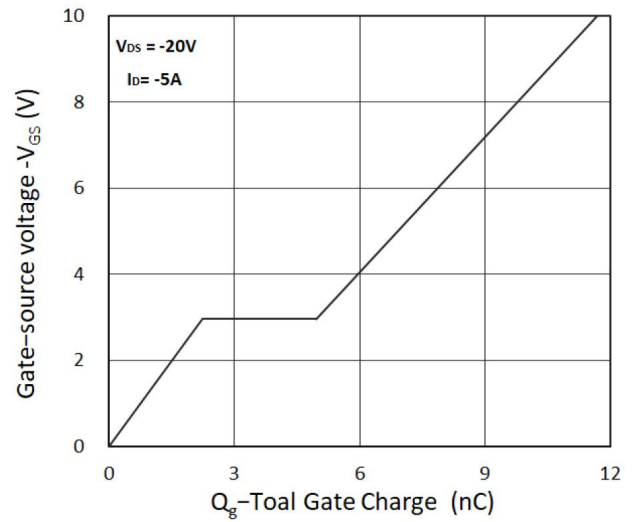
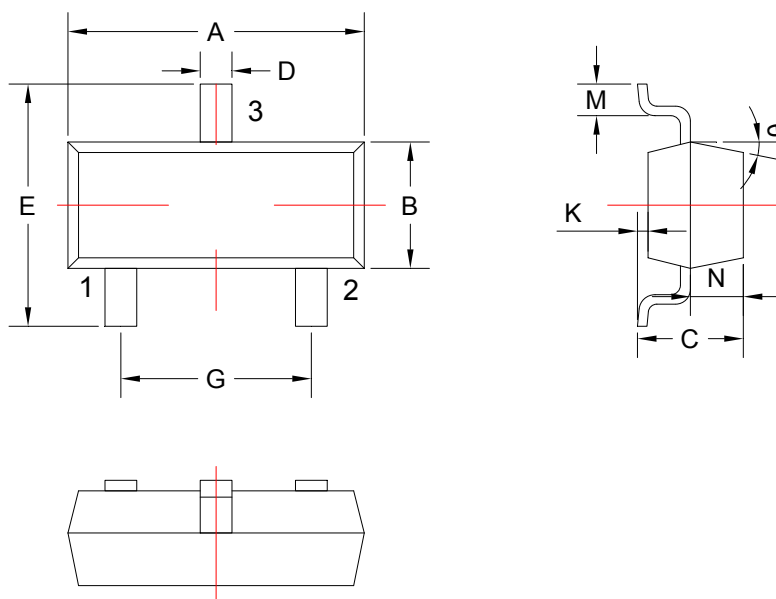


Figure 8. Gate Charge Characteristics

8. Dimension (SOT-23)



COMMON DIMENSIONS CUNITS MEASURE=MILLIMETER					
SYMBOL	MIN	MAX	SYMBOL	MIN	MAX
A	2.85	3.04	G	1.80	2.00
B	1.20	1.40	K	0	0.10
C	0.90	1.10	M	0.20	-
D	0.40	0.50	N	0.50	0.70
E	2.25	2.55	θ	5°	9°

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