

General Description

The CMSA075N04 uses advanced SGT technology to provide excellent RDS(ON). This device is ideal for load switch and battery protection applications.

Product Summary

BVDSS	R _{D(on)} max.	ID
40V	7.5mΩ	65A

Applications

- POL applications
- BLDC Motor driver

DFN-8 5x6 Pin Configuration



Features

- Low ON-resistance
- Low Gate Charge
- Surface Mount Package
- RoHS Compliant

Absolute Maximum Ratings

Type	Package	Marking
CMSA075N04	DFN-8 5x6	CMSA075N04

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current	65	A
I _D @T _C =100°C	Continuous Drain Current	45	A
I _{DM}	Pulsed Drain Current	260	A
EAS	Single Pulse Avalanche Energy ¹	56	mJ
P _D @T _C =25°C	Total Power Dissipation	45	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient(Steady-State)	---	60	°C/W
R _{θJC}	Thermal Resistance Junction-case	---	2.78	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	40	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=28\text{A}$	---	6.6	7.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=25\text{A}$	---	9.4	12.5	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D =250\mu\text{A}$	1	---	3	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=32\text{V}$, $V_{\text{GS}} =0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=20\text{A}$	---	16	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.9	---	Ω
Q_g	Total Gate Charge	$I_D=12\text{A}$	---	6	---	nC
Q_{gs}	Gate-Source Charge		---	3	---	
Q_{gd}	Gate-Drain Charge		---	1.2	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}$ $V_{\text{GS}}=10\text{ V}$ $R_{\text{GEN}}=3.3\Omega$ $I_D=1\text{A}$	---	15	---	ns
T_r	Rise Time		---	6	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	20	---	
T_f	Fall Time		---	11	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	770	---	pF
C_{oss}	Output Capacitance		---	170	---	
C_{rss}	Reverse Transfer Capacitance		---	6	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	65	A
I_{SM}	Pulsed Source Current		---	---	260	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=28\text{A}$, $T_J=25^\circ\text{C}$	---	0.9	1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $I_{\text{AS}}=15\text{A}$.

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Cmos reserves the right to improve product design ,functions and reliability without notice.

Typical Characteristics
