

General Description

The 80R290R is power MOSFET using Cmos's advanced super junction technology that can realize very low on resistance and gate charge. It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of low EMI to designers as well as low switching loss.

Product Summary

BVDSS	R _{D(on)} max.	ID
800V	0.29Ω	17A

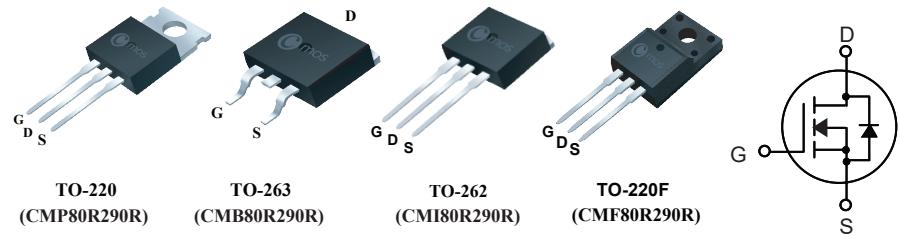
Applications

- Charger
- Adaptor
- Power Supply

TO-220/263/262/220F Pin Configuration

Features

- Fast switching
- 100% avalanche tested
- RoHS Compliant



Absolute Maximum Ratings

Symbol	Parameter	220/263/262	220F	Units
V _{DS}	Drain-Source Voltage	800		V
V _{GS}	Gate-Source Voltage	±30		V
I _D @T _C =25°C	Continuous Drain Current	17	17*	A
I _D @T _C =100°C	Continuous Drain Current	10.8	10.8*	A
I _{DM}	Pulsed Drain Current	68	68*	A
EAS	Single Pulse Avalanche Energy (Note 1)	21.6		mJ
P _D @T _C =25°C	Total Power Dissipation	220	43	W
T _{STG}	Storage Temperature Range	-55 to 150		°C
T _J	Operating Junction Temperature Range	-55 to 150		°C

* Drain current limited by maximum junction temperature.

Thermal Data

Symbol	Parameter	220/263/262	220F	Unit
R _{θJA}	Thermal Resistance Junction-ambient	63.6	63.6	°C/W
R _{θJC}	Thermal Resistance Junction-case	0.57	2.9	°C/W

800V, 0.235Ω typ., 17A N-Channel Super Junction Power MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	800	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=11\text{A}$	---	0.235	0.29	Ω
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=800\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	±100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=25\text{V}$, $I_D=10\text{A}$	---	12	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	5.2	---	Ω
Q_g	Total Gate Charge	$I_D=17\text{A}$	---	39	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=640\text{V}$	---	7.3	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	19.2	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=400\text{V}$	---	28	---	ns
T_r	Rise Time	$V_{\text{GS}}=10\text{V}$	---	50	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$I_D=17\text{A}$	---	129	---	
T_f	Fall Time	$R_G=25\Omega$	---	44	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1500	---	pF
C_{oss}	Output Capacitance		---	2270	---	
C_{rss}	Reverse Transfer Capacitance		---	100	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	17	A
I_{SM}	Pulsed Source Current		---	---	68	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=17\text{A}$	---	0.9	1.4	V

Note :

1.The EAS data shows Max. rating .The test condition is $V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=30\text{mH}$, $I_{\text{AS}}=1.2\text{A}$.

This product has been designed and qualified for the consumer market.
 Cmos assumes no liability for customers' product design or applications.
 Cmos reserves the right to improve product design ,functions and reliability without notice.

800V, 0.235Ω typ., 17A N-Channel Super Junction Power MOSFET

Typical Characteristics

