

### General Description

The CMSA6284 uses advanced SGT technology to provide excellent  $R_{DS(ON)}$ . This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

### Features

- Low ON-resistance
- Low Gate Charge
- Small Footprint (5x6 mm) for Compact Design
- RoHS Compliant

### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ\text{C}$	Continuous Drain Current	78	A
$I_D @ T_c = 100^\circ\text{C}$	Continuous Drain Current	49	A
$I_{DM}$	Pulsed Drain Current	234	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	98	mJ
$P_D @ T_c = 25^\circ\text{C}$	Total Power Dissipation	80	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient(Steady-State)	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case (Steady-State)	---	1.56	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	80	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=20\text{A}$	---	5.7	6.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=15\text{A}$	---	8.2	9.3	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D =250\mu\text{A}$	1	---	3	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}} =0\text{V}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=20\text{A}$	---	26	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	0.9	---	$\Omega$
$Q_g$	Total Gate Charge	$I_D=18\text{A}$	---	34	---	nC
$Q_{\text{gs}}$	Gate-Source Charge		---	7.5	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	4	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=40\text{V}$	---	7	---	ns
$T_r$	Rise Time		---	4	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	30	---	
$T_f$	Fall Time		---	5	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	1900	---	pF
$C_{\text{oss}}$	Output Capacitance		---	960	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	80	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	78	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	234	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=28\text{A}$ , $T_J=25^\circ\text{C}$	---	0.87	1.2	V

Note :

1.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=50\text{V}$  ,  $V_{\text{GS}}=10\text{V}$  ,  $L=1\text{mH}$  ,  $I_{\text{AS}}=14\text{A}$ .

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

**Typical Characteristics**
