

General Description

This Power MOSFET is produced using Cmos's advanced planar stripe DMOS technology. These devices are well suited for low voltage applications such as automotive, DC/DC converters, and high efficiency switching for power management in portable and battery operated products.

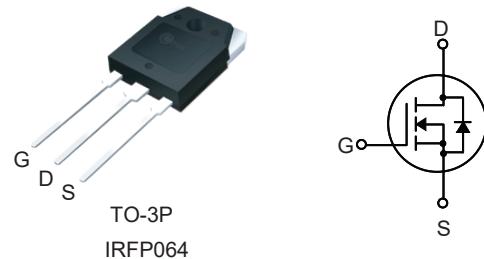
Product Summary

BVDSS	R _{Ds(on)} max.	ID
60V	10mΩ	110A

Applications

- Switch Mode Power Supply
- Uninterruptible Power Supply
- Power Factor Correction

TO-3P Pin Configuration



Features

- Low On-Resistance
- 100% avalanche tested
- RoHS Compliant

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current	110	A
I _D @T _C =100°C	Continuous Drain Current	77	A
I _{DM}	Pulsed Drain Current	440	A
EAS	Single Pulse Avalanche Energy ¹	2178	mJ
P _D @T _C =25°C	Total Power Dissipation	200	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient	---	40	°C/W
R _{θJC}	Thermal Resistance Junction-case	---	0.63	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=30\text{A}$	---	8.3	10	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	uA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_C=150^\circ\text{C}$	---	---	10	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}= \pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=30\text{A}$	---	35	---	S
Q_g	Total Gate Charge	$I_D=42\text{A}$	---	115	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=48\text{V}$	---	13	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	55	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DS}}=30\text{V}$ $I_D = 42\text{A}$ $R_G = 25\Omega$	---	52	---	ns
T_r	Rise Time		---	142	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	355	---	
T_f	Fall Time		---	230	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2700	---	pF
C_{oss}	Output Capacitance		---	1050	---	
C_{rss}	Reverse Transfer Capacitance		---	500	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	110	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=30\text{ A}$, $T_J=25^\circ\text{C}$	---	0.85	1.3	V

Note :

1 : The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=66\text{A}$.

This product has been designed and qualified for the consumer market.

Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

Typical Characteristics
