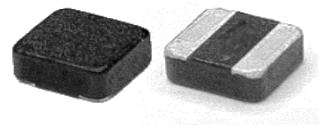


**FEATURES**

- RoHS, Halogen Free and REACH Compliance
- High performance (Isat) realized by metal dust core.
- Low coil resistance with large currents.
- Operate temperature range ....  $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$  (Including self temp. rise)



**APPLICATIONS**

- Smart phone, pad
- Notebooks, VR, AR
- Portable gaming devices, Smart wear, Wi-Fi module

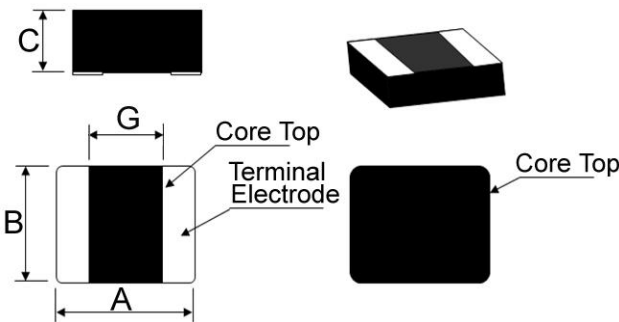
**PRODUCT IDENTIFICATION**

AFE 121065 S R47 M B T

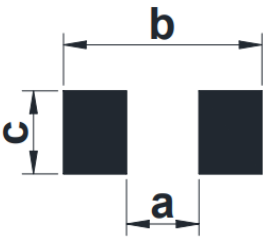
①      ②      ③ ④ ⑤ ⑥ ⑦

- ① Series Name:Mini Molding Chip type power inductor
- ② Size Code: L\*W\*T
- ③ Feature Type:Standard
- ④ Initial inductance value: R47 = 0.47uH
- ⑤ Inductance Tolerance:M $\pm$ 20%
- ⑥ Coating color:B=Black
- ⑦ Packing: Tape & Reel

**Dimensions: [mm]**



**Recommended Pattern**



Series	A	G	B	C	b	a	c
AFE121065S	1.2 $\pm$ 0.2	0.4 $\pm$ 0.2	1.0 $\pm$ 0.2	0.65Max.	1.30	0.30	1.10

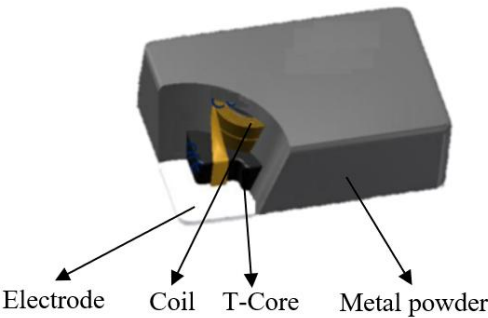
# Electrical Properties:

P/N	L0(μH) @ (0A) 1MHz	Rdc(mΩ)		Heat rating current Irms(A)		Saturation current Isat(A)	
		Typical	Max	Typical	Max	Typical	Max
AFE121065S2R2MBT	2.2	280	340	1.0	0.9	1.3	1.2

## Test remarks

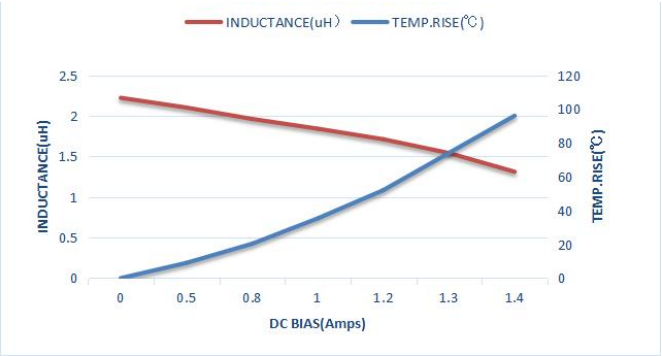
- Note 1.: All test data is referenced to 25 °C ambient.
- Note 2.: Test Condition: 1MHz, 1.0Vrms.
- Note 3.: Irms: DC current (A) that will cause an approximate ΔT of 40 °C.
- Note 4.: Isat: DC current (A) that will cause L0 to drop approximately 30%.
- Note 5.: Operating Temperature Range -55°C to + 125°C.
- Note 6.: The part temperature (ambient + temp rise) should not exceed 125 under °C the worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.
- Note 7.: The rated current as listed is either the saturation current or the heating current depending on which value is lower.

# Structure



# Current Characteristic

AFE121065S2R2MBT



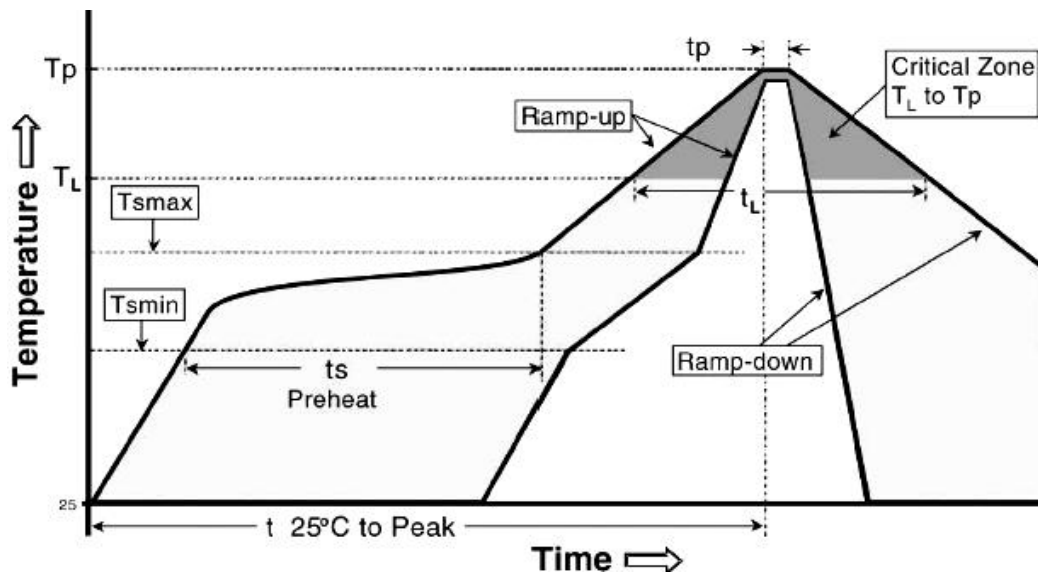
## Reliability

Item	Requirements	Test Methods and Remarks
Insulation Resistance	$\geq 100\text{M}\Omega$	100 VDC between inductor coil and The middle of the top surface of the body for 60 seconds.
Solderability	90% or more of electrode area shall be coated by new solde.	Dip pads in flux . Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). Solder Temperature: $245 \pm 5^\circ\text{C}$ . Immersion Time: $(5 \pm 1)$ s.
Resistance to Soldering Heat	No visible mechanical damage. Inductance change: Within $\pm 10\%$ .	Dip pads in flux. Solder Composition: Sn/Ag3.0/Cu0.5(Pb-Free). Solder Temperature: $260 \pm 5^\circ\text{C}$ . Immersion Time: $10 \pm 1$ sec.
Adhesion of teral electrode	Strong bond between the pad and the core, without come off PCB.	Inductors shall be subjected to $(260 \pm 5)^\circ\text{C}$ for $(20 \pm 5)$ s Soldering in the base whit 0.3mm solder. And then aplombelectrode way plus tax 10 N for $(10 \pm 1)$ seconds.
High temperature	No case deformation or change in appearance. Inductance change: Within $\pm 10\%$	Temperature: $125 \pm 2^\circ\text{C}$ . Time : 1000 hours. Measurement at $24 \pm 4$ hours after test conclusion.
Low temperature	No visible mechanical damage. Inductance change: Within $\pm 10\%$	Temperature: $-40 \pm 2^\circ\text{C}$ . Time : 1000 hours. Measurement at $24 \pm 4$ hours after test conclusion.
Thermal shock	No visible mechanical damage. Inductance change: Within $\pm 10\%$	The test sample shall be placed at $(-55 \pm 3)^\circ\text{C}$ and $(125 \pm 3)^\circ\text{C}$ for $(30 \pm 3)$ , different temperature conversion time is 2~3 utes. The temperature cycle shall be repeated 32 cycles. Placed at room temperature for 2 hours, within $48 \pm 4$ hours of testing.
Temperature characteristic	Inductance change Pc-b,Pc-d: Within $\pm 20\%$	a: $+20^\circ\text{C}$ (30~45) → b: $-40^\circ\text{C}$ (30~45) → c: $+20^\circ\text{C}$ (30~45) → d: $+125^\circ\text{C}$ (30~45) → e: $+20^\circ\text{C}$ (30~45) $P_{c-b} = \frac{L_b - L_c}{L_c} \times 100\%$ ; $P_{c-d} = \frac{L_d - L_c}{L_c} \times 100\%$
Static Humidity	No visible mechanical damage. Inductance change: Within $\pm 10\%$	Inductors shall be subjected to $(95 \pm 3)\% \text{RH}$ . at $(60 \pm 2)^\circ\text{C}$ for $(1000 \pm 4)$ h. Placed at room temperature for 2 hours, within 48 hours of testing.
	No visible mechanical damage. Inductance change: Within $\pm 10\%$	Inductors shall be store at $(85 \pm 2)^\circ\text{C}$ for $(1000 \pm 4)$ hours with Irms applied. Placed at room temperature for 2 hours, within 48 hours of testing

## Soldering Condition

(This is for recommendation, please customer perform adjustment according to actual application)

Recommend Reflow Soldering Profile : (solder : Sn96.5 / Ag3 / Cu0.5)



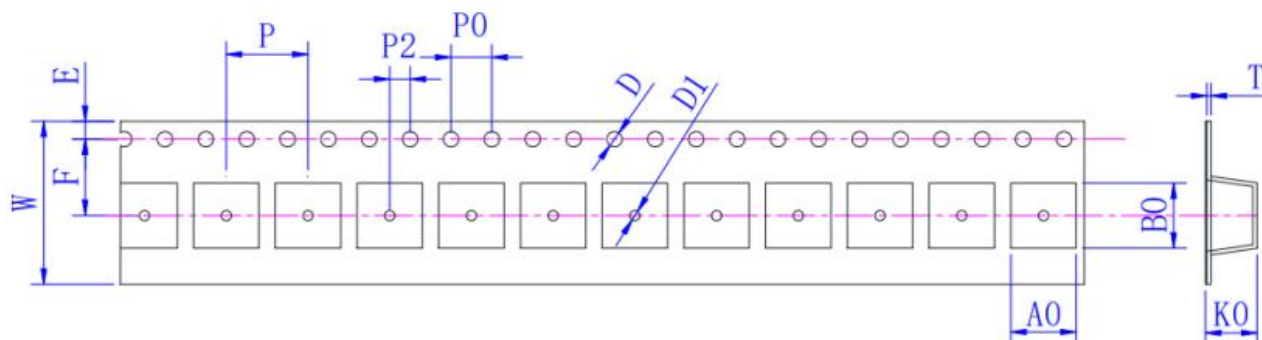
Profile Feature	Lead (Pb)-Free solder
Preheat:	
Temperature Min ( $T_{smin}$ )	150°C
Temperature Max ( $T_{smax}$ )	200°C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60 -120 seconds
Average ramp-up rate: ( $T_{smax}$ to $T_p$ )	3°C / second max.
Time maintained above :	
Temperature ( $T_L$ )	217°C
Time ( $t_L$ )	60-150 seconds
Peak Temperature ( $T_p$ )	260°C
Time within $+0_{-5}^{\circ}\text{C}$ of actual peak Temperature ( $t_p$ ) <sup>2</sup>	10 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8minutes max.

Allowed Re-flow times : 2 times

Remark : To avoid discoloration phenomena of chip on terminal electrodes, please use N<sub>2</sub> Re-flow furnace .

## Packing

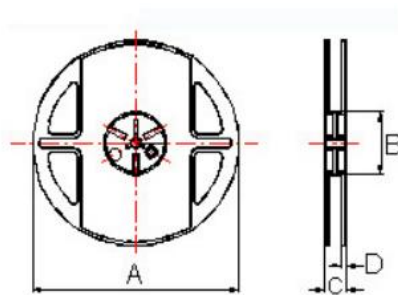
Dimension of plastic taping: (Unit: mm)



Series	W $\pm 0.30$	A0 $\pm 0.05$	B0 $+0.1/-0$	D $+0.1/-0$	D1 Min	E $\pm 0.10$	F $\pm 0.10$	K0 $\pm 0.05$	P0 $\pm 0.10$	P2 $\pm 0.10$	P $\pm 0.10$	T $\pm 0.05$
121065	8.00	1.30	1.50	1.50	1.0	1.75	3.50	0.80	4.00	2.00	4.00	0.23

Dimension of Reel : (Unit: mm)

Type	A $\pm 0.5$	B $\pm 0.5$	C $\pm 0.5$	D $\pm 1$
All	178	60	12	1.5



Packing Quantity: 5000pcs/Reel