

VBFB1410 Datasheet

N-Channel 40 V (D-S) MOSFET

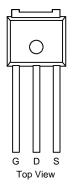
| PRODUCT SUMMARY | | | | |
|---------------------|--|--------------------|-----------------------|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) | Q _g (Typ.) | |
| 40 | $0.0F3 \text{ at V}_{GS} = 10 \text{ V}$ | 55 ^d | F9.5 | |
| 40 | 0.0FI at $V_{GS} = 4.5 \text{ V}$ | I 5 ^d | 19.5 | |

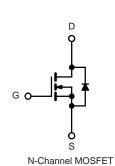
FEATURES

- · Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
 Compliant to RoHS Directive 2002/95/EC









APPLICATIONS

- · Power Supply
 - Secondary Synchronous Rectification
- DC/DC Converter

| ABSOLUTE MAXIMUM RATINGS | S T _C = 25 °C, unless oth | erwise noted | | | |
|---|---|-----------------------------------|--------------------|------|--|
| Parameter | | Symbol | Limit | Unit | |
| Drain-Source Voltage | | V _{DS} | 40 | V | |
| Gate-Source Voltage | | V _{GS} ± 20 | | v | |
| Continuous Drain Current (T _{.I} = 150 °C) | T _C = 25 °C | I_ | 55 ^d | A | |
| Continuous Diam Current (1) = 130 °C) | T _C = 70 °C | I _D | I 5 ^d | | |
| Pulsed Drain Current | | I _{DM} | 165 | А | |
| Avalanche Current | | I _{AS} | H4 | | |
| Single Avalanche Energy ^a | L = 0.1 mH | E _{AS} | Ϊ 8 | mJ | |
| Mariana Darra Dissinational | T _C = 25 °C | D. | Í 5.5 ^b | W | |
| Maximum Power Dissipation ^a | T _A = 25 °C ^c | $ P_{D}$ | 2.7 | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | °C | |

| THERMAL RESISTANCE RATINGS | | | |
|--|-------------------|-------|------|
| Parameter | Symbol | Limit | Unit |
| Junction-to-Ambient (PCB Mount) ^c | R _{thJA} | ĺ4 | °C/W |
| Junction-to-Case (Drain) | R _{thJC} | 2.Ï | C/VV |

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.c. When mounted on 1" square PCB (FR-4 material).
- d. Package limited.



| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|---|----------------------|--|------|-------|-------|------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V_{DS} | V _{DS} = 0 V, I _D = 250 μA | 40 | | | ١/ |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$ | 1 | | 2.5 | V |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 250 | nA |
| | | V _{DS} = 40V, V _{GS} = 0 V | | | 1 | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = 40V, V _{GS} = 0 V, T _J = 125 °C | | | 50 | μA |
| | | V _{DS} = 40V , V _{GS} = 0 V, T _J = 150 °C | | | 250 | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$ | 55 | | | Α |
| Drain-Source On-State Resistance ^a | В | V _{GS} = 10 V, I _D = 22 A | | 0.0F3 | | Ω |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} = 4.5 V, I _D = 20 A | | 0.0FI | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 15 V, I _D = 20 A | | 1€0 | | S |
| Dynamic ^b | | | | | | |
| Input Capacitance | C _{iss} | V _{GS} = 0 V, V _{DS} = 15 V, f = 1 MHz | | 1100 | | pF |
| Output Capacitance | C _{oss} | | | 460 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 350 | | |
| Total Gate Charge ^c | | V _{DS} = 15 V, V _{GS} = 10 V, I _D = 20 A | | H6 | | |
| Total Gate Charge | Qg | | | 25 | | ~C |
| Gate-Source Charge ^c | Q_{gs} | V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 20 A | | Î | | nC |
| Gate-Drain Charge ^c | Q_{gd} | | | Í .7 | | |
| Gate Resistance | R _g | f = 1 MHz | 0.4 | 2 | 4 | Ω |
| Turn-On Delay Time ^c | t _{d(on)} | | | 8 | 16 | |
| Rise Time ^c | t _r | V_{DD} = 15 V, R_{L} = 1.5 Ω | | 9 | 18 | |
| Turn-Off Delay Time ^c | t _{d(off)} | $I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$ | | 35 | 53 | ns |
| Fall Time ^c | t _f | | | 9 | 18 | |
| Drain-Source Body Diode Ratings a | nd Characteris | stics T _C = 25 °C ^b | | | | |
| Continuous Current | I _S | | | | 55 | |
| Pulsed Current | I _{SM} | | | | 165 | Α |
| Forward Voltage ^a | V_{SD} | I _F = 10 A, V _{GS} = 0 V | | 0.75 | 1.5 | V |
| Reverse Recovery Time | t _{rr} | | | 34 | 51 | ns |
| Peak Reverse Recovery Current | I _{RM(REC)} | I _F = 10 A, dI/dt = 100 A/μs | | 2 | 3 | Α |
| Reverse Recovery Charge | Q _{rr} | | | 34 | 51 | nC |

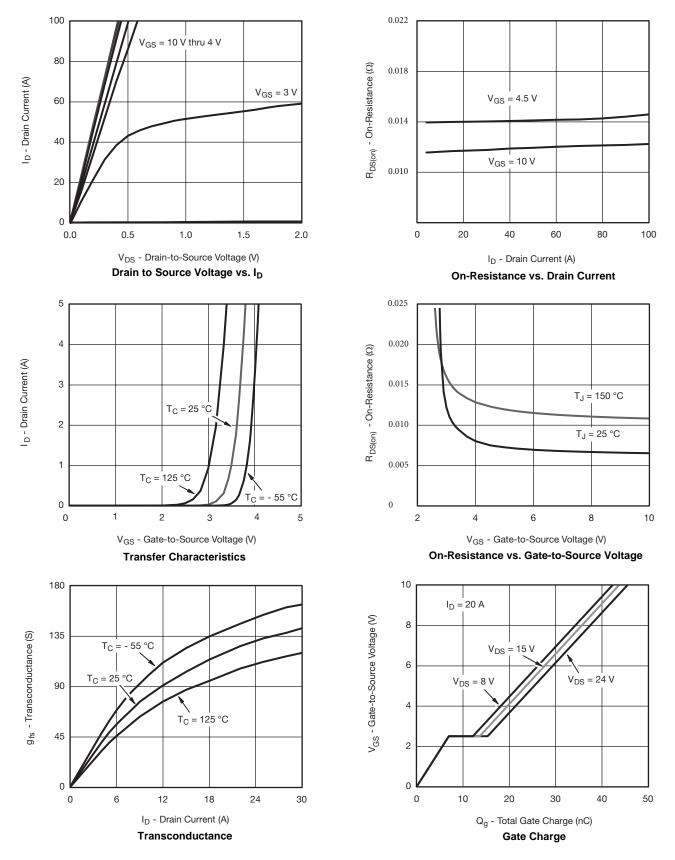
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

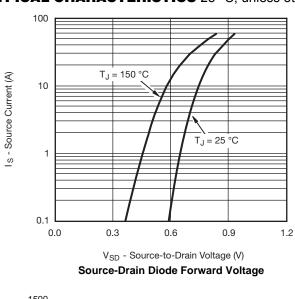


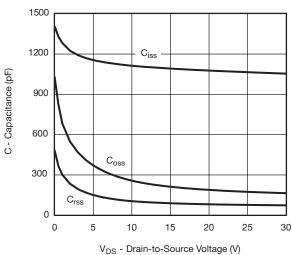
TYPICAL CHARACTERISTICS 25 C, unless otherwise noted



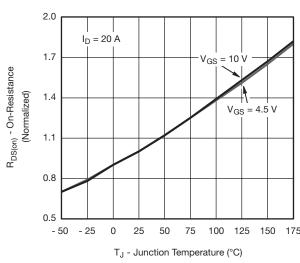


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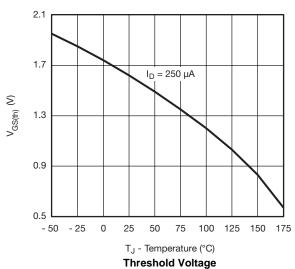


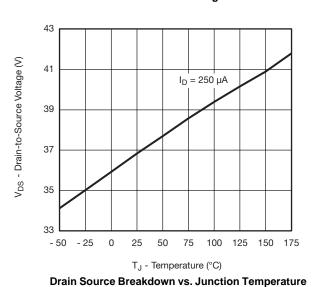


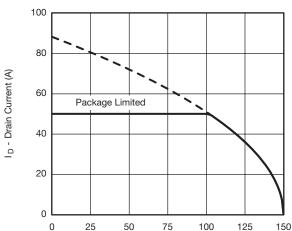
Capacitance







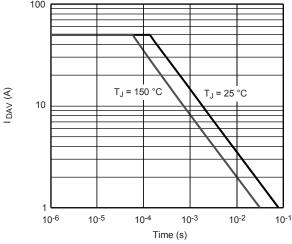


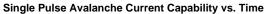


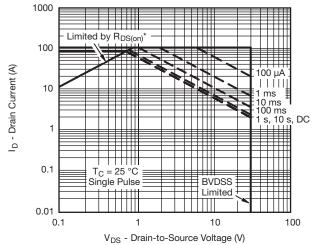
Current Derating



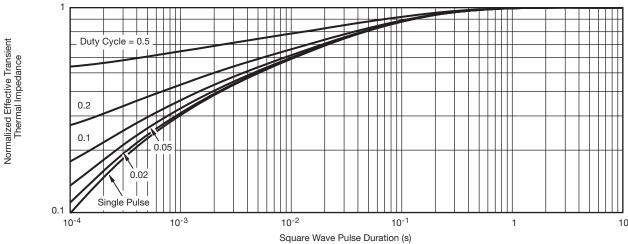
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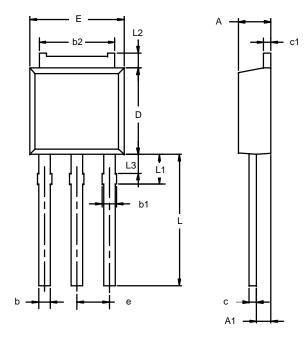
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified **Safe Operating Area**



Normalized Thermal Transient Impedance, Junction-to-Case



TO-251AA



| Note: | Dimension | I 3 is for | reference | only |
|-------|-----------|------------|-----------|------|
| | | | | |

| | MILLIMETERS | | INCHES | | |
|-----|-------------|----------|--------|-------|--|
| Dim | Min | Max | Min | Max | |
| Α | 2.21 | 2.38 | 0.087 | 0.094 | |
| A1 | 0.89 | 1.14 | 0.035 | 0.045 | |
| b | 0.71 | 0.89 | 0.028 | 0.035 | |
| b1 | 0.76 | 1.14 | 0.030 | 0.045 | |
| b2 | 5.23 | 5.43 | 0.206 | 0.214 | |
| С | 0.46 | 0.58 | 0.018 | 0.023 | |
| с1 | 0.46 | 0.58 | 0.018 | 0.023 | |
| D | 5.97 | 6.22 | 0.235 | 0.245 | |
| Е | 6.48 | 6.73 | 0.255 | 0.265 | |
| е | 2.28 | 2.28 BSC | | BSC | |
| L | 3.89 | 9.53 | 0.153 | 0.375 | |
| L1 | 1.91 | 2.28 | 0.075 | 0.090 | |
| L2 | 0.89 | 1.27 | 0.035 | 0.050 | |
| L3 | 1.15 | 1.52 | 0.045 | 0.060 | |

DWG: 5346



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