

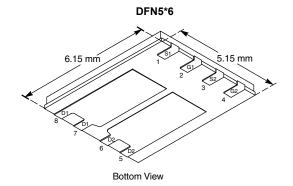
SI7844DP-VB Datasheet Dual N-Channel 30 V (D-S) MOSFET

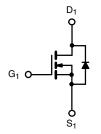
| PRODUCT SUMMARY | | | | | |
|---------------------|---------------------------------|--------------------|--|--|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) | | | |
| 30 | 0.018 at V _{GS} = 10 V | 22 | | | |

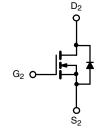
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted) | | | | | | |
|--|------------------------|-----------------------------------|------------|--------------|------|--|
| Parameter | | Symbol | 10 s | Steady State | Unit | |
| Drain-Source Voltage | | V _{DS} | 30 | | V | |
| Gate-Source Voltage | | V_{GS} | ± 20 | | V | |
| Continuous Drain Current (T _{.I} = 150 °C) ^a | T _A = 25 °C | 1 | 22 | | ٨ | |
| Continuous Diam Current (1, = 150 C) | T _A = 70 °C | l _D | 15 | | | |
| Pulsed Drain Current | | I _{DM} | 50 | | Α | |
| Continuous Source Current (Diode Conduction) ^a | | I _S | 2.9 | | | |
| Maximum Power Dissipation ^a | T _A = 25 °C | D. | 3.5 2.2 | | W | |
| Maximum Fower Dissipation | T _A = 70 °C | P _D | | | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 | 5 to 150 | °C | |
| Soldering Recommendations (Peak Temperature) | | | 260 | | | |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|--------------|-------------------|---------|---------|------|
| Parameter | | Symbol | Typical | Maximum | Unit |
| Maniero de La Alianda Andrianda | t ≤ 10 s | B | 26 | 35 | |
| Maximum Junction-to-Ambient ^a | Steady State | R _{thJA} | 60 | 85 | °C/W |
| Maximum Junction-to-Case (Drain) | Steady State | R_{thJC} | 3.9 | 5.5 | |

Notes:

a. Surface mounted on 1" x 1" FR4 board.

服务热线:400-655-8788

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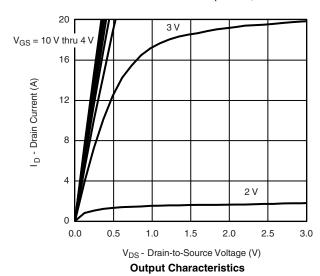
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|---------------------|--|-----------|-------|-------|------|--|
| Static | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 0.8 | | 2.4 | V | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA | |
| Zara Cata Valtaga Drain Current | | V _{DS} = 30 V, V _{GS} = 0 V | s = 0 V 1 | | 1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$ | | | 5 | μΑ | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$ | 20 | | | Α | |
| | _ | V _{GS} = 10 V, I _D = 10 A | | 0.018 | | Ω | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = 4.5 \text{ V}, I_D = 8.5 \text{ A}$ | | 0.024 | | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 15 V, I _D = 10 A | | 22 | | S | |
| Diode Forward Voltage ^a | V_{SD} | I _S = 2.9 A, V _{GS} = 0 V | | 0.75 | 1.2 | V | |
| Dynamic ^b | | | | | | | |
| Total Gate Charge | Qg | | | 13 | 20 | | |
| Gate-Source Charge | Q_{gs} | $V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$ | | 2 | | nC | |
| Gate-Drain Charge | Q_{gd} | | | 2.7 | | | |
| Gate Resistance | R_g | | 0.5 | | 3.2 | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 8 | 16 | | |
| Rise Time | t _r | $V_{DD} = 15 \text{ V}, R_L = 15\Omega$ | | 10 | 20 |] | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong 1 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 6 \Omega$ | | 21 | 40 | ns | |
| Fall Time | t _f | | | 10 | 20 | | |
| Source-Drain Reverse Recovery Time | t _{rr} | I _F = 2.9 A, dI/dt = 100 A/μs | | 40 | 80 | | |

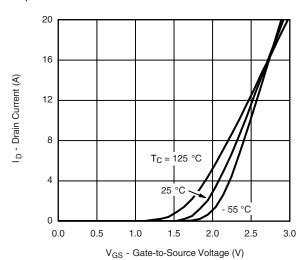
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

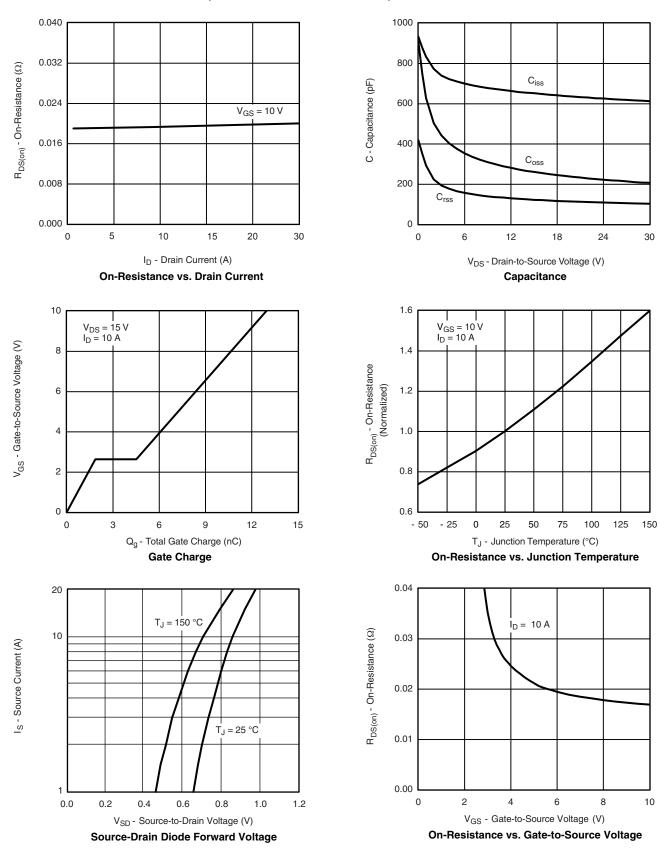




Transfer Characteristics

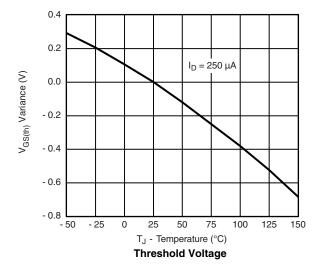


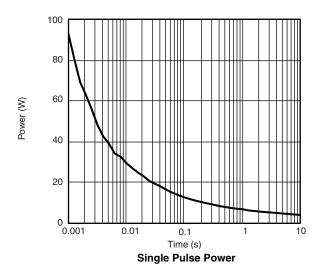
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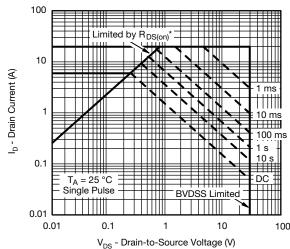


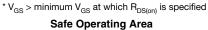


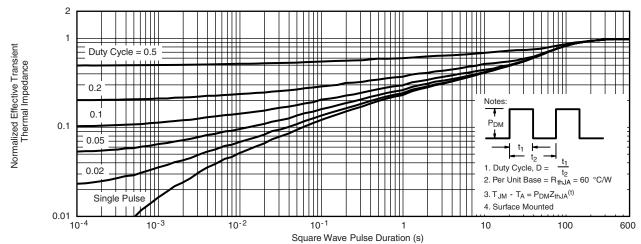
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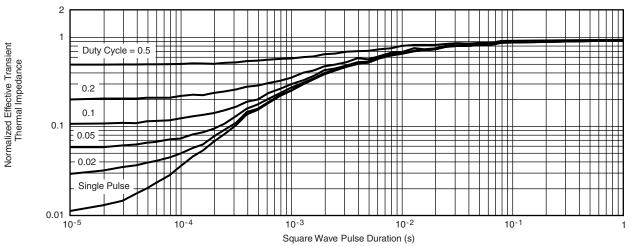




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

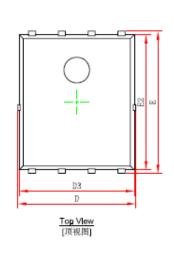


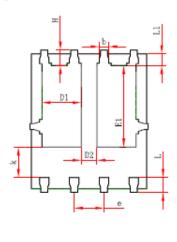
Normalized Thermal Transient Impedance, Junction-to-Case



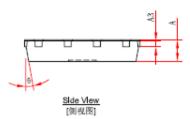
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PDFNWB5×6-8L-A PACKAGE OUTLINE DIMENSIONS





Bottom View [背视图]



| Symbol | Dimensions | In Millimeters | Dimensions In Inches | | |
|--------|----------------------|----------------|----------------------|-------|------|
| | Min. | Max. | Min. | Max. | |
| Α | 0.900 | 1.000 | 0.035 | 0.039 | |
| A3 | 0.254 REF. 0.010REF. | | REF. | | |
| D | 4.944 | 5.096 | 0.195 | 0.201 | |
| E | 5.974 | 6.126 | 0.235 | 0.241 | |
| D1 | 1.470 | 1.870 | 0.058 | 0.074 | |
| D2 | 0.470 | 0.870 | 0.019 | 0.034 | |
| E1 | 3.375 | 3.575 | 0.133 | 0.141 | |
| D3 | 4.824 | 4.976 | 0.190 | 0.196 | |
| E2 | 5.674 | 5.826 | 0.223 | 0.229 | |
| k | 1.190 | 1.390 | 0.047 | 0.055 | |
| b | 0.350 | 0.450 | 0.014 | 0.018 | |
| е | 1.270TYP. | | 1.270TYP. 0.050TYP. | | TYP. |
| L | 0.559 | 0.711 | 0.022 | 0.028 | |
| L1 | 0.424 | 0.576 | 0.017 | 0.023 | |
| Н | 0.574 | 0.726 | 0.023 | 0.029 | |
| θ | 10° | 12° | 10° | 12° | |



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