

## P-Channel 60 V (D-S) MOSFET

$V_{DS}$		-60	V
$R_{DS(on),typ}$	$V_{GS}=10V$	48	$m\Omega$
$R_{DS(on),typ}$	$V_{GS}=4.5V$	57	$m\Omega$
$I_D$		-30	A

### FEATURES

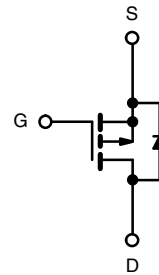
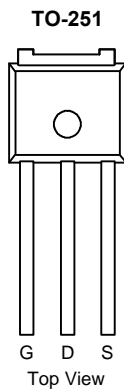
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available

### APPLICATIONS

- High Side Switch for Full Bridge Converter
- DC/DC Converter for LCD Display



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise note)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	- 60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150\text{ }^\circ\text{C}$ )	$T_C = 25\text{ }^\circ\text{C}$	$I_D$ - 30	A
	$T_C = 125\text{ }^\circ\text{C}$	- 20	
Pulsed Drain Current	$I_{DM}$	- 100	
Avalanche Current, Single Pulse	$L = 0.1\text{ mH}$	$I_{AS}$ - 22	mJ
Repetitive Avalanche Energy, Single Pulse <sup>a</sup>		$E_{AS}$ 24.2	
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	$P_D$ 38.5 <sup>c</sup>	W
	$T_A = 25\text{ }^\circ\text{C}$	2.3 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b</sup>	$R_{thJA}$	$t \leq 10\text{ s}$	17	21	$^\circ\text{C/W}$
		Steady State	45	55	
Maximum Junction-to-Case	$R_{thJC}$	2.7	3.25		

Notes:

- Duty cycle  $\leq 1\%$ .
- When mounted on 1" square PCB (FR-4 material).
- See SOA curve for voltage derating.
- Based up on  $T_C = 25\text{ }^\circ\text{C}$ .

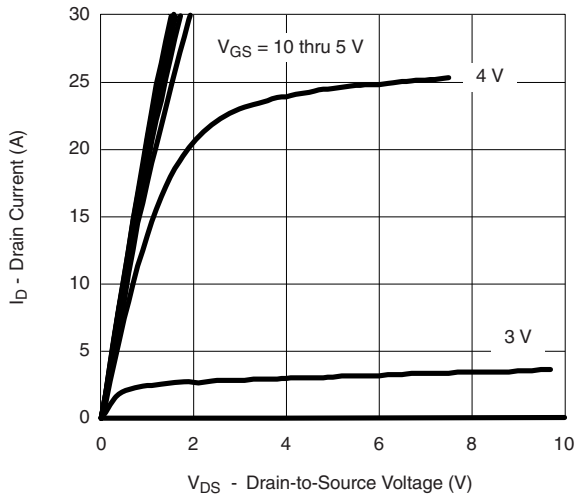
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise note)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	- 60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 1		- 3	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$			- 1	$\mu\text{A}$
		$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			- 50	
		$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 150\text{ }^\circ\text{C}$			- 125	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$	- 30			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -10\text{ A}$		48		m $\Omega$
		$V_{GS} = -10\text{ V}, I_D = -10\text{ A}, T_J = 125\text{ }^\circ\text{C}$		100		
		$V_{GS} = -10\text{ V}, I_D = -10\text{ A}, T_J = 150\text{ }^\circ\text{C}$		120		
		$V_{GS} = -4.5\text{ V}, I_D = -5\text{ A}$		57		
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -10\text{ A}$		22		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = -25\text{ V}, f = 1\text{ MHz}$			1900	$\mu\text{F}$
Output Capacitance	$C_{oss}$				130	
Reverse Transfer Capacitance	$C_{rss}$				90	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = -30\text{ V}, V_{GS} = -10\text{ V}, I_D = -10\text{ A}$		26		nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			4.5		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			7		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		7		$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = -30\text{ V}, R_L = 3\text{ }\Omega$ $I_D \cong -19\text{ A}, V_{GEN} = -10\text{ V}, R_g = 2.5\text{ }\Omega$		8	15	ns
Rise Time <sup>c</sup>	$t_r$			9	15	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			65	100	
Fall Time <sup>c</sup>	$t_f$			30	45	
<b>Drain-Source Body Diode and Characteristics</b> ( $T_C = 25\text{ }^\circ\text{C}$ ) <sup>b</sup>						
Continuous Current	$I_S$				- 30	A
Pulsed Current	$I_{SM}$				- 30	
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = -19\text{ A}, V_{GS} = 0\text{ V}$		- 1	- 1.5	V
Reverse Recovery Time	$t_{rr}$	$I_F = -19\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		41	61	ns

Notes:

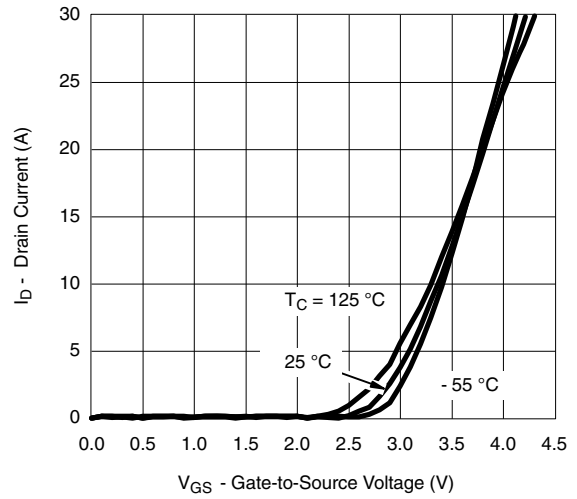
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



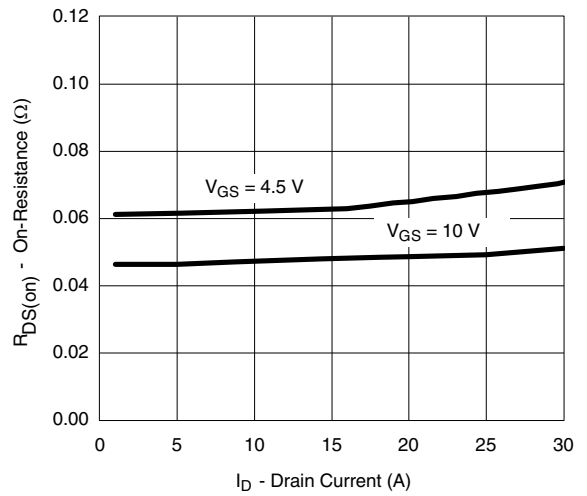
**Output Characteristics**



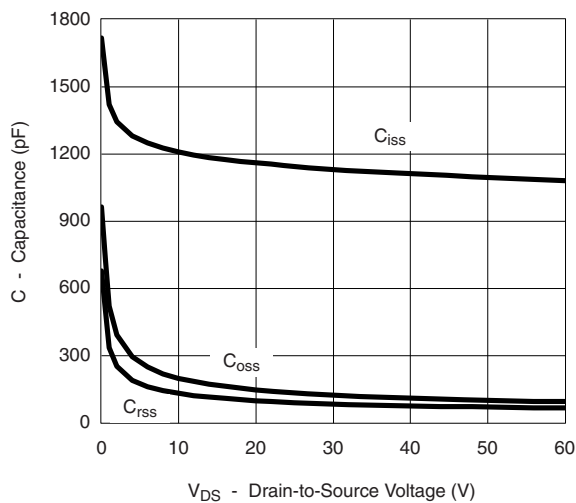
**Transfer Characteristics**



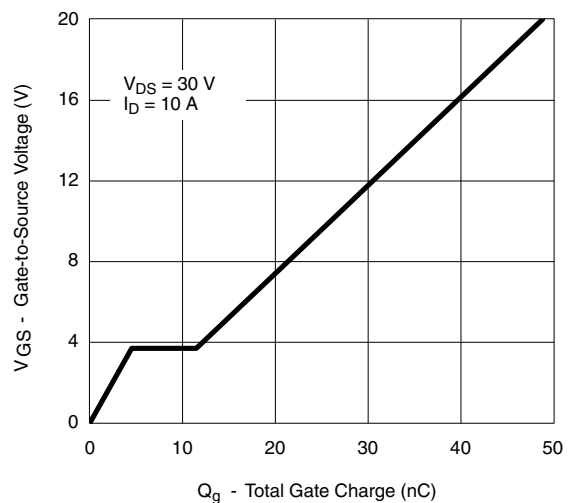
**Transconductance**



**On-Resistance vs. Drain Current**

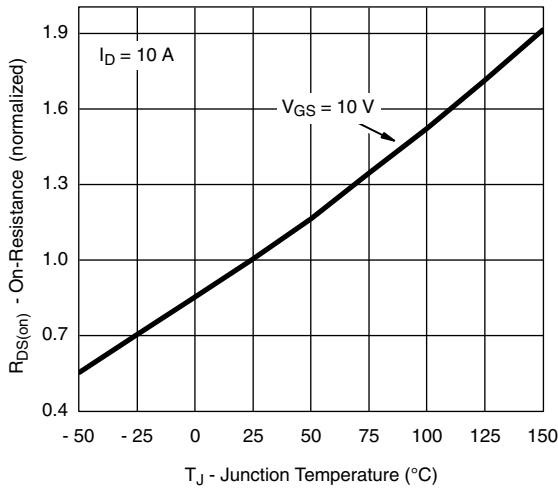


**Capacitance**



**Gate Charge**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

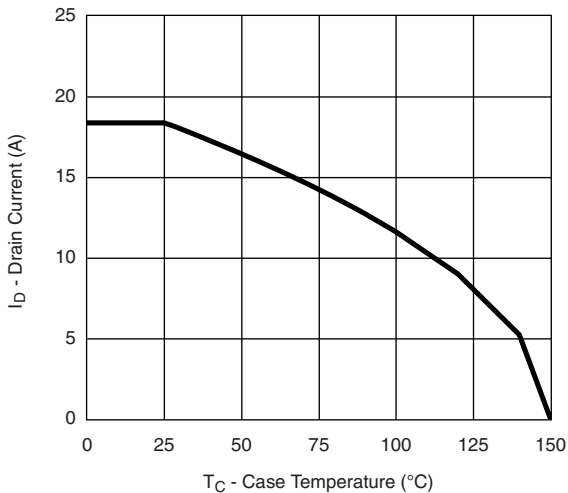


On-Resistance vs. Junction Temperature

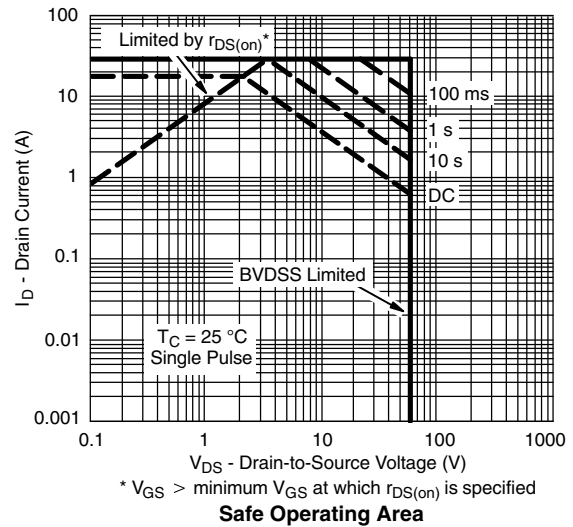


Source-Drain Diode Forward Voltage

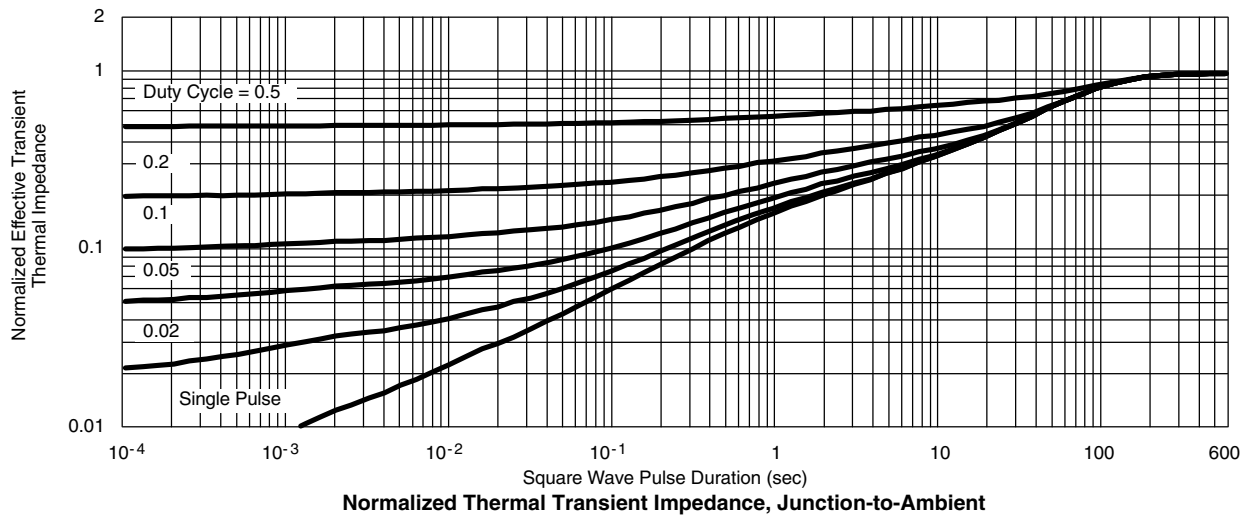
**THERMAL RATINGS**



Maximum Drain Current vs. Case Temperature

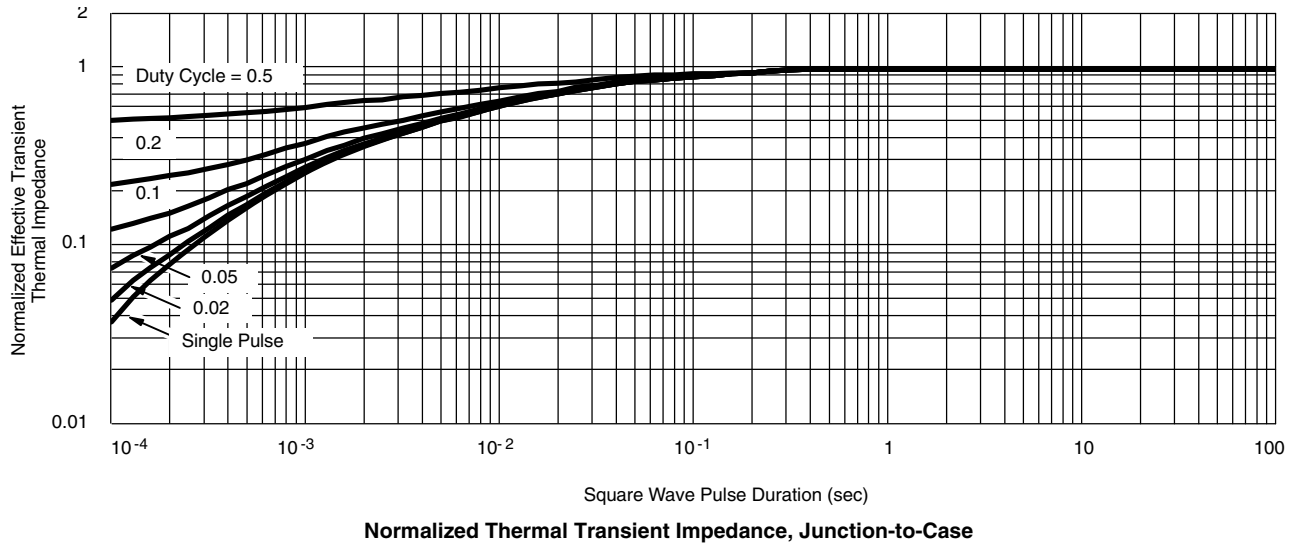


Safe Operating Area

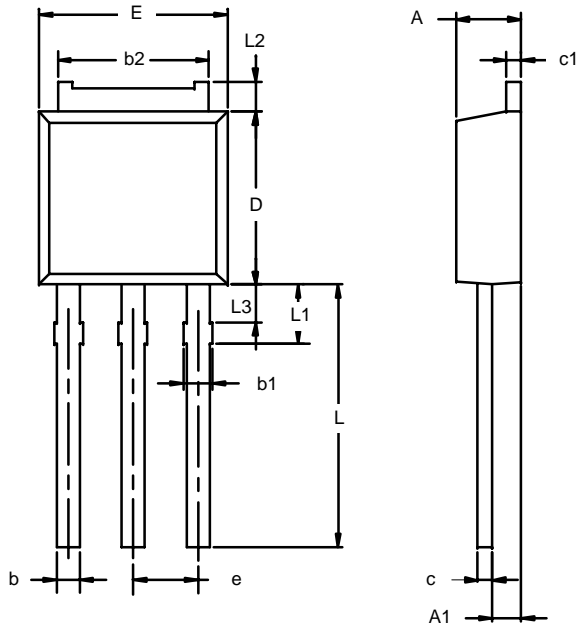


Normalized Thermal Transient Impedance, Junction-to-Ambient

**THERMAL RATINGS**



**TO-251AA**



Note: Dimension L3 is for reference only.

Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
<b>A</b>	2.21	2.38	0.087	0.094
<b>A1</b>	0.89	1.14	0.035	0.045
<b>b</b>	0.71	0.89	0.028	0.035
<b>b1</b>	0.76	1.14	0.030	0.045
<b>b2</b>	5.23	5.43	0.206	0.214
<b>c</b>	0.46	0.58	0.018	0.023
<b>c1</b>	0.46	0.58	0.018	0.023
<b>D</b>	5.97	6.22	0.235	0.245
<b>E</b>	6.48	6.73	0.255	0.265
<b>e</b>	2.28 BSC		0.090 BSC	
<b>L</b>	3.89	9.53	0.153	0.375
<b>L1</b>	1.91	2.28	0.075	0.090
<b>L2</b>	0.89	1.27	0.035	0.050
<b>L3</b>	1.15	1.52	0.045	0.060

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DWG: 5346

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