

HUFA76633S3ST-VB Datasheet N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)		
100	0.030 at V _{GS} = 10 V	45		
	0.035 at V _{GS} = 4.5 V	40		

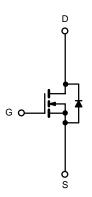
FEATURES

- TrenchFET® Power MOSFETS
- 175 °C Junction Temperature
- Low Thermal Resistance Package









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _C = 25 °C, unless otherwise noted						
Parameter	-	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	100			
Gate-Source Voltage	V _{GS}	± 20	- V			
Continuous Danis Comment /T. 475 90\	T _C = 25 °C	L	45			
Continuous Drain Current (T _J = 175 °C)	T _C = 125 °C	l _D	30	Δ.		
Pulsed Drain Current	I _{DM}	135	A A			
Avalanche Current		I _{AR}			35	
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	61	mJ		
	T _C = 25 °C	В	127 ^b	10/		
Maximum Power Dissipation ^a	T _A = 25 °C°	$ P_D$	3.75	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Limit	Unit		
Junction-to-Ambient	(PCB Mount) ^c	R _{thJA}	40	°C/W		
Junction-to-Case (Drain)		R _{thJC}	1.4	C/VV		

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{SS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1		3		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = 80 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 80 V, V _{GS} = 0 V, T _J = 125 °C			50		
		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 175 °C			250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	75			Α	
		V _{GS} = 10 V, I _D = 5 A		0.030		Ω	
	_	$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$		0.035			
Drain-Source On-State Resistance ^a	r _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}, T_J = 125 \text{ °C}$		0.050			
		V _{GS} = 10 V, I _D = 3 A, T _J = 175 °C		0.062			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10			S	
Dynamic ^b	•			•			
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		3100		pF	
Output Capacitance	C _{oss}			410			
Reverse Transfer Capacitance	C _{rss}			150			
Total Gate Charge ^c	Qg			35	60	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		11			
Gate-Drain Charge ^c	Q_{gd}			9			
Gate Resistance	R_{G}			1.7		Ω	
Turn-On Delay Time ^c	t _{d(on)}			11	20		
Rise Time ^c	t _r	V_{DD} = 50 V, R_L = 1.25 Ω		12	20	ns	
Turn-Off Delay Time ^c	t _{d(off)}	$I_D\cong 40$ A, V_{GEN} = 10 V, R_G = 2.5 Ω		30	45		
Fall Time ^c	t _f			12	20		
Source-Drain Diode Ratings and Cha	racteristics T	_C = 25 °C ^b		•			
Continuous Current	I _S				40	۸	
Pulsed Current	I _{SM}				120	А	
Forward Voltage ^a	V _{SD}	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			60	100	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	$I_F = 30 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		5	8	Α	
Reverse Recovery Charge	Q _{rr}			0.15	0.4	uС	

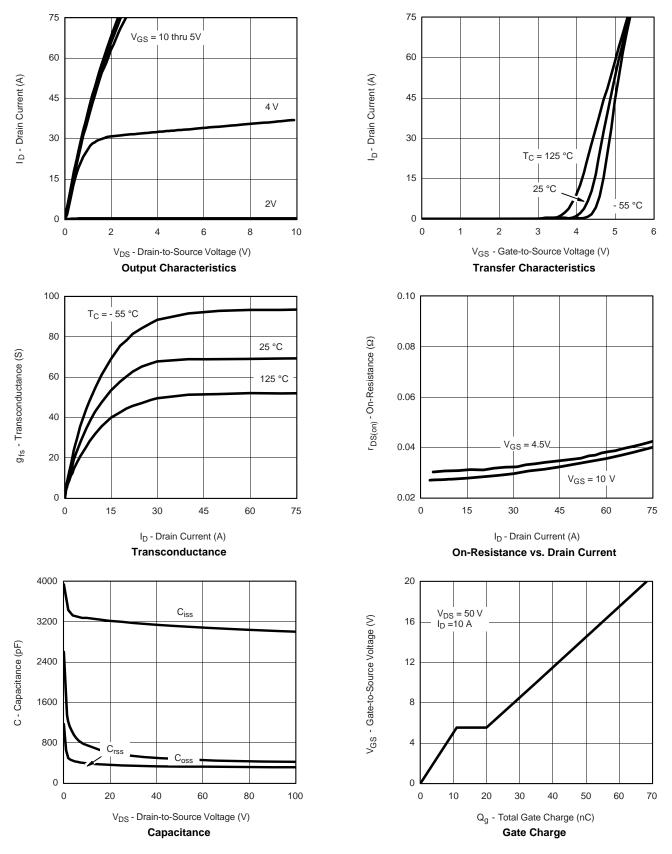
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

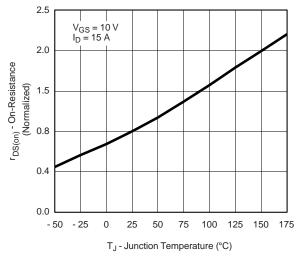


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

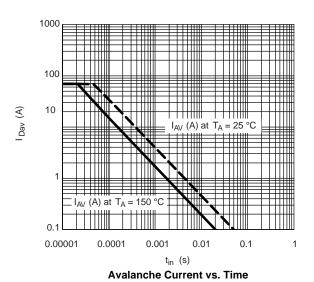




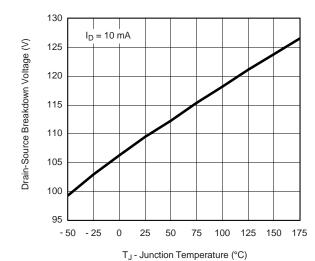
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On-Resistance vs. Junction Temperature



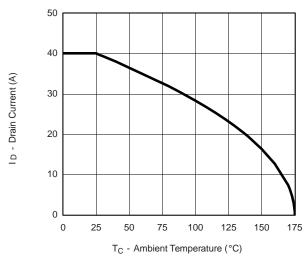
Source-Drain Diode Forward Voltage



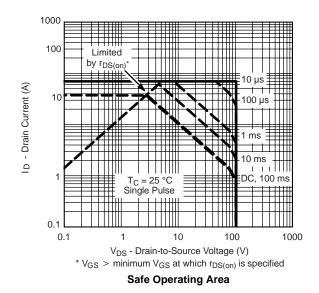
Drain-Source Breakdown Voltage vs. Junction Temperature

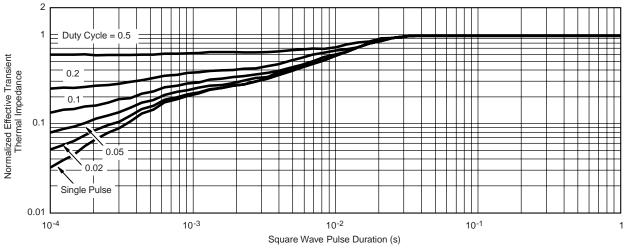


THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature





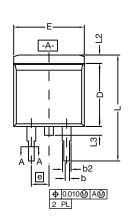
Normalized Thermal Transient Impedance, Junction-to-Case

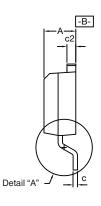
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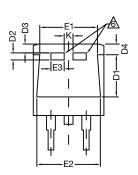
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TO-263 (D²PAK): 3-LEAD

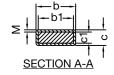








DETAIL A (ROTATED 90°)



	INCHES		MILLIN	METERS	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	0.160	0.190	4.064	4.826	
b	0.020	0.039	0.508	0.990	
b1	0.020	0.035	0.508	0.889	
b2	0.045	0.055	1.143	1.397	
Thin lead	0.013	0.018	0.330	0.457	
Thick lead	0.023	0.028	0.584	0.711	
Thin lead	0.013	0.017	0.330	0.431	
Thick lead	0.023	0.027	0.584	0.685	
c2	0.045	0.055	1.143	1.397	
D	0.340	0.380	8.636	9.652	
D1	0.220	0.240	5.588	6.096	
D2	0.038	0.042	0.965	1.067	
D3	0.045	0.055	1.143	1.397	
D4	0.044	0.052	1.118	1.321	
E	0.380	0.410	9.652	10.414	
E1	0.245	-	6.223	-	
E2	0.355	0.375	9.017	9.525	
E3	0.072	0.078	1.829	1.981	
е	0.100 BSC		2.54 BSC		
K	0.045	0.055	1.143	1.397	
L	0.575	0.625	14.605	15.875	
L1	0.090	0.110	2.286	2.794	
L2	0.040	0.055	1.016	1.397	
L3	0.050	0.070	1.270	1.778	
L4	0.010 BSC 0.254 B		BSC		
М	-	0.002	-	0.050	
	A b b1 b2 Thin lead Thick lead Thick lead c2 D D1 D2 D3 D4 E E1 E2 E3 e K L L1 L2 L3 L4	DIM. MIN. A 0.160 b 0.020 b1 0.020 b2 0.045 Thin lead 0.013 Thick lead 0.023 Thin lead 0.023 c2 0.045 D 0.340 D1 0.220 D2 0.038 D3 0.045 D4 0.044 E 0.380 E1 0.245 E2 0.355 E3 0.072 e 0.100 K 0.045 L 0.575 L1 0.090 L2 0.040 L3 0.050 L4 0.010 M -	DIM. MIN. MAX. A 0.160 0.190 b 0.020 0.039 b1 0.020 0.035 b2 0.045 0.055 Thin lead 0.013 0.018 Thick lead 0.023 0.028 Thin lead 0.013 0.017 Thick lead 0.023 0.027 c2 0.045 0.055 D 0.340 0.380 D1 0.220 0.240 D2 0.038 0.042 D3 0.045 0.055 D4 0.044 0.052 E 0.380 0.410 E1 0.245 - E2 0.355 0.375 E3 0.072 0.078 e 0.100 BSC K 0.045 0.055 L 0.575 0.625 L1 0.090 0.110 L2 0.040 0.055	DIM. MIN. MAX. MIN. A 0.160 0.190 4.064 b 0.020 0.039 0.508 b1 0.020 0.035 0.508 b2 0.045 0.055 1.143 Thin lead 0.013 0.018 0.330 Thick lead 0.023 0.028 0.584 Thin lead 0.013 0.017 0.330 Thick lead 0.023 0.027 0.584 c2 0.045 0.055 1.143 D 0.340 0.380 8.636 D1 0.220 0.240 5.588 D2 0.038 0.042 0.965 D3 0.045 0.055 1.143 D4 0.044 0.052 1.118 E 0.380 0.410 9.652 E1 0.245 - 6.223 E2 0.355 0.375 9.017 E3 0.072 0.078	

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DWG: 5843

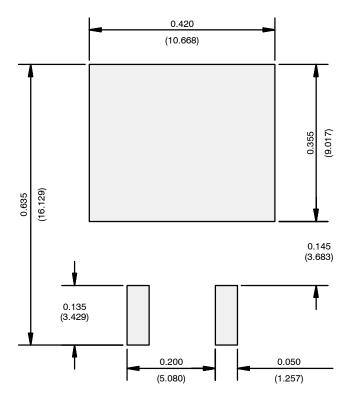
Note

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB.
 Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

This feature is for thick lead.



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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