

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	$R_{DS(on)}(\Omega)$ $I_{D}(A)^{d}$ $Q_{g}(Typ.$			
- 30	0.021 at $V_{GS} = -10 \text{ V}$	- 24 ^e	15 nC		
- 30	0.027 at $V_{GS} = -4.5 \text{ V}$	- 18.7	15110		

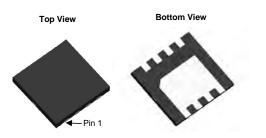
FEATURES

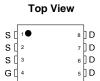
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS COMPLIANT HALOGEN FREE

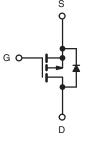
DFN 3x3 EP





APPLICATIONS

- Notebook PC
 - Load Switch
 - Battery Switch
 - Adaptor Switch



P-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 30	V	
Gate-Source Voltage		V_{GS}	± 20	v
	T _C = 25 °C		- 24 ^e	
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 70 °C		- 23.8	
Continuous Diam Current (1) = 150 °C)	T _A = 25 °C	l _D	- 10.5 ^{a, b}	
	T _A = 70 °C		- 8.3 ^{a, b}	Α
Pulsed Drain Current		I _{DM}	- 50	
Continuous Source-Drain Diode Current	T _C = 25 °C		- 23.2	
Continuous Source-Drain Diode Current	T _A = 25 °C	ls =	- 2.9 ^{a, b}	
Avalanche Current		I _{AS}	- 20	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	20	mJ
	T _C = 25 °C		27.8	
Mayimum Dawar Dissination	T _C = 70 °C	ь —	17.8	w
Maximum Power Dissipation	T _A = 25 °C	P _D	3.5 ^{a, b}	VV
	T _A = 70 °C		2.2 ^{a, b}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature) ^{f, 9}		260		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	29	36	°C/W	
Maximum Junction-to-Case	Steady State	R_{thJC}	3.6	4.5	- C/VV	

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 81 °C/W.
- d. Based on $T_C = 25$ °C.
- e. Package Limited.
- f. The DFN 3 x 3 is a leadless package. The end of the lead terminal is exposed ppper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- g. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 31			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		4.5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μΑ	
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 30			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 10.5 A		0.021		Ω	
		V _{GS} = - 4.5 V, I _D = - 8.3 A		0.027			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 10.5 A		23		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1350			
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		215		pF	
Reverse Transfer Capacitance	C _{rss}]		185			
Total Oaks Observe	Q_g $V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -10.5 \text{ A}$ $V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -10.5 \text{ A}$	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -10.5 \text{ A}$		32	50	nC	
Total Gate Charge				15	25		
Gate-Source Charge			4		nC		
Gate-Drain Charge	Q _{gd}]		7.5			
Gate Resistance	R_g	f = 1 MHz	1.2	5.8	11.6	Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r			8	15		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 8.4 A, V_{GEN} = - 10 V, R_g = 1 Ω		45	70		
Fall Time	t _f]		12	25		
Turn-On Delay Time	t _{d(on)}			42	70	ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 1.8 Ω		35	60		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 8.4 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		40	70	=	
Fall Time	t _f]		16	30		
Drain-Source Body Diode Characterist	ics						
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			- 23.2	۸	
Pulse Diode Forward Current	I _{SM}				- 50	Α	
Body Diode Voltage	V _{SD}	I _S = - 8.4 A, V _{GS} = 0 V		- 0.85	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			34	60	ns	
Body Diode Reverse Recovery Charge	Q_{rr}] _ 9.4.4. dl/dt _ 100.4/::2 T		22	40	nC	
Reverse Recovery Fall Time	t _a	$I_F = -8.4 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 °\text{C}$		11			
Reverse Recovery Rise Time	t _b	1		23		ns	

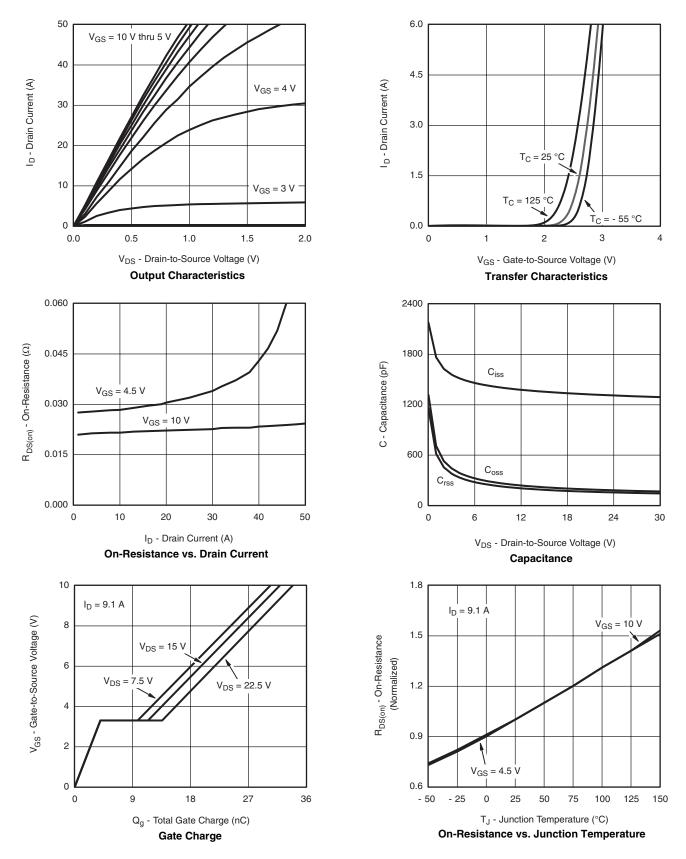
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

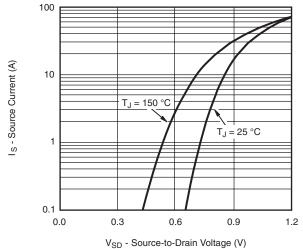


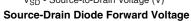
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

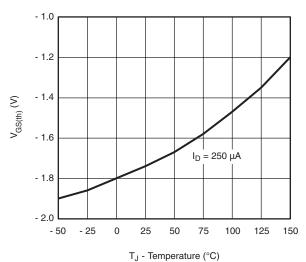




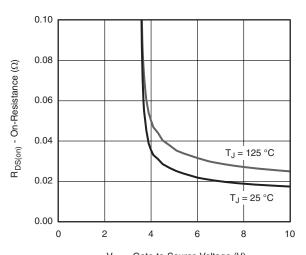
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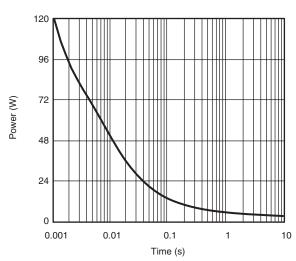


Threshold Voltage

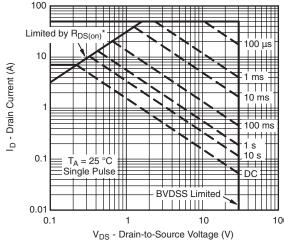


V_{GS} - Gate-to-Source Voltage (V)





Single Pulse Power, Junction-to-Ambient

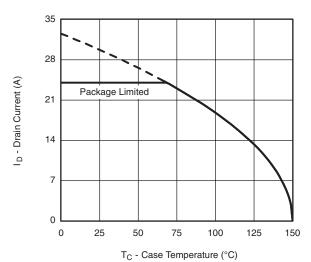


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

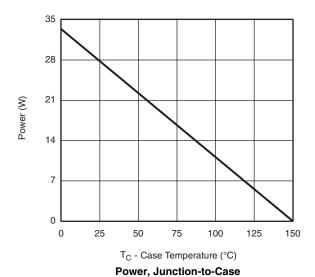
Safe Operating Area

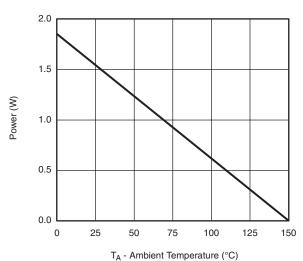


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*





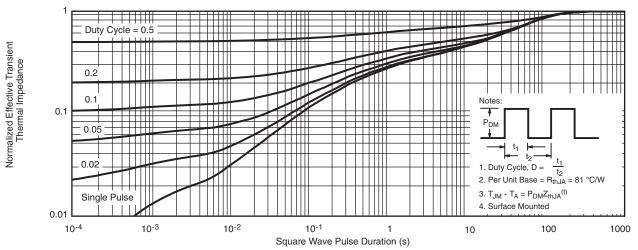
Power Derating, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

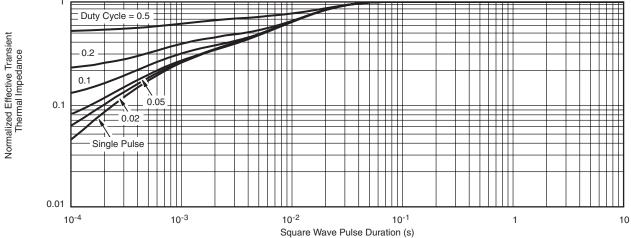
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



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