

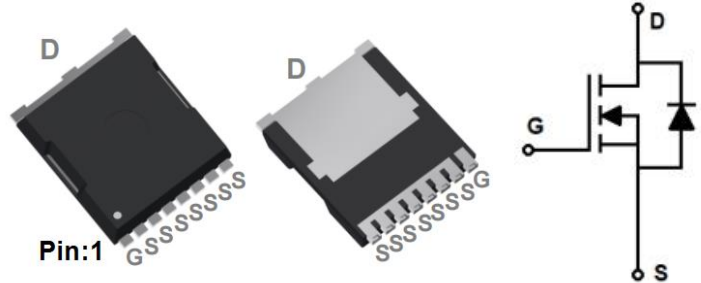
**Features**

- N-Channel, *Low  $R_{DS(on)}$  @  $V_{GS}=10V$*
- *10V Logic Level Control*
- *100% UIS Tested*
- *Green Device Available*

$V_{(BR)DSS}$	$R_{DS(ON)}$ Typ	$I_D$ Max
150V	6.0m $\Omega$ @ 10V	160A

**Applications**

- Quick Charger
- Load Switch
- LED backlighting
- Telecom
- Industrial power supplies


**Order Information**

Product	Package	Marking	Packing
DPL6R315H	TOLL	6R315H	2000PCS/Reel

**TOLL**
**Absolute Maximum Ratings**

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Symbol	Parameter	Rating	Unit
<b>Common Ratings (<math>T_j=25^\circ\text{C}</math> Unless Otherwise Noted)</b>			
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	150	V
$T_{J, STG}$	Junction and Storage Temperature Range	-50 to 150	$^\circ\text{C}$
<b>Mounted on Large Heat Sink</b>			
$I_{DM}$	Pulse Drain Current Tested <sup>①</sup>	$T_C=25^\circ\text{C}$	440 A
$I_S$	Diode continuous forward current	$T_C=25^\circ\text{C}$	160 A
$I_D$	Continuous Drain Current	$T_C=25^\circ\text{C}$	160 A
		$T_C=70^\circ\text{C}$	128 A
$P_D$	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	750 W
EAS	Avalanche energy, single pulsed <sup>②</sup>	1083.75	mJ
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.168	$^\circ\text{C/W}$

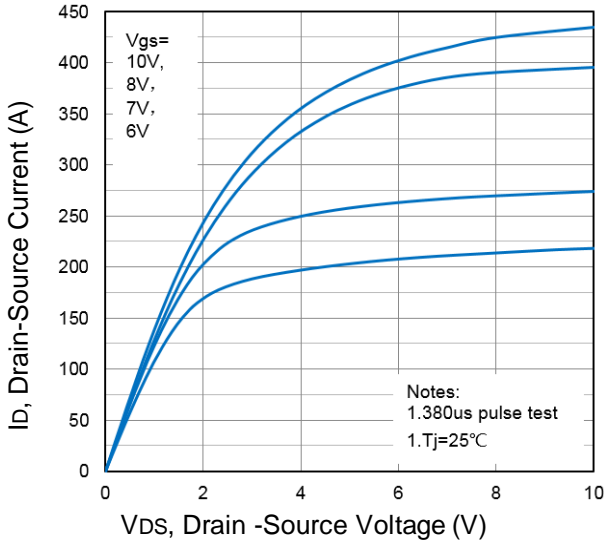
Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	150	–	–	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(T <sub>C</sub> =25°C)	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	–	–	1	μA
	Zero Gate Voltage Drain Current(T <sub>C</sub> =125°C)	V <sub>DS</sub> =120V, V <sub>GS</sub> =0V	–	–	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V	–	–	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>③</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	–	6.0	7.0	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =75V, V <sub>GS</sub> =0V, f=1MHz	–	5000	–	pF
C <sub>oss</sub>	Output Capacitance		–	595	–	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		–	9	–	pF
R <sub>g</sub>	Gate Resistance	f=1MHz	–	1.6	–	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =10V I <sub>D</sub> =50, V <sub>GS</sub> =10V	–	78.5	–	nC
Q <sub>gs</sub>	Gate Source Charge		–	25.3	–	nC
Q <sub>gd</sub>	Gate Drain Charge		–	20	–	nC
<b>Switching Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
t <sub>d(on)</sub>	Turn on Delay Time	V <sub>DD</sub> =10V, I <sub>D</sub> =1A, R <sub>G</sub> =6Ω, V <sub>GS</sub> =10V	–	22	–	ns
t <sub>r</sub>	Turn on Rise Time		–	25	–	ns
t <sub>d(off)</sub>	Turn Off Delay Time		–	35	–	ns
t <sub>f</sub>	Turn Off Fall Time		–	18	–	ns
<b>Source Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage <sup>③</sup>	I <sub>SD</sub> =20A, V <sub>GS</sub> =0V	–	–	1.2	V

Notes: ① Pulse width limited by maximum allowable junction temperature

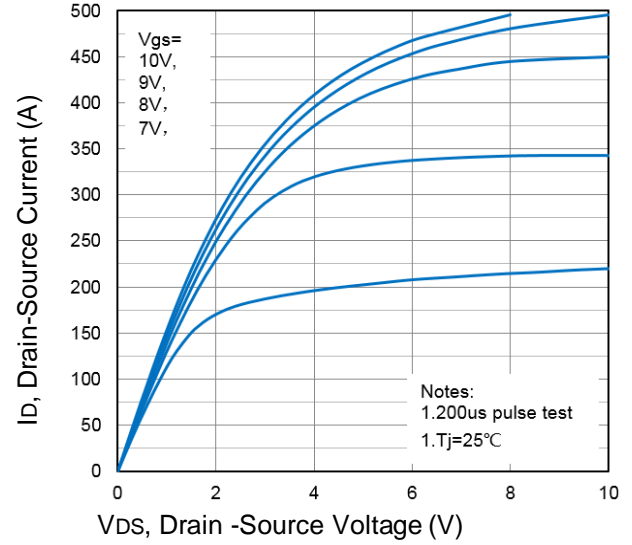
② Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.3mH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 87A, V<sub>GS</sub> = 10V. Part not recommended for use above this value

③ Pulse width ≤ 300μs; duty cycle ≤ 2%.

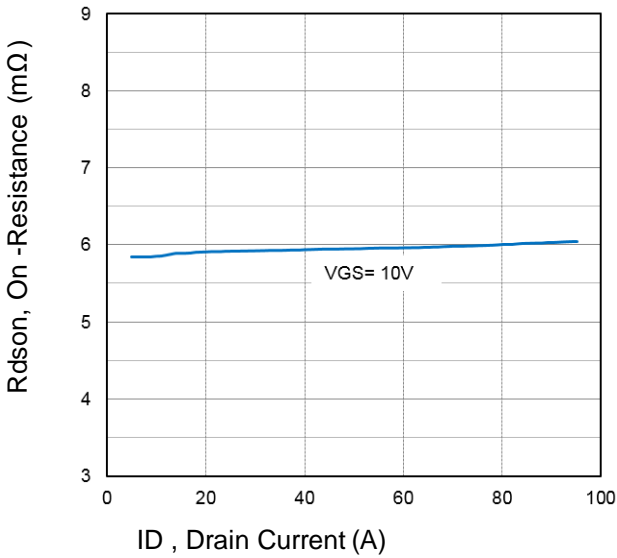
**Typical Characteristics**



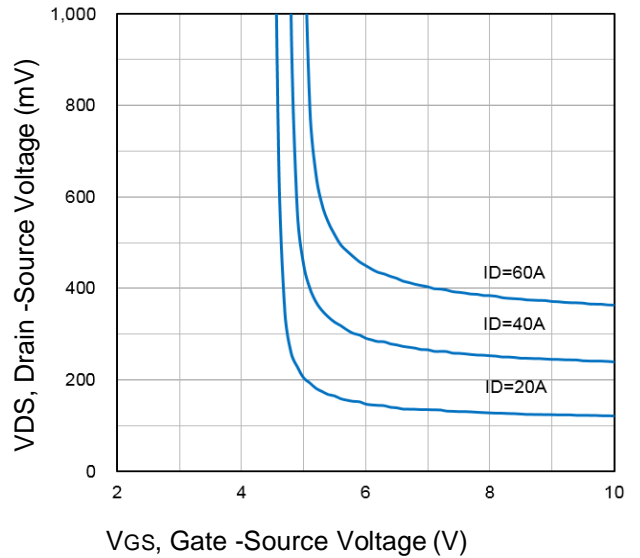
**Fig1.** Typical Output Characteristics



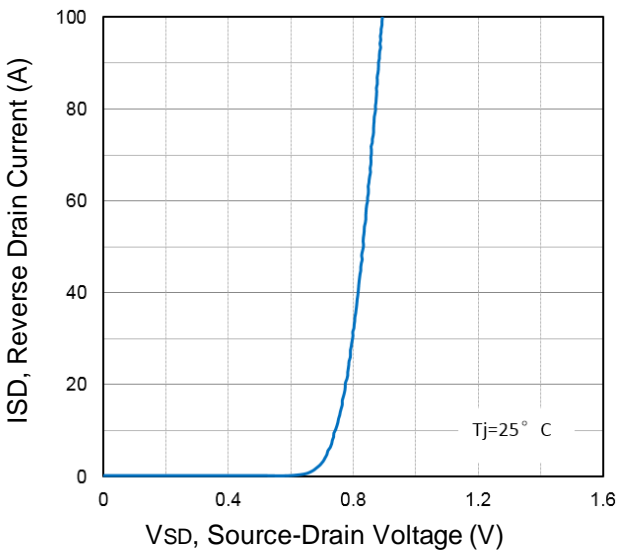
**Fig2.** Typical Output Characteristics



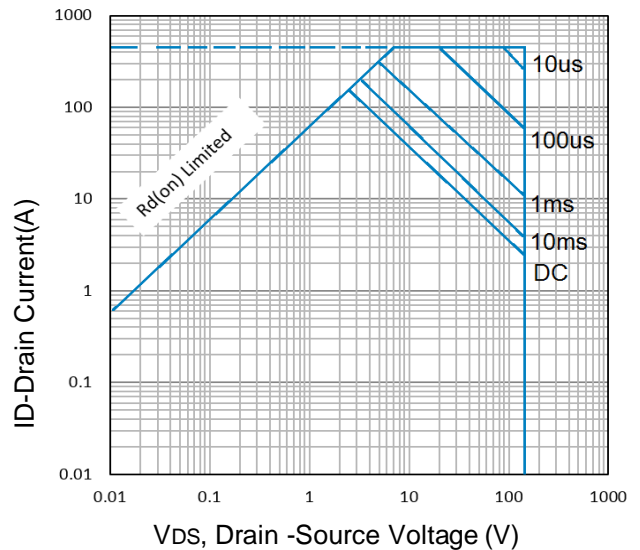
**Fig3.** On-Resistance vs. Drain Current



**Fig4.** Drain-Source Voltage vs Gate-Source Voltage

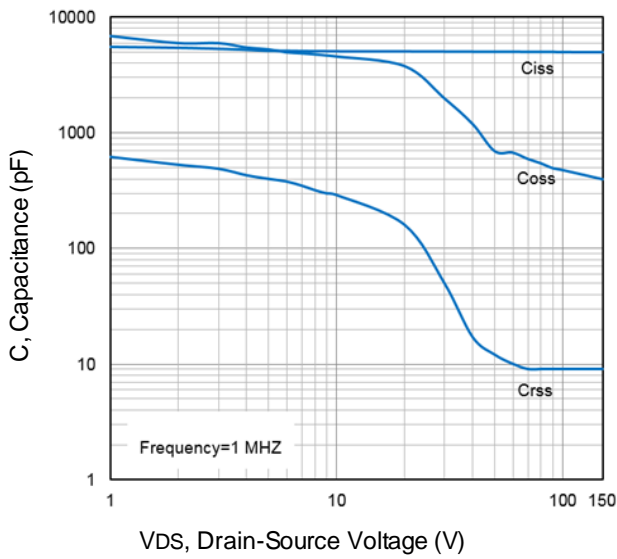


**Fig5.** Typical Source-Drain Diode Forward Voltage

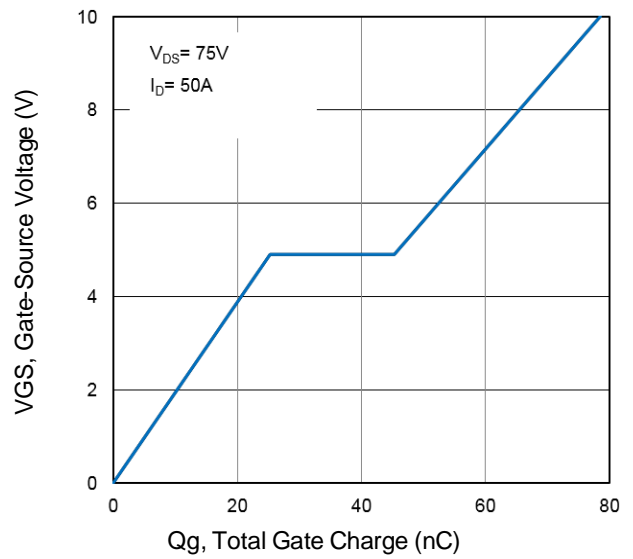


**Fig6.** Maximum Safe Operating Area

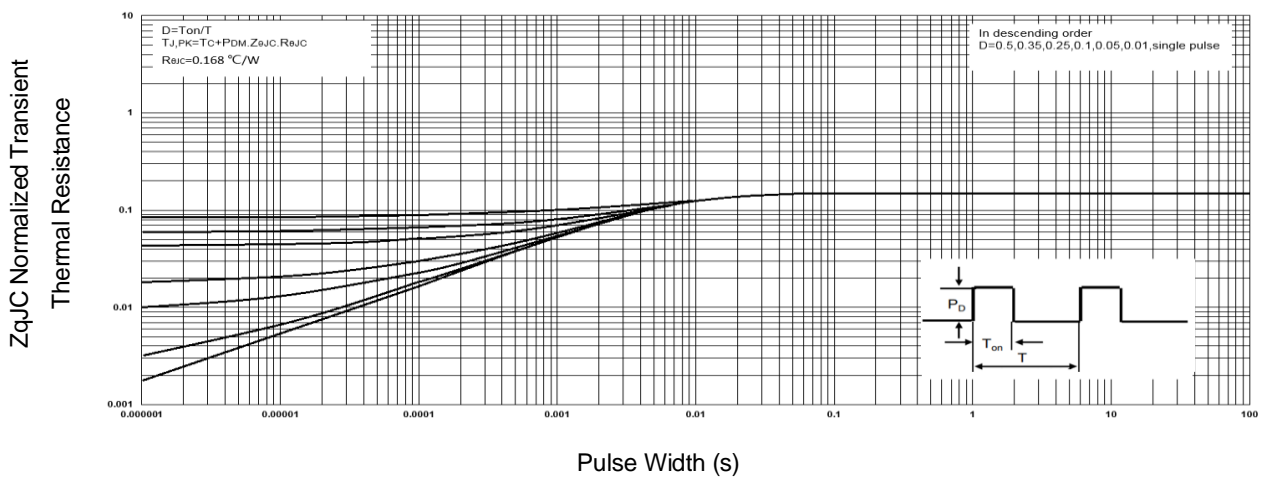
**Typical Characteristics**



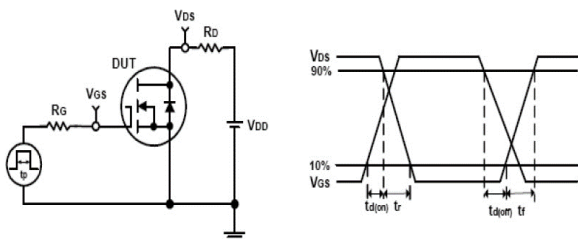
**Fig7.** Typical Capacitance Vs. Drain-Source Voltage



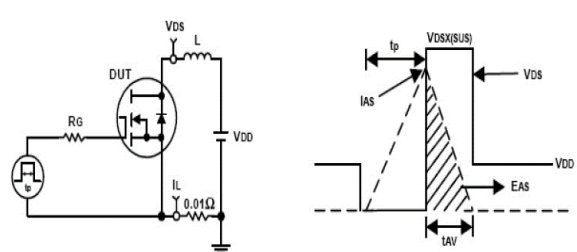
**Fig8.** Typical Gate Charge Vs. Gate-Source Voltage



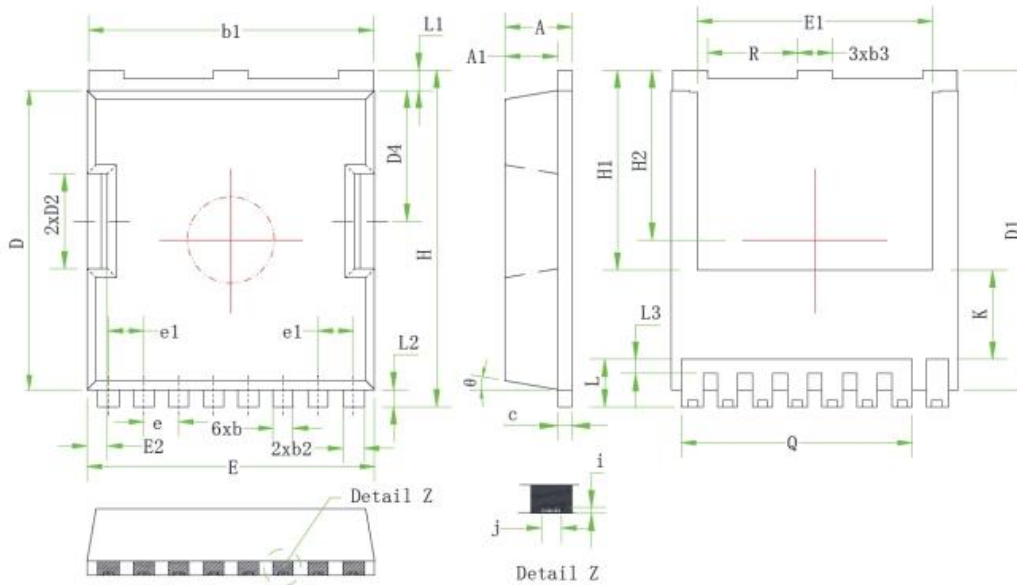
**Fig9.** Normalized Maximum Transient Thermal Impedance



**Fig10.** Switching Time Test Circuit and waveforms



**Fig11.** Unclamped Inductive Test Circuit and waveform

**TOLL Mechanical Data**


Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.25	2.30	2.35	E2	0.65	0.70	0.75
A1	1.75	1.80	1.85	H	11.60	11.70	11.80
b	0.65	0.70	0.75	H1	6.95 BSC		
$b_1$	9.75	9.80	9.85	H2	5.90 BSC		
$b_2$	0.70	0.75	0.80	i	0.10 REF		
$b_3$	1.15	1.20	1.25	j	0.35 REF		
c	0.45	0.50	0.55	K	3.10 REF		
D	10.35	10.40	10.45	L	1.55	1.65	1.75
$D_1$	11.00	11.10	11.20	$L_1$	0.65	0.70	0.75
$D_2$	3.25	3.30	3.35	$L_2$	0.50	0.60	0.70
$D_4$	4.50	4.55	4.60	$L_3$	0.40	0.50	0.60
e	1.20 BSC			Q	7.95 REF		
$e_1$	1.225 BSC			R	3.05	3.10	3.15
E	9.85	9.90	9.95	$\theta$	10°REF		
$E_1$	8.00	8.10	8.20				

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