

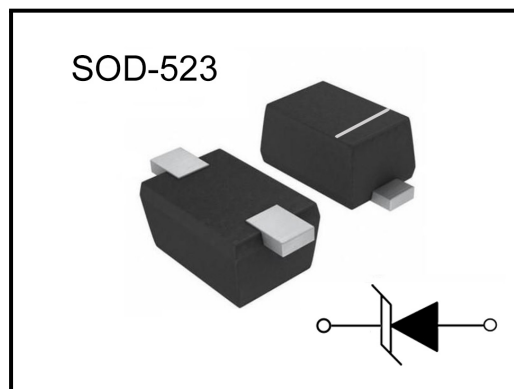
ESD5Z3.3T1G-N

ESD Protection Diode

Features

- 150Watts peak pulse power ($t_p = 8/20\mu s$)
- Unidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping Voltage
- Low leakage current
- IEC 61000-4-2 $\pm 30kV$ contact ; $\pm 30kV$ air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 15A (8/20 μs)

Package



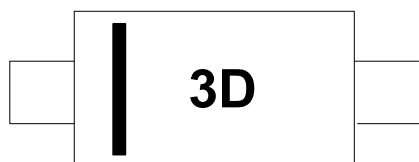
Applications

- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Pagers Peripherals

Mechanical Characteristics

- SOD523 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

Marking



Ordering information

Order code	Package	Base qty	Delivery mode
ESD5Z3.3T1G-N	SOD-523	3k	Tape and reel

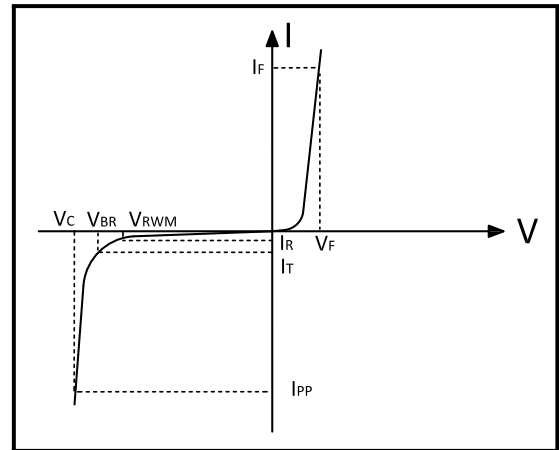


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ESD Protection Diode

Electrical Parameters ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current



Note: 8/20us pulse Waveform.

Absolute Maximum Rating

Rating	Symler	Value	Units
Peak Pulse Power ($t_p = 8/20\mu\text{s}$)	P_{PP}	150	Watts
Peak Pulse Current ($t_p = 8/20\mu\text{s}$)	I_{PP}	15	A
ESD per IEC 61000-4-2 (Air)	V_{ESD}	30	KV
ESD per IEC 61000-4-2 (Contact)		30	
Lead Soldering Temperature	T_L	260(10seconds)	$^\circ\text{C}$
Junction Temperature	T_J	-55 to + 150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to + 150	$^\circ\text{C}$

Electrical Characteristics

Parameter	Symler	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}	—	—	—	3.3	V
Reverse Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	3.5	—	—	V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3\text{V}, T = 25^\circ\text{C}$	—	—	0.2	μA
Clamping Voltage	V_C	$I_{PP} = 15\text{A}, t_p = 8/20\mu\text{s}$	—	10	—	V
Junction Capacitance	C_j	$V_R = 0\text{V}, f = 1\text{MHz}$	—	20	—	pF



Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

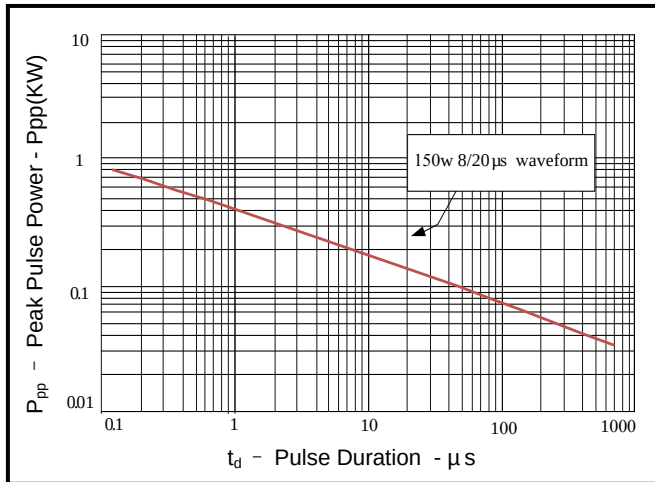


Figure 2: Power Derating Curve

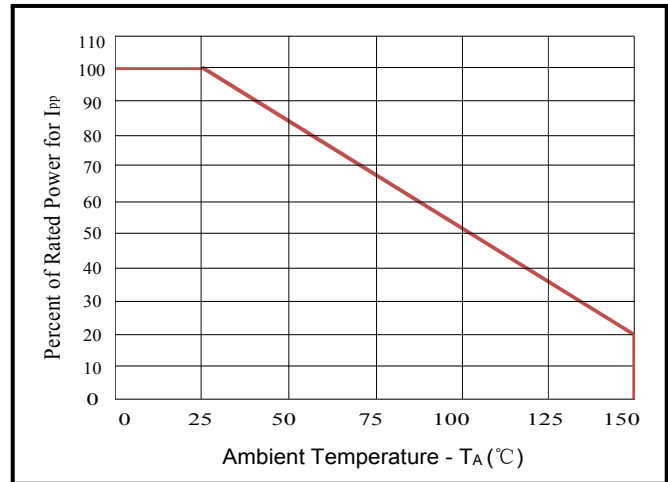


Figure 3: Pulse Waveform

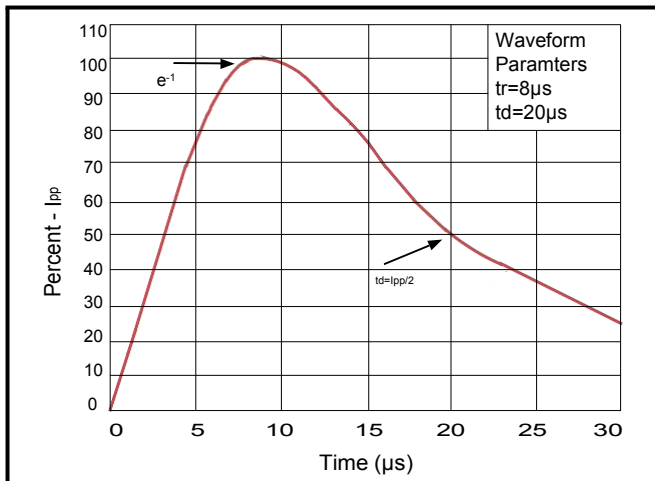
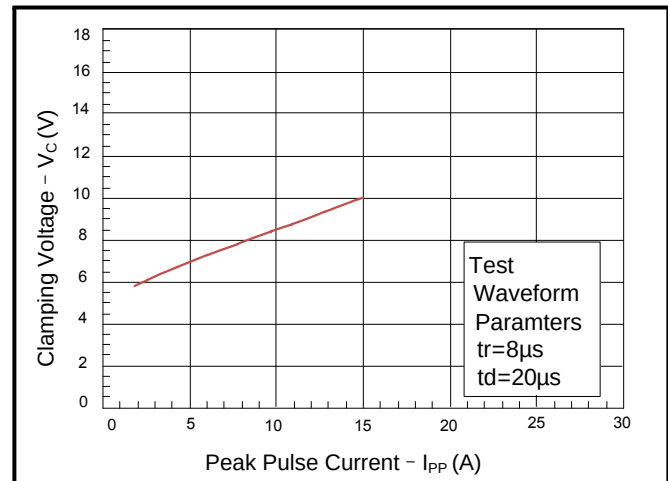
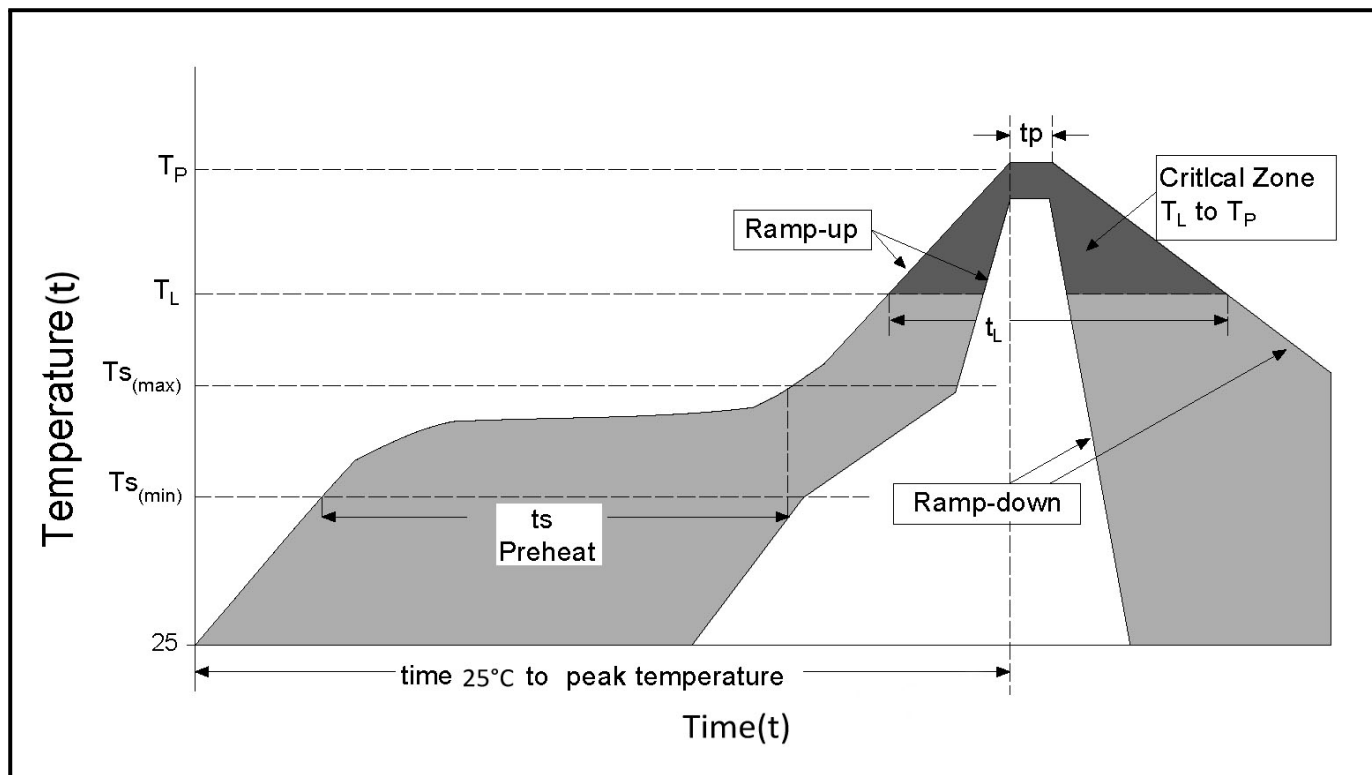


Figure 4: Clamping Voltage vs. Ipp



Soldering Parameters



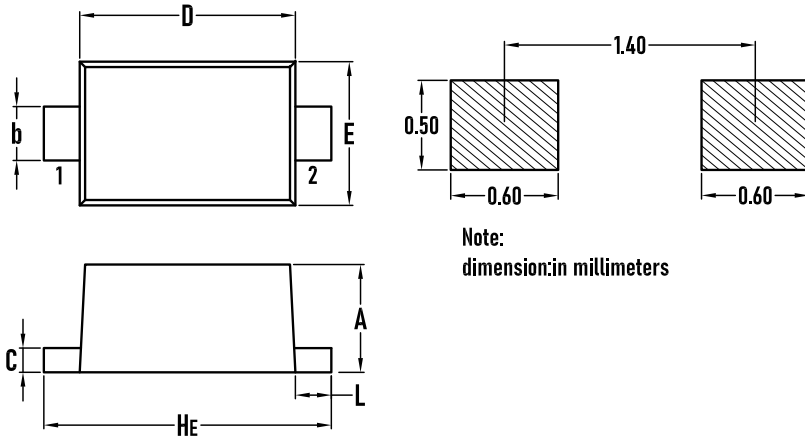
Reflow Condition		Lead-free assembly
Pre Heat	- Temperature Min ($T_{S(min)}$)	150°C
	- Temperature Max ($T_{S(max)}$)	200°C
	- Time (min to max) (t_s)	60 - 180 secs
Average ramp up rate (Liquidus Temp (T_L) to peak)		3°C/second max
$T_{S(max)}$ to T_L - Ramp-up Rate		3°C/second max
Reflow	- Temperature (T_L) (Liquidus)	217°C
	- Time (t_L)	60 - 150 secs
Peak Temperature (T_P)		260 ^{+0/-5} °C
Time within 5°C of actual peak Temperature (t_p)		20 - 40 secs
Ramp-down Rate		6°C/second max
Time 25°C to peak Temperature (t)		8 minutes Max.
Do not exceed		260°C



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ESD Protection Diode

Outline Drawing – SOD-523



SYMBOL	MILLIMETER		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.50	0.70	0.020	0.028
b	0.25	0.35	0.010	0.014
C	0.07	0.20	0.0028	0.0079
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
H _E	1.50	1.70	0.059	0.067
L	0.15	0.25	0.006	0.010

