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April 1st, 2010 Rene<mark>sas E</mark>lectronics Corporation

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HA17904 Series

Dual Operational Amplifier

REJ03D0688-0100

(Previous: ADE-204-046)

Rev.1.00 Jun 15, 2005

Description

HA17904 is dual operational amplifier which, provide internal phase compensation and high gain, and mono power source operation is possible. It can be widely applied to control equipment and to general use.

Features

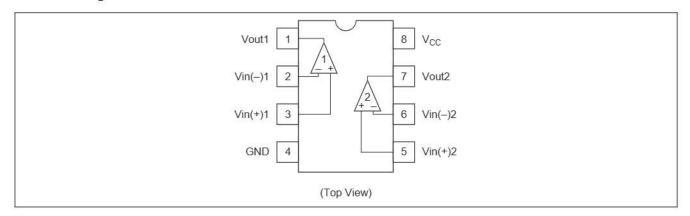
- · Wide range of operating supply voltage and mono power source operation is possible.
- Wide range of common mode input voltage possible to operate with an input around 0V, and output around 0V is available.
- · Frequency characteristics and input bias currrent are temperature compensated.

Ordering Information

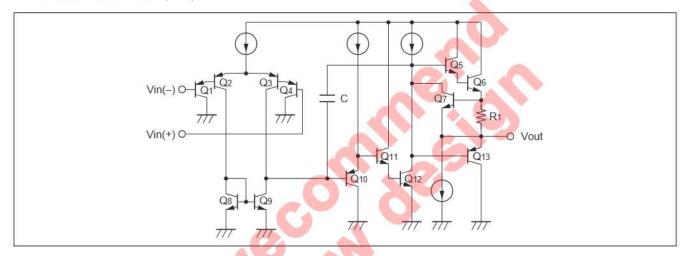
Type No.	Application	Package Code (Previous Code)					
HA17904PSJ	Car use	PRDP0008AF-A (DP-8B)					
HA17904FPJ		PRSP0008DE-B (FP-8DGV)					
HA17904FPK		PRSP0008DE-B (FP-8DGV)					



Pin Arrangement



Circuit Schematic (1/2)



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

		Ratings					
Item	Symbol	HA17904PSJ	HA17904FPJ	HA17904FPK	Unit		
Supply voltage	V _{cc}	32	32	32	V		
Output sink current	I _{O sink}	50	50	50	mA		
Common-mode input voltage	V _{CM}	-0.3 to V _{CC}	-0.3 to V _{CC}	-0.3 to V _{CC}	V		
Common-mode differential voltage	V IN(diff)	±V _{CC}	±V _{CC}	±V _{CC}	V		
Power dissipation	PT	570* ¹	385* ²	385* ²	mW		
Operating temperature range	Topr	-40 to +85	-40 to +85	-40 to +125	°C		
Storage temperature range	Tstg	-55 to +125	-55 to +125	-55 to +150	°C		

Notes: 1. These are the allowable values up to Ta = 55 °C. Derate by 8.3mW/°C above that temperature.

2. These are the allowable values up to Ta = 45 °C mounting on 30% wiring density glass epoxy board. Derate by 7.14mW/°C above that temperature.

Electrical Characteristics 1

 $(V_{CC} = +15V, Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input offset voltage	V _{IO}	1-	3	7	mV	$V_{CM} = 7.5V$, $R_S = 50\Omega$, $Rf = 50k\Omega$
Input offset current	lio		5	50	nA	$V_{CM} = 7.5V$, $I_{IO} = I_{I(+)} - I_{I(-)} $
Input bias current	I _{IB}	-	30	250	nA	V _{CM} = 7.5V
Power source rejection ratio	PSRR		93	914	dB	$R_S = 1k\Omega$, $Rf = 100k\Omega$
Voltage gain	A _{VD}	75	90	· —	dB	$R_L = \infty$, $R_S = 1k\Omega$, $Rf = 100k\Omega$
Common mode rejection ratio	CMR		80	A= 0	dB	$R_S = 50\Omega$, $Rf = 5k\Omega$
Common mode input voltage range	V _{CM (+)}	13.5	Y	276	V	$R_S = 1k\Omega$, $Rf = 100k\Omega$
	V _{CM (-)}		/ _	-0.3	V	$R_S = 1k\Omega$, $Rf = 100k\Omega$
Peak-to-peak output voltage	Vop-p	7	13.6		V	f = 100Hz, R_L = 20k Ω , R_S = 1k Ω , R_S = 100k Ω
Output source current	losource	20	40	-	mA	$V_{IN}^{+} = 1V, V_{IN}^{-} = 0V, V_{OH} = 10V$
Output sink current	losink	10	20	1 <u>2 - 12</u>	mA	$V_{IN}^- = 1V, V_{IN}^+ = 0V, V_{OL} = 2.5V$
Output sink current	losink	15	50	9 	μА	$V_{IN}^- = 1V, V_{IN}^+ = 0V,$ Vout = 200mV
Supply current	Icc_	V _	0.8	2	mA	V _{IN} = GND, R _L = ∞
Slew rate	SR		0.2	1	V/μs	$R_L = \infty$, $V_{CM} = 7.5V$, $f = 1.5kHz$
Channel separation	CS		120	=	dB	f = 1kHz

Electrical Characteristics 2

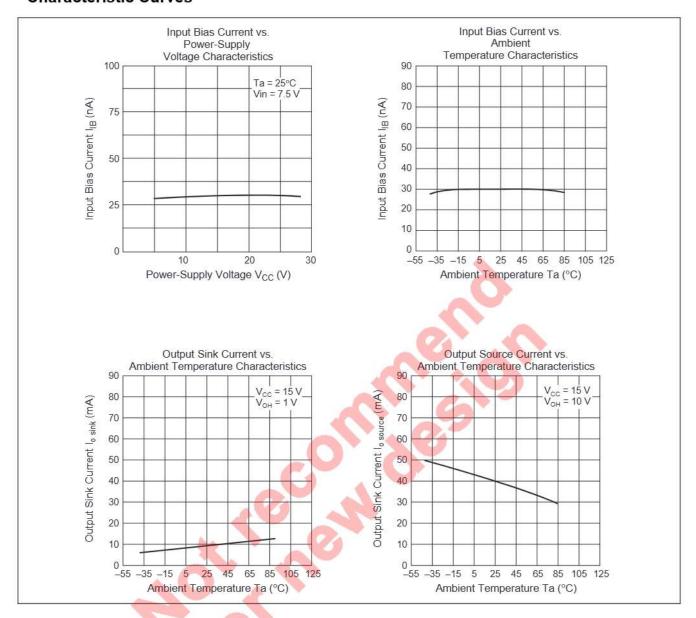
 $(V_{CC} = +15V, Ta = -40 \text{ to } +125^{\circ}C)$

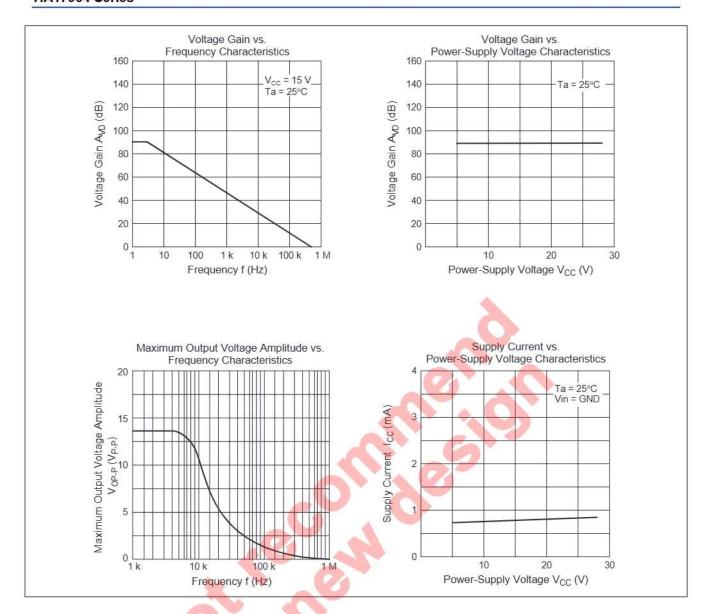
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input offset voltage	V _{IO}		_	7	mV	V_{CM} = 7.5V, R_S = 50 Ω , R_L = 50k Ω
Input offset current	I _{IO}	2-1 3	g2 58	200	nA	$V_{CM} = 7.5V$, $I_{IO} = [I_{I(+)} - I_{I(-)}]$
Input bias current	I _{IB}	-	-	500	nA	V _{CM} = 7.5V
Common mode input voltage range	V _{CM}	0	33 —- 33	13.0	V	$R_S = 1k\Omega$, $Rf = 100k\Omega$
Supply current	Icc	 .	-	4	mA	V _{IN} = GND, R _L = ∞

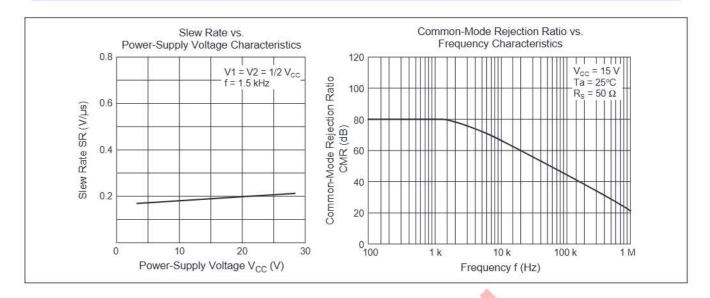
Note: As for the characteristic curve, refer to HA17904FPK.



Characteristic Curves

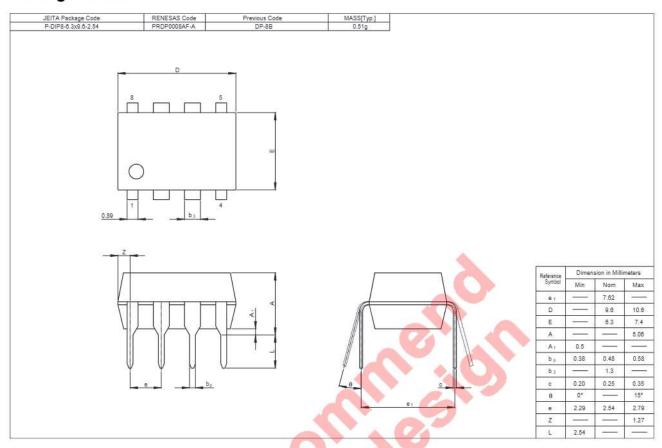


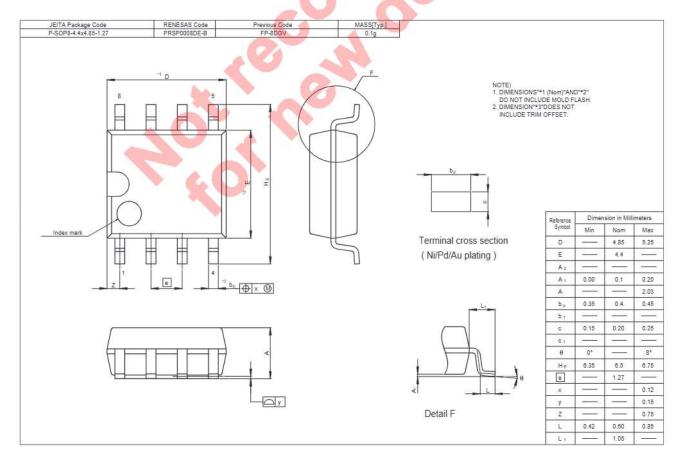






Package Dimensions





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