Features

¥JY€♀I∩ 益嘉源

- Assemblage design, sturdy structure.
- High inductance, high current, low magnetic loss, low ESR, small parasitic capacitance.
- Flat wire winding, achieve alow D.C.Resistance.
- Temperature rise current and saturation current is less influenced by environment.
- Operating temperature range:-55°C ~ +125°C.

Applications

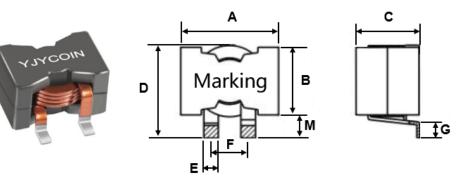
- Low profile, high current power supplies.
- Battery powered devices.
- DC/DC converters in distributed power systems.
- DC/DC converters for field programmable gate array.

Product Identification

YSFP			
(1)	(2)	(3)	(4)

- (1) : Type
- (2): Dimensions
- (3): Inductance value
- (4) : Inductance Tolerance: M=±20%,K=±10%,J=±5%

Shapes and Dimensions (Unit: mm)



TYPE	A Max.	B Max.	C Max.	D Max.	E	F	G
YSFP2920S	28.0	19.7	21.0	28.5	4.0±0.5	10.0±1.0	4.5±0.5

深圳市益嘉源电子有限公司

http://www.yjycoin.com

YJY€№I∩ 益嘉源

Electrical requirements

Part Number	L (uH)	Test Freq.	DCR Max.(m Ω)	l sat (A)	l rms (A)
YSFP2920S-330M	33±20%	100KHz/0.25V	2.5	11	33

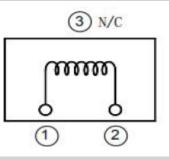
 $\%\,$ All test data is based on 25 $\,\,{}^\circ\!{}^\circ\!{}^\circ$ ambient.

 $\%\,$ DC current(A) that will cause an approximate $\Delta T40\,^\circ\! \mathbb{C}\,.$

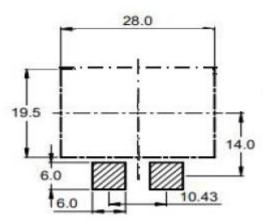
% DC current(A) that will cause L0 to drop approximately 30% Typ.

* The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design,component.PWB trace size and thickness,airflow and other cooling provision all affect the part temperature.Part temperature should be verified in the den application.

Electrical schematics

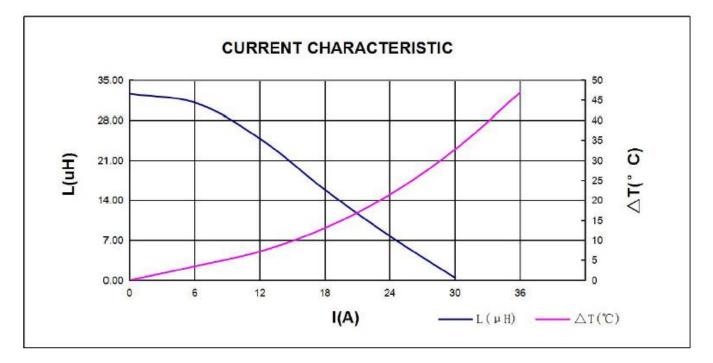


Recommended PCB Layout



Saturation current VS temperature rise current curve

YJYSAIA 益嘉源



¥JY€№I∩ 益嘉源

High Current Power Inductor

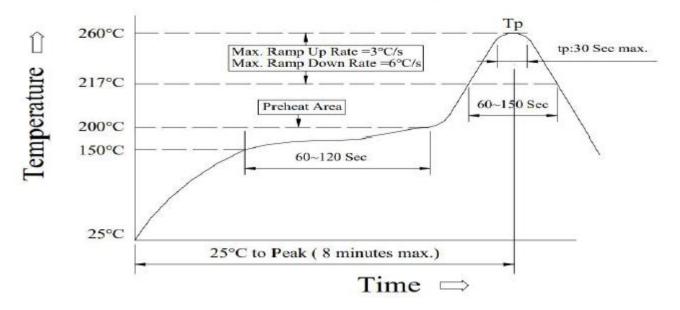
Reliability

Item	Specification and Requirement	Test Method
		Solder heat proof:
Solder a bility test	Terminals area must have 95% min solder	$$ ①Preheating:160±10 \degree C for 90 seconds
	coverage	②Retention time:245±5℃ for 2±0.5 seconds
		① Vibration frequency:(10Hz to 55Hz to
		10Hz) in 60 seconds as a period
Vibration test	Inductance change:Within±5% Without	② Vibration time: Reriod cycled for 2 hours
	Mechanical damage such as break	in each of 3 mutual perpendicular directions.
		③ Amplitude:1.5mm Max.
		① Peak value:100G.
	Inductance change: Within±5% Without	② Duration of pulse:11ms.
Shock test	Mechanical damage such as break	③ Times in each positive and negative
		direction of 3 mutual perpendicular directions
		① Repeat 100 cycle as follow (-55±2℃
		30±3 minutes),Room temperature,5 minutes
-	Inductance change: Within±5% Without	(+125±2℃,30±3 minutes)
Thermal shock	Mechanical damage such as break	② Recovery:48+4/-0 hours of recovery
		Under the standard condition after the test.
		(see Note 1)
		① Environment condition:85±2°C
High temperature	Inductance change: Within±5% Without	Applied current:Rated current
life test	Mechanical damage such as break	② Duration:1000+4/-0 hours(see Note 1)
		① Environment condition:60±2°C
Humidity	Inductance change: Within±5% Without	Humidity:90-95%
Resistance	Mechanical damage such as break	Applied current:Rated current
		② Duration:1000+4/-0 hours(see Note 1)
Low temperature	Inductance change: Within±5% Without	Store temperature -55 $\pm\pm2^\circ\!\!\mathbb{C}$ for total
life test	Mechanical damage such as break	1000+4/-0 hours
High temperature	Inductance change: Within±5% Without	Store temperature +125±2°Cfor total
life test	Mechanical damage such as break	1000+4/-0 hours



Reflow Profile

Power Choke Coil Type



Reflow Soldering Method

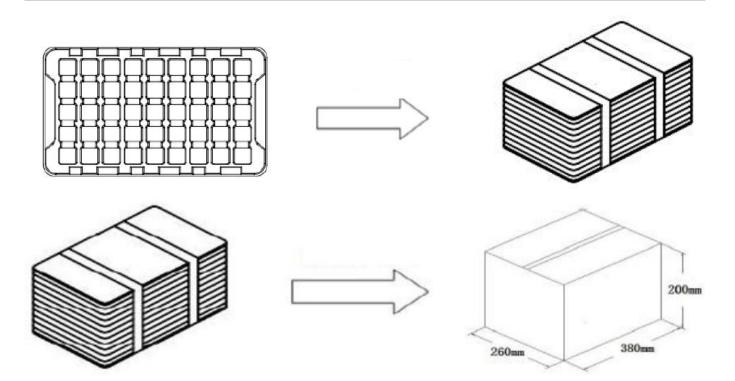
Poflow Soldoring	Tp:255 ~ 260 ℃ Max. 30 seconds(tp)		
Reflow Soldering	217℃ 60 ~ 150 seconds		
Pre-Heat	150 ~ 200℃ 60 ~ 150 seconds		
Time 25 $^\circ\!\!\!\!\!^\circ$ to peak temperature	8 minutes Max.		

Soldering iron method

 $350\pm5^{\circ}$ °C Max.3 seconds.

High Current Power Inductor





Product Series	Quantity/Tray	Quantity/Carton
YSFP2920S	40 PCS	280 PCS