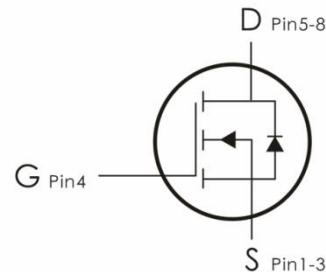
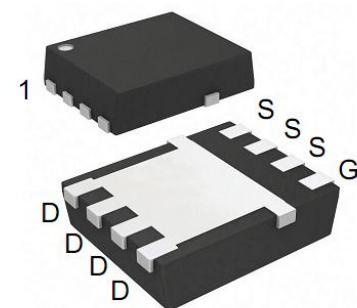


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=30V, I_D=60A, R_{DS(on)}<7 m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DON60N03	60N03	DFN5*6-8	5000 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	60	A
	Continuous Drain Current- $T_C=100^\circ C$	38	
I_{DM}	Pulsed Drain Current ¹	200	
P_D	Power Dissipation	24	W
E_{AS}	Single pulse avalanche energy ²	49	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	°C

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	5.2	°C/W
R_{eJA}	Thermal Resistance,Junction to Ambient ³	39	°C/W

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250 \mu\text{A}$	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=30\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics <small>(Note3)</small>						
$V_{\text{GS(th)}}$	Gate-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_D=250 \mu\text{A}$	1.0	1.8	2.5	V
$R_{\text{DS(ON)}}$	Drain-Source On Resistance ⁴	$V_{\text{GS}}=10\text{V}, I_D=20\text{A}$	---	5.5	7	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=15\text{A}$	---	8.5	11	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1115	---	pF
C_{oss}	Output Capacitance		---	170.1	--	
C_{rss}	Reverse Transfer Capacitance		---	136.5	---	
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}, I_D=15\text{A}, R_G=3 \Omega, V_{\text{GS}}=10\text{V}$	---	7.35	---	ns
t_r	Rise Time		---	15.7	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		---	26.2	---	ns
t_f	Fall Time		---	6.3	---	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_D=20\text{A}$	---	24.1	---	nc
Q_{gs}	Gate-Source Charge		---	4.7	---	nc
Q_{gd}	Gate-Drain "Miller" Charge		---	5.7	---	nc
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{SD}}=30\text{A}$	---	---	1.2	V
I_s	Continuous Drain Current	$V_D=V_G=0\text{V}$	---	---	60	A
I_{SM}	Pulsed Drain Current		---	---	200	A
Tr	Reverse Recovery Time	$I_F=20\text{A}, T_J=25^\circ\text{C}$ $dI/dt=100\text{A}/\mu\text{s}$	---	10	---	ns
Q_{rr}	Reverse Recovery Charge		---	3	---	nc

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25°C, V_{DD}=15V, V_G=10V, R_G=25ohm, L=0.5mH, I_{AS}=14A
3. R_{θJA} is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

Typical Characteristics: (T_C=25°C unless otherwise noted)

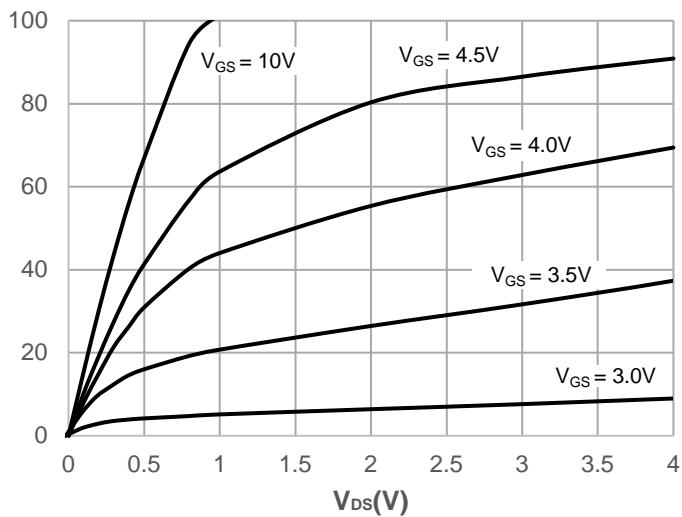


Figure 1: Output Characteristics

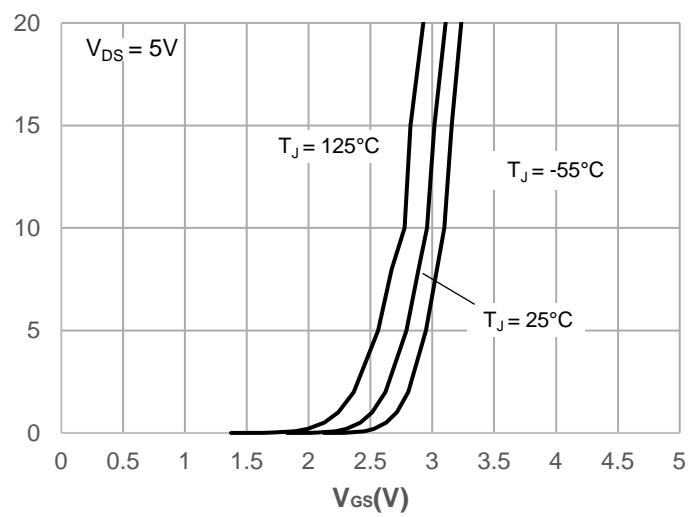


Figure 2: Typical Transfer Characteristics

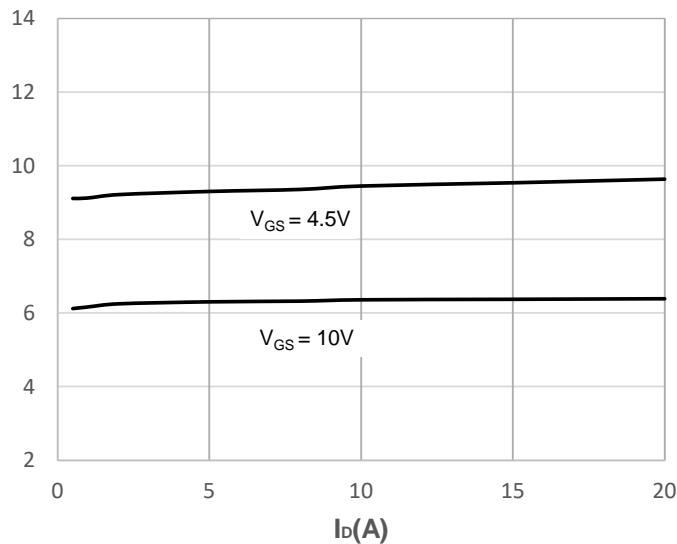


Figure 3: On-resistance vs. Drain Current

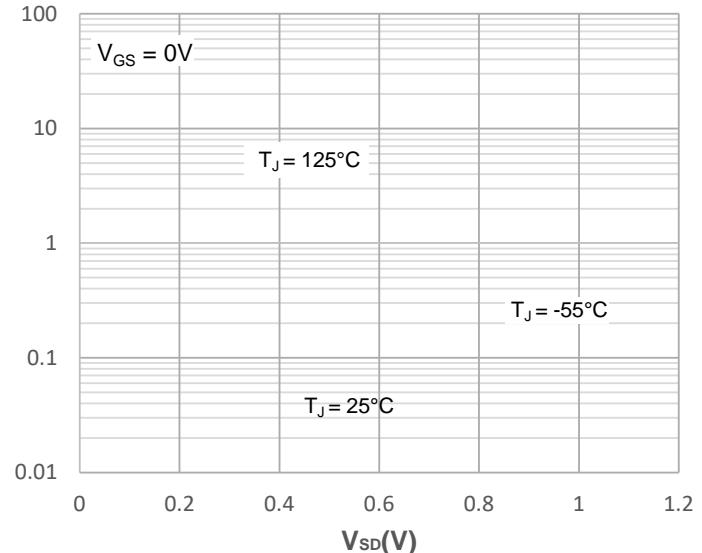


Figure 4: Body Diode Characteristics

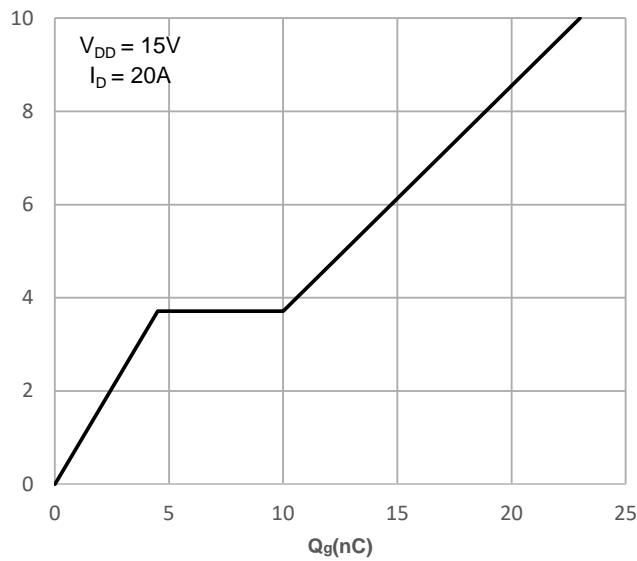


Figure 5: Gate Charge Characteristics

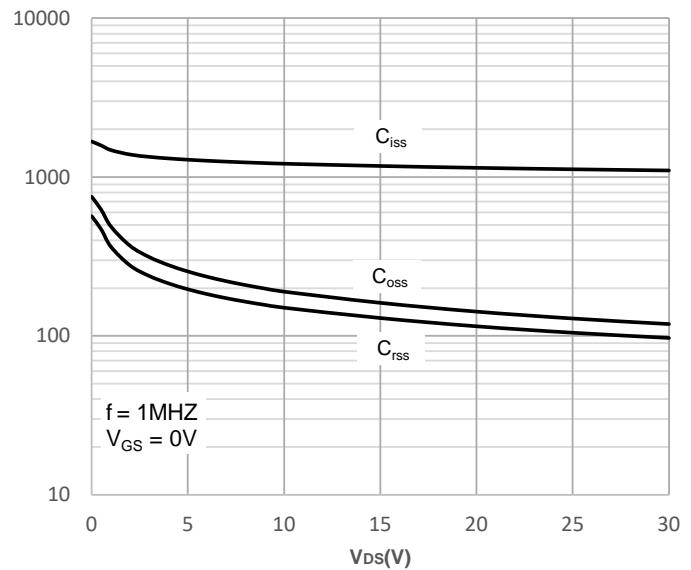


Figure 6: Capacitance Characteristics

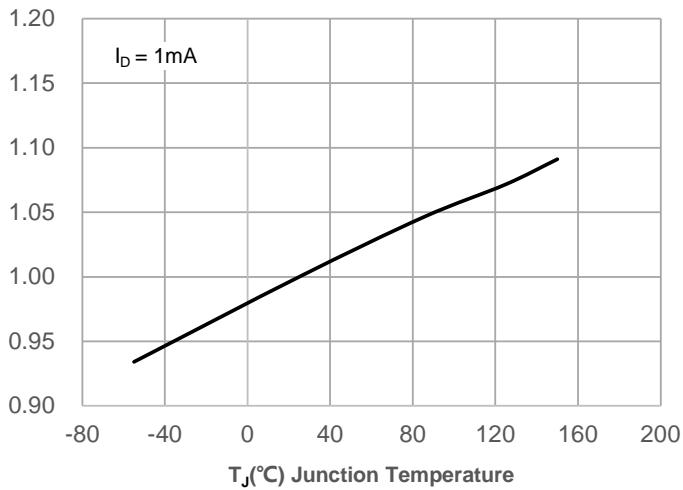


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

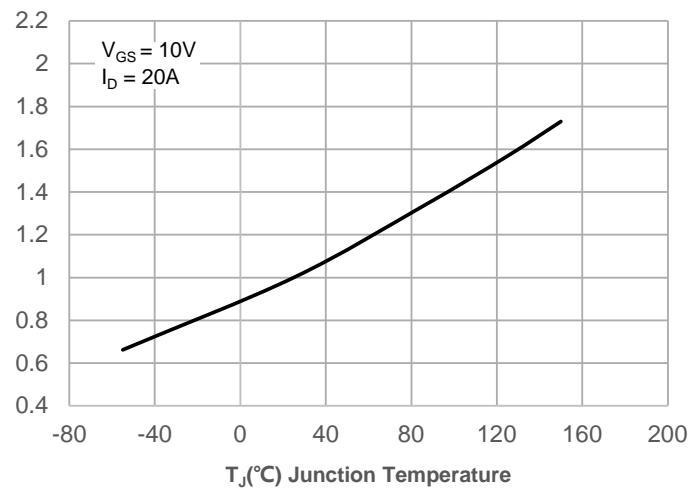


Figure 8: Normalized on Resistance vs. Junction Temperature

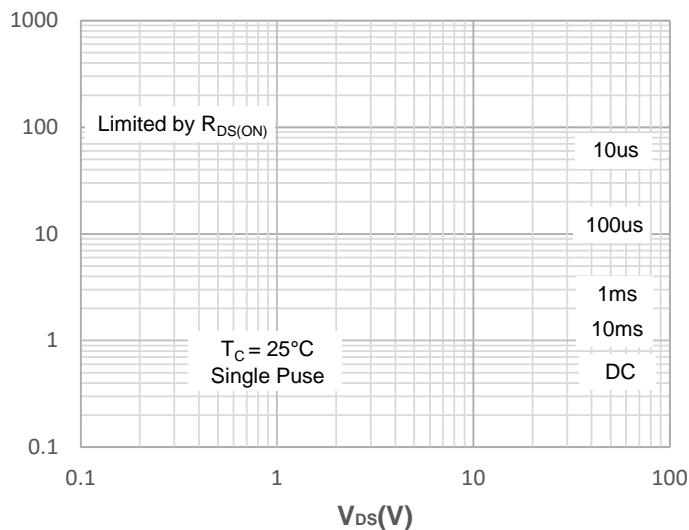


Figure 9: Maximum Safe Operating Area

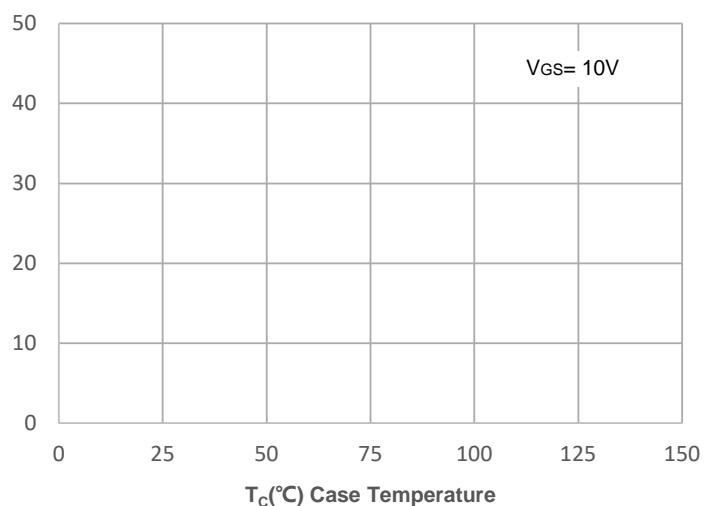


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

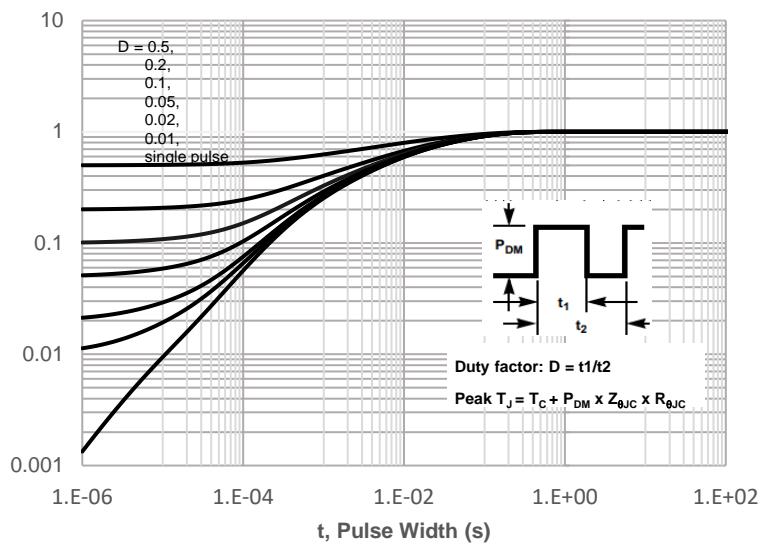


Figure 11: Normalized Maximum Transient Thermal Impedance

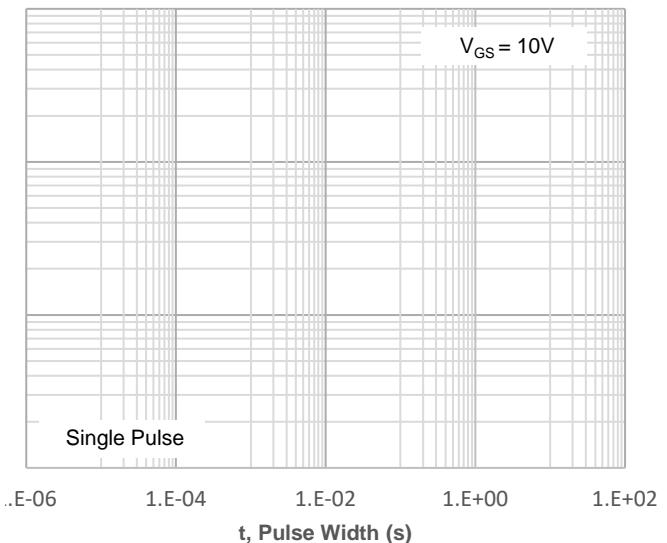
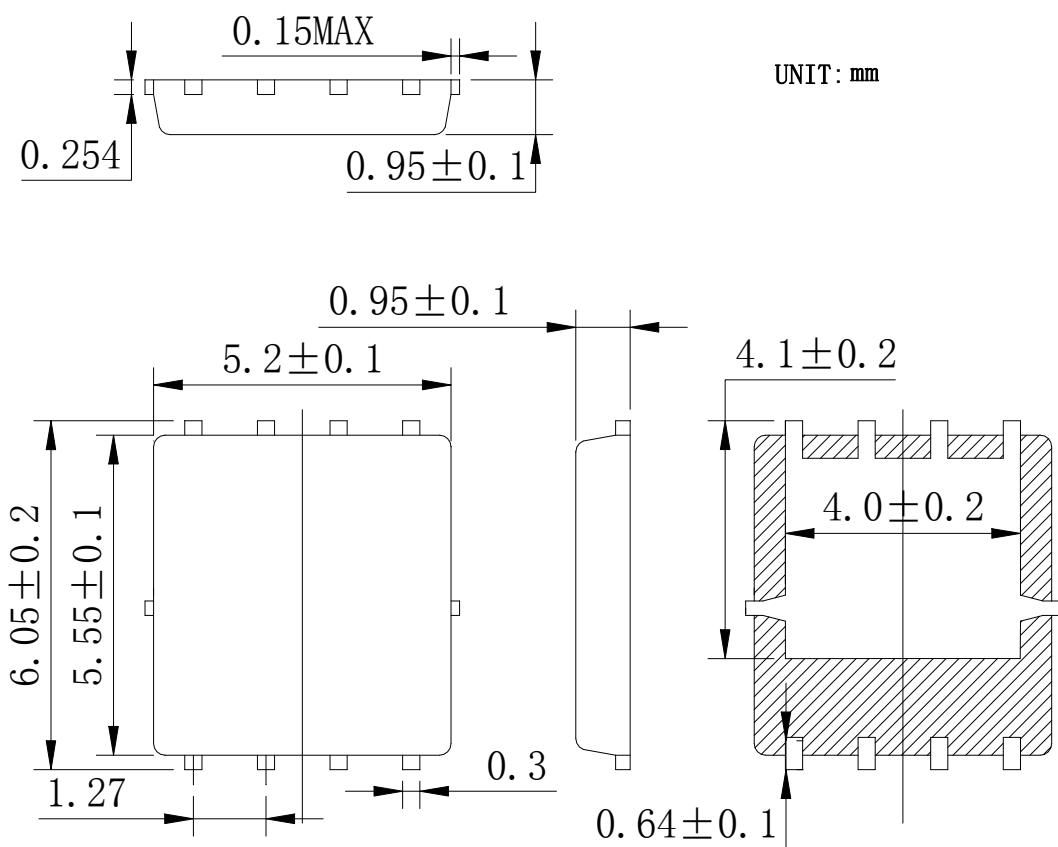


Figure 12: Peak Current Capacity

DFN5x6-8 Package Information:



Marking Information:

①. Doingter LOGO

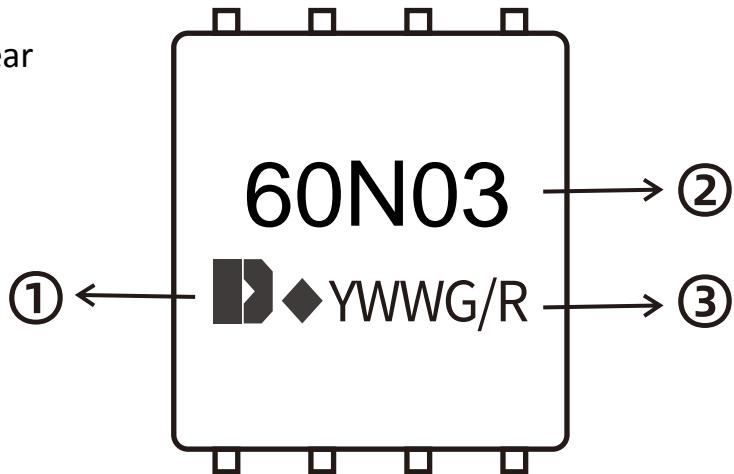
②. Part NO.

③. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
2.0	2024-06-08	Release of final version

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