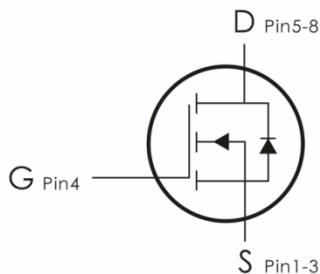
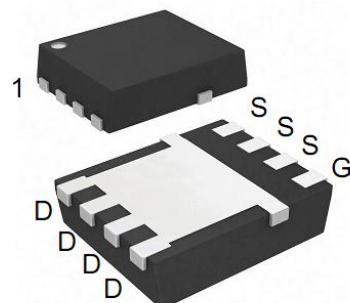


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.



Features:

- 1) $V_{DS}=40V, I_D=70A, R_{DS(ON)}<8.5m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.

Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DON70N04	70N04	DFN5*6-8	5000 pcs/Reel

Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_C=25^\circ C$	70	A
	Continuous Drain Current- $T_C=100^\circ C$	44	
	Pulsed Drain Current	280	
E_{AS}	Single Pulse Avalanche Energy	76	mJ
P_D	Power Dissipation	72.3	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

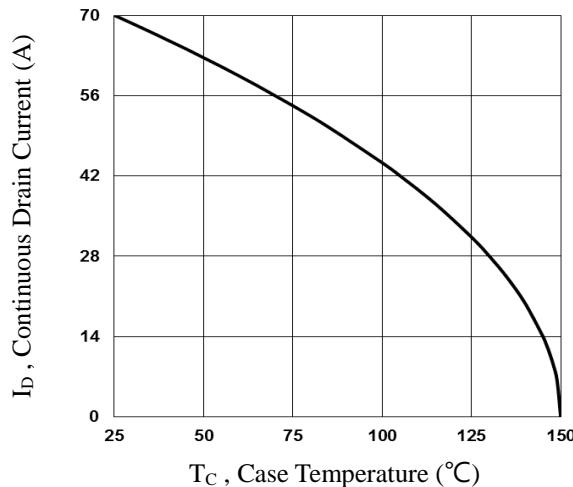
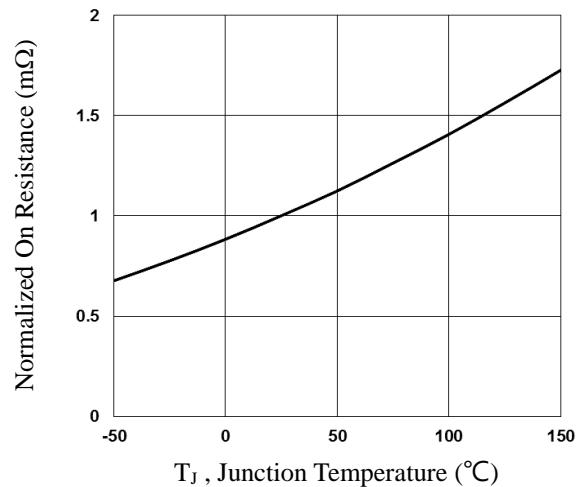
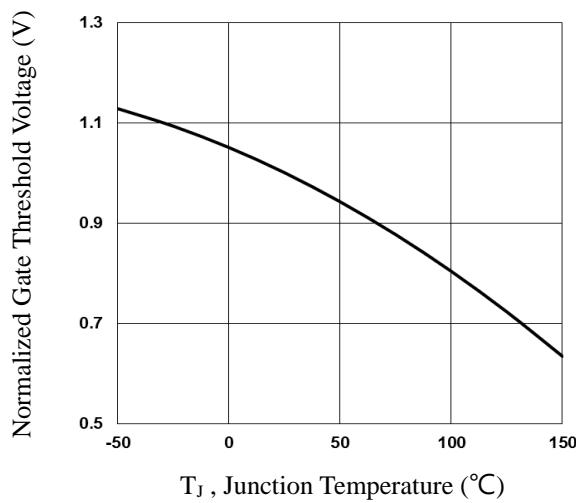
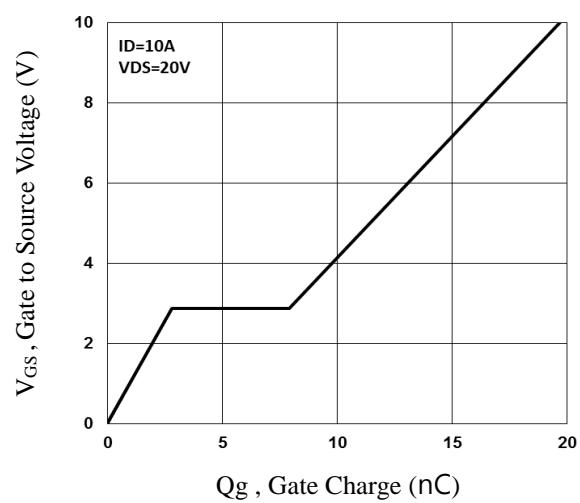
Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case ²	1.73	$^\circ C/W$
R_{eJA}	Thermal Resistance Junction to mbient	62	$^\circ C/W$

Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	40	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=40\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics³						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1.2	1.6	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance ²	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=15\text{A}$	---	6.5	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$	---	9	12	
G_{FS}	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	13	---	S
Dynamic Characteristics⁴						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1278	2200	pF
C_{oss}	Output Capacitance		---	135	250	
C_{rss}	Reverse Transfer Capacitance		---	87	170	
Switching Characteristics⁴						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time 3 , 4	$V_{\text{DD}}=15\text{V}, I_{\text{D}}=1\text{A}, R_{\text{G}}=3.3\Omega$ $V_{\text{GS}}=10\text{V}$	---	13.2	25	ns
t_r	Rise Time 3 , 4		---	2.2	5	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time 3 , 4		---	72	130	ns
t_f	Fall Time 3 , 4		---	4.5	10	ns
Q_g	Total Gate Charge 3 , 4	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}$	---	19.7	30	nC
Q_{gs}	Gate-Source Charge 3 , 4 3 , 4		---	2.8	4.2	nC
Q_{gd}	Gate-Drain "Miller" Charge 3 , 4		---	5.1	7.6	nC
Drain-Source Diode Characteristics						
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{SD}	Source-Drain Diode Forward Voltage ³	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}$	---	---	1	V
I_{S}	Continuous Source Current	$V_{\text{G}}=V_{\text{D}}=0\text{V}$, Force Current	---	---	70	A
I_{sm}	Pulsed Source Current		---	---	140	A
Trr	Reverse Recovery Time	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$ $T_J=25^\circ\text{C}$	---	17	---	ns
Q_{rr}	Reverse Recovery Charge		---	2.8	---	nC

Notes:

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=39A., R_G=25\Omega$, Starting $T_J=25^{\circ}C$.
3. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

Typical Characteristics: ($T_C=25^{\circ}C$ unless otherwise noted)

Fig.1 Continuous Drain Current vs. T_c

Fig.2 Normalized RDSON vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

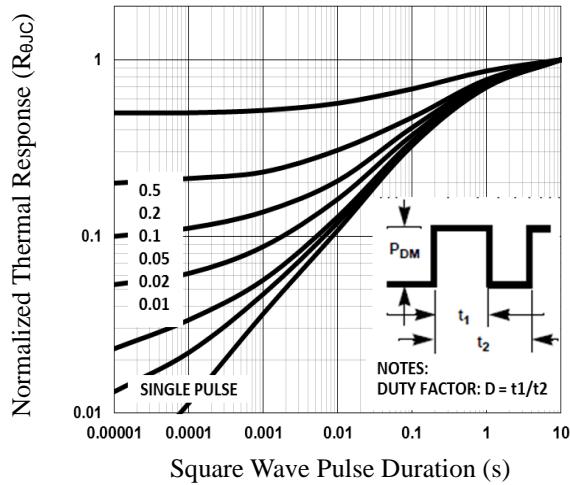


Fig.5 Normalized Transient Impedance

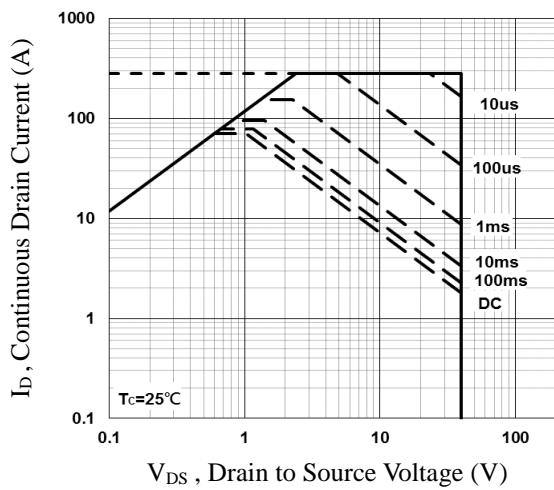


Fig.6 Maximum Safe Operation Area

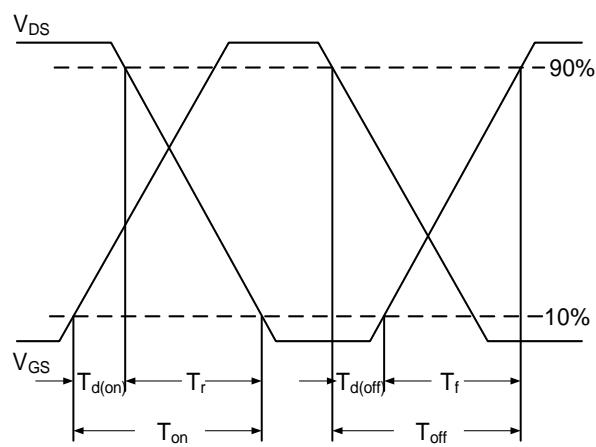


Fig.7 Switching Time Waveform

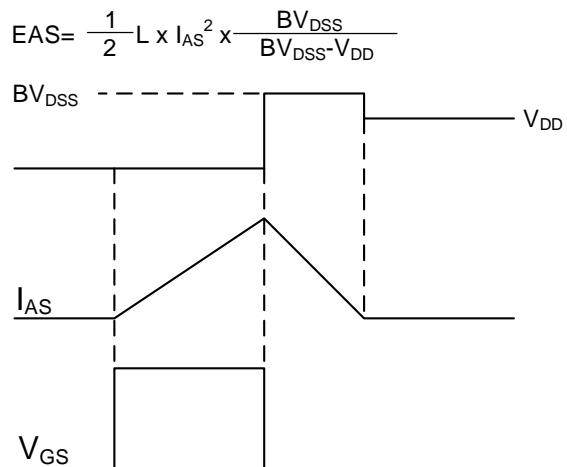
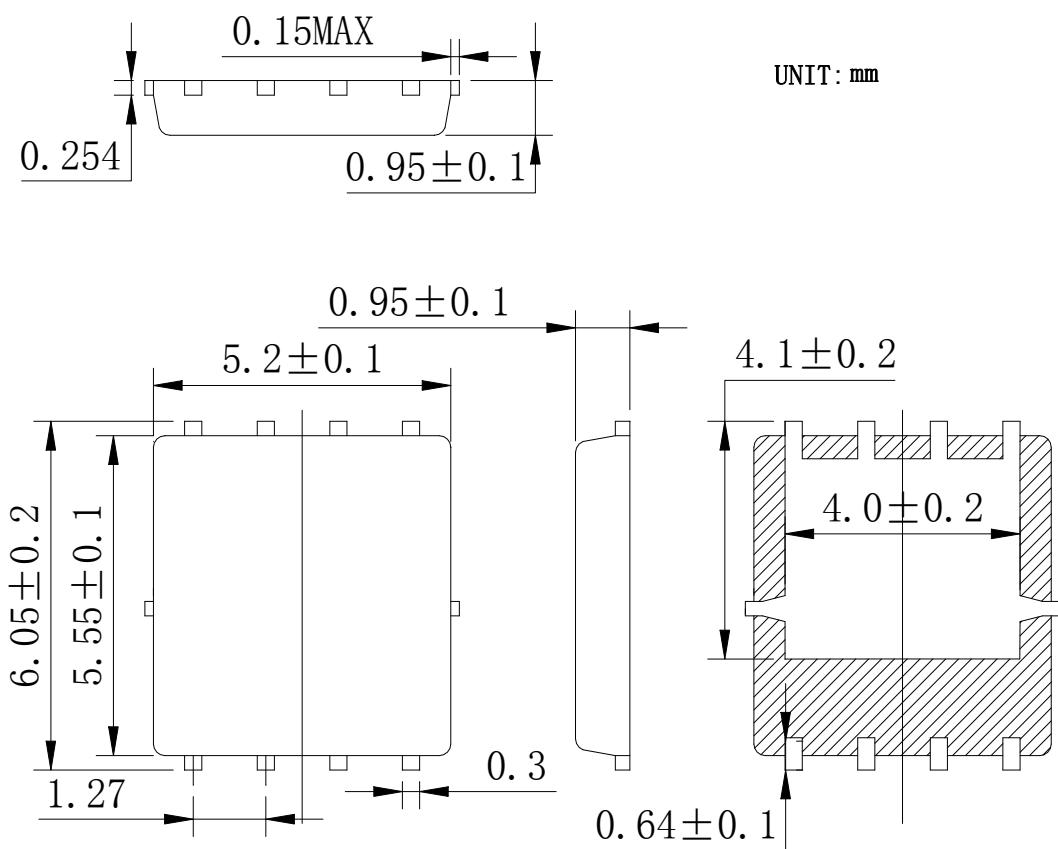


Fig.8 EAS Waveform

DFN5x6-8Package Information:



Package Information:

①. Doingter LOGO

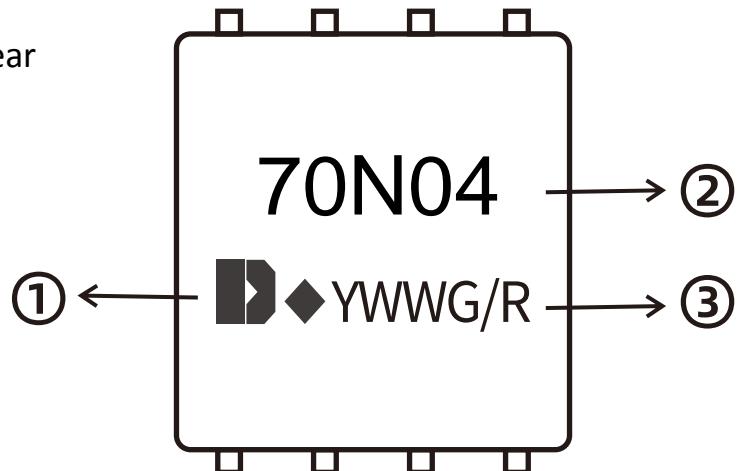
②. Part NO.

③. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



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