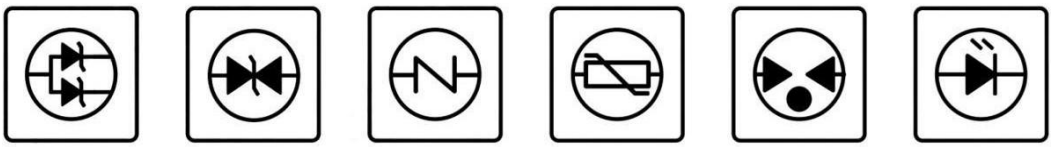
MSKSEMI 美森科
SEMICONDUCTOR



BTR06D3-MS

Product specification

1.1 Technology Data	Symbol		Value	Unit
Maximum allowable continuous DC voltage	V_{DC}		12	V
Varistor voltage measured *1	Vv		60	V
Typical capacitance value measured at 1MHz	C		3	pF
Typical capacitance value tolerance			+80-20	%
Maximum ESD allowable clamping Voltage*2	V_{CLAMP}	<	30	V
Leakage current at V_{DC}*3 (At initial state)	I_{LDC}	<	0.1	uA
Leakage current at V_{DC}*3 (After ESD Test)	I_{LDCA}	<	2	uA

1.2 Reference Data

Response time	T_{rise}	<	0.5	ns
Operation ambient temperature			-50~ +85	℃
Storage temperature			-50~+125	℃
ESD testing	IEC61000-4-2		Level 4	

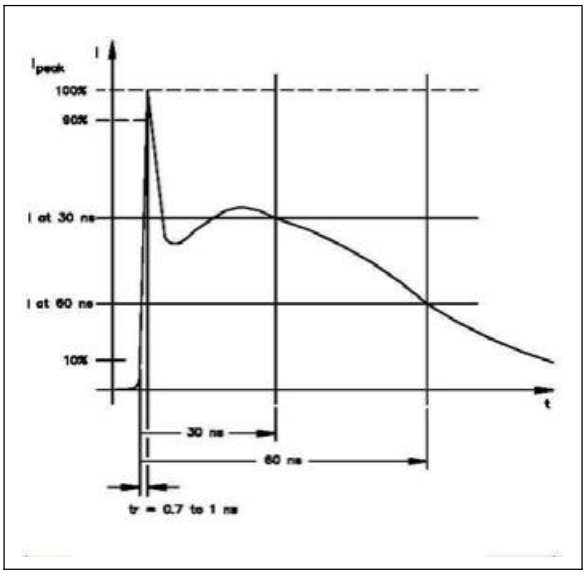
1.3 Other Data

Body			ZnO	
End termination			Ag/Ni/Sn	
Packaging			Reel	
Complies with Standard			IEC61000-4-2	
Complies with RoHs Standard			Yes	
Lead Content		<	1000	ppm
Marking			None	

Notes :

- 1 The varistor voltage was measured at 1 mA current
- 2 The Clamping voltage was measured at 8*20 us standard current.
- 3 The Leakage current was measured at working voltage.
- 4 The Energy only for customer reference.
- 5 The components shall be employed within 1 year, in the nitrogen condition.

2 ESD Wave Form



IEC61000-4-2 Standards

SEVERITY LEVEL	AIRDIRCHARGE	DIRECT DISCHARGE
1	2 KV	2 KV
2	4 KV	4 KV
3	8 KV	6 KV
4	15 KV	8 KV

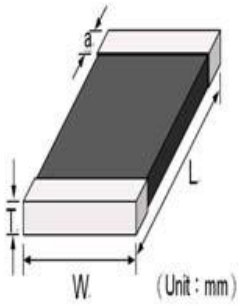
EC 61000-4-2 Compliant ESD Current Pulse Waveform

3 Environment Reliability Test

Characteristic	Test method and description			
High Temperature Storage	The specimen shall be subjected to 125 ± 2℃ for 1000 ± 12 hours in a thermostatic bath without load and then stored at room temperature and normal humidity for 1 to 2 hours. The change of varistor voltage shall be within 10 % .			
Temperature Cycle	The temperature cycle of specified temperatureshall be repeated five times and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10 % and mechanical damage shall be examined.	Step	Temperature	Period
		1	-40±3℃	30Min±3
		2	Room Temperature	1 hour
		3	125±3℃	30Min±3
		4	Room Temperature	1 hour
High Temperature Load	After being continuously applied the maximum allowable voltage at 85 ± 2℃ for 1000± 2 hours, the specimen shall be stored at room temperature and normal humidity for one or two hours, the change of varistor voltage shall be within 10% .			
Damp Heat Load/ Humidity Load	The specimen should be subjected to 40 ± 2℃, 90 to 95 % RH environment, and the maximum allowable voltage applied for 1000 hours, then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10%			
Low Temperature Storage	The specimen should be subjected to -40 ± 2℃ , without load for 500 hours and then stored at room temperature for one or two hours. The change of varistor voltage shall be within 10 %			

4 Size

Mode	Length(L)	Width(W)	Thickness(T)	Termination(a)
0603	1.60±0.15	0.80±0.10	0.90 max	0.3±0.1



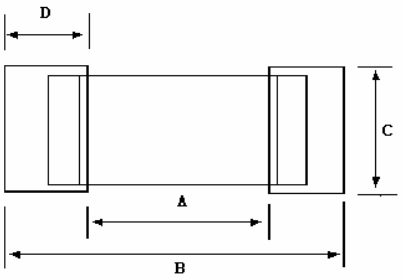
4.1REELSPECIFICATION

P/N	PKG	QTY
BTR06D3-MS	0603	4000

5 Soldering Recommendations

5.1 Recommended solder pad layout

	A	B	C	D
0603	0.9~1.2	2.7~3.2	0.7~1.0	0.9~1.2

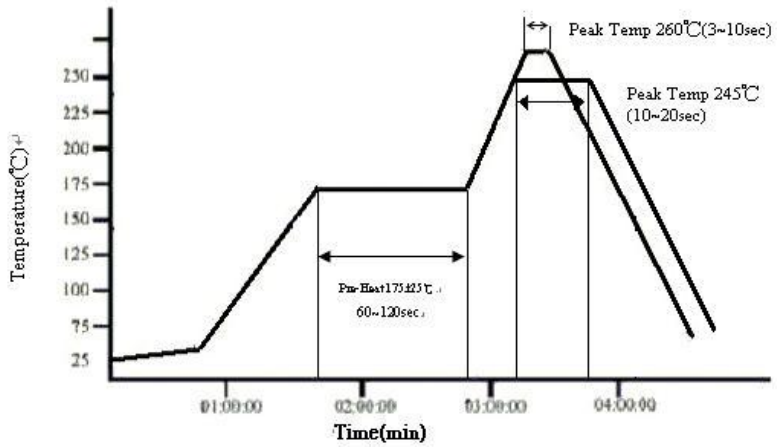


5.2 The SIR test of the solder paste shall be done（Based on JIS-Z-3284）

5.3 Steel plate and foot distance printing

Foot distance printing (mm)	Steel Plate thickness (mm)
> 0.65mm	0.18mm
0.65mm~0.5mm	0.15mm
0.50mm~0.40mm	0.12mm
>=0.40 mm	0.10mm

5.4The IR reflow and temperature of Soldering for Pb Free



IR reflow Pb Free Process suggestion profile

(1) The solder recommend is Sn96.5/Ag 3.5 of 120 to 150 μm

(2) Ramp-up rate (217˚C to Peak) + 3˚C/second max

(3) Temp. maintain at 175 +/-25˚C 180 seconds max

(4) Temp. maintain above 217˚C 60-150 seconds

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