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Siliup Semiconductor

SP25N50TF

500V N-Channel Power MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
500V	0.21Ω@10V	25A

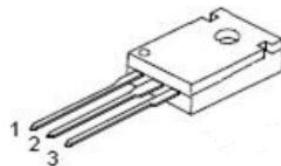
Feature

- Fast Switching
- Low Gate Charge and Rdson
- 100% Single Pulse avalanche energy Test

Applications

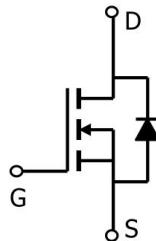
- DC-DC Converter
- Ideal for high-frequency switching and synchronous rectification

Package

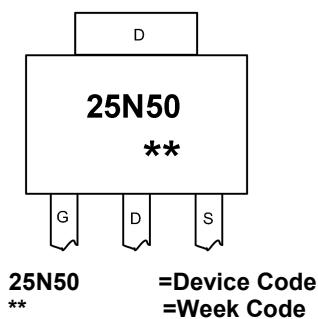


TO-247(1:G 2:D 3:S)

Circuit diagram



Marking



25N50
** =Device Code
 =Week Code



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Absolute maximum ratings (Ta=25°C,unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V _{DS}	500	V
Gate-Source Voltage	V _{GS}	±30	V
Continuous Drain Current ¹ (T _C =25°C)	I _D	25	A
Pulsed Drain Current ²	I _{DM}	100	A
Single Pulse Avalanche Energy ³	E _{AS}	1215	mJ
Total Power Dissipation(T _C =25°C)	P _D	300	W
Thermal Resistance Junction-Case ¹	R _{θJC}	0.42	°C/W
Storage Temperature Range	T _{STG}	-55 to 150	°C
Operating Junction Temperature Range	T _J	-55 to 150	°C

Electrical characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , ID=250uA	500	---	---	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =400V , V _{GS} =0V , TJ=25°C	---	---	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V , V _{DS} =0V	---	---	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , ID =250uA	2	3	4	V
Static Drain-Source on-Resistance	R _{DS(ON)}	V _{GS} =10V , ID=12A	---	0.21	0.26	Ω
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V , V _{GS} =0V , f=1MHz	---	3468	---	pF
Output Capacitance	C _{oss}		---	217	---	
Reverse Transfer Capacitance	C _{rss}		---	12	---	
Switching Characteristics						
Total Gate Charge	Q _g	V _{DS} =400V , V _{GS} =10V , ID=20A	---	63	---	nC
Gate-Source Charge	Q _{gs}		---	16	---	
Gate-Drain Charge	Q _{gd}		---	24	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =250V , V _{GS} =10V , RG=25Ω, ID=12A	---	37	---	ns
Rise Time	T _r		---	64	---	
Turn-Off Delay Time	T _{d(off)}		---	86	---	
Fall Time	T _f		---	46	---	

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width ≈ 300us , duty cycle ≈ 2%
3. The EAS data shows Max. rating . The test condition is R_G=30Ω ,L=10mH



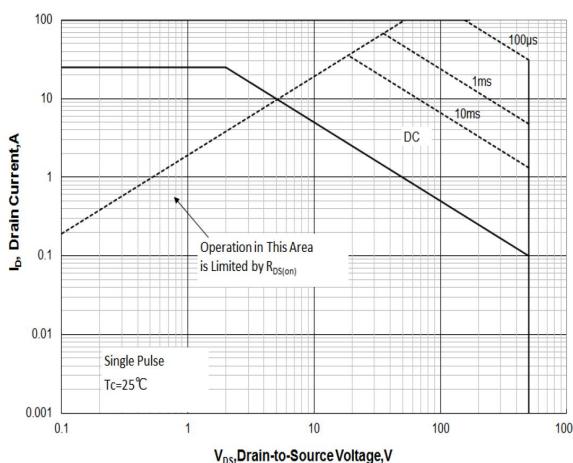
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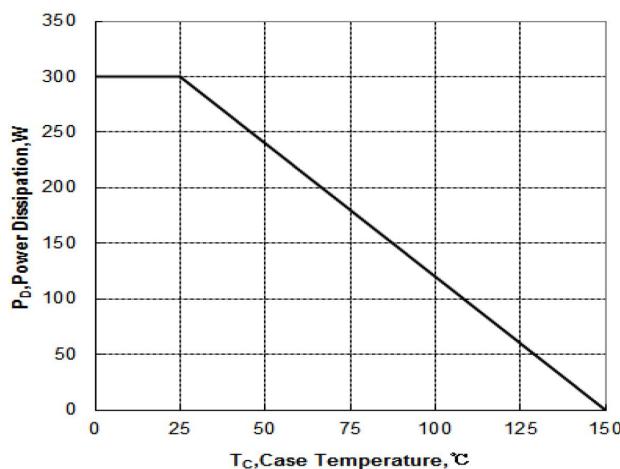
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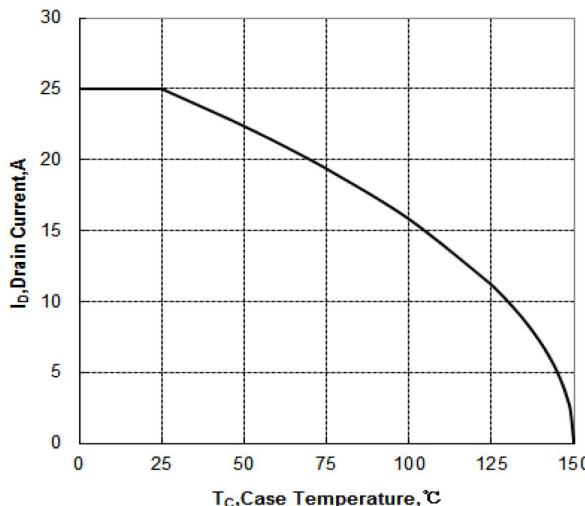
Typical Characteristics



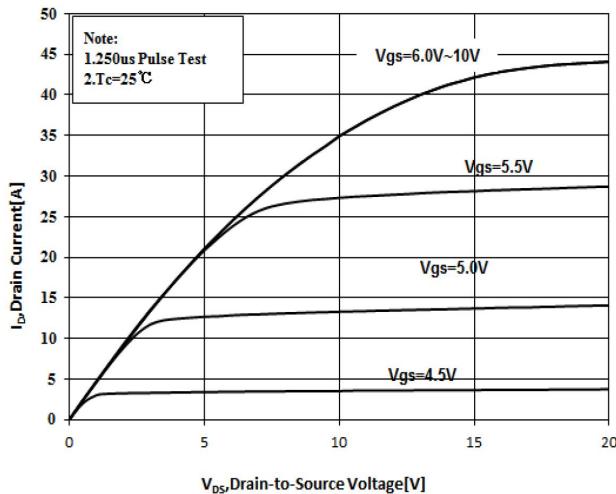
Maximum Forward Bias Safe Operating Area



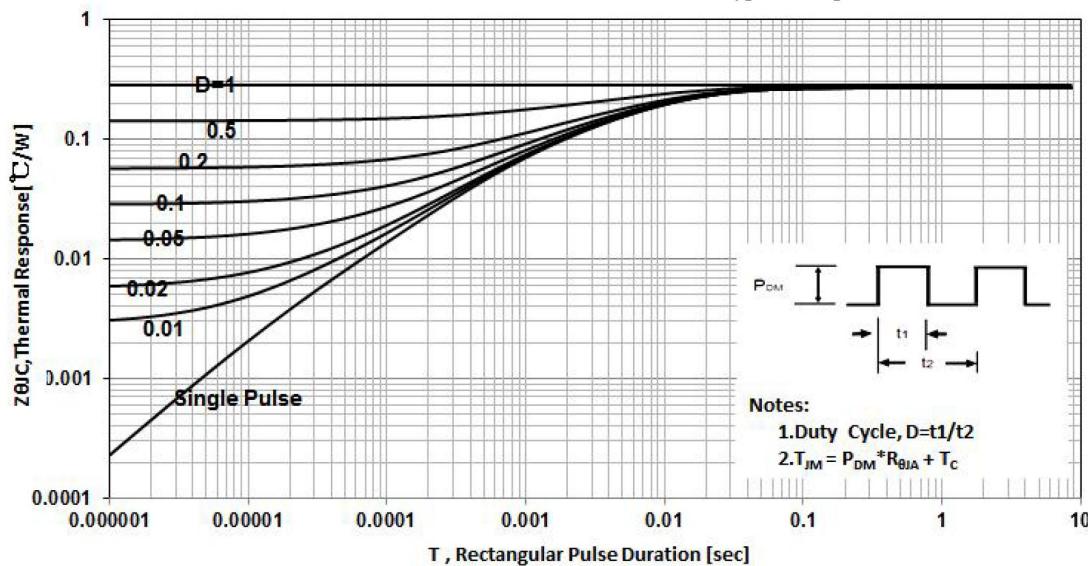
Maximum Power dissipation vs Case Temperature



Maximum Continuous Drain Current vs Case Temperature



Typical Output Characteristics



Maximum Effective Thermal Impedance , Junction to Case

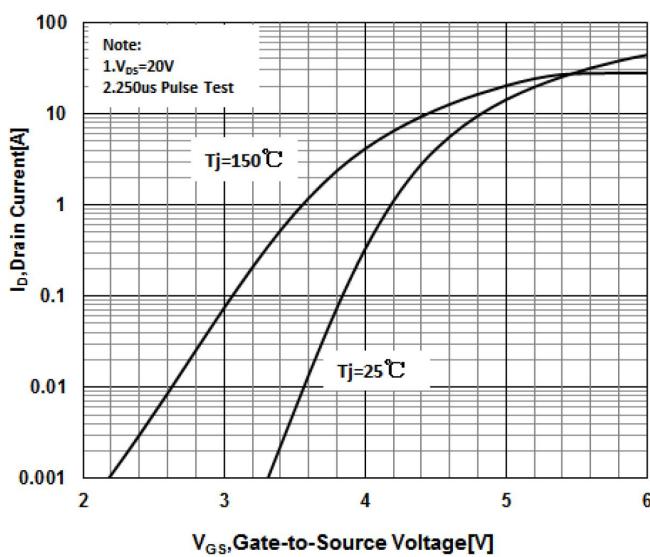


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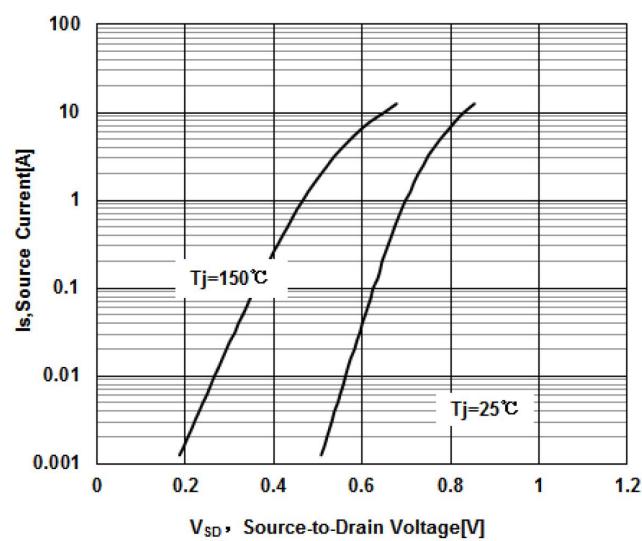
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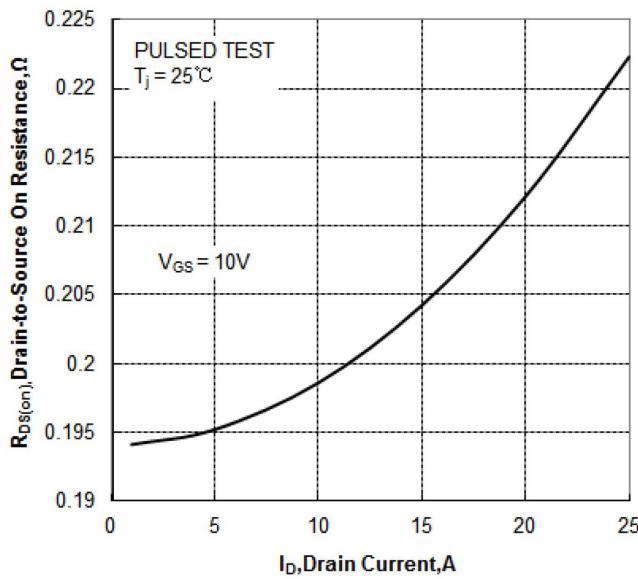
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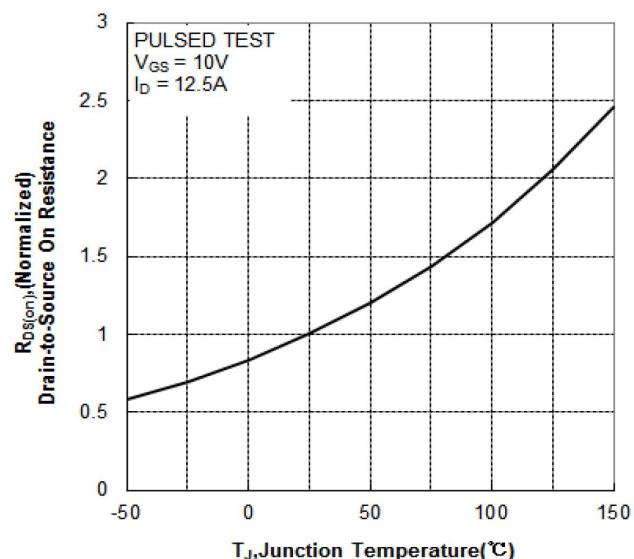
Typical Transfer Characteristics



Typical Body Diode Transfer Characteristics



Typical Drain to Source ON Resistance
vs Drain Current



Typical Drian to Source on Resistance
vs Junction Temperature

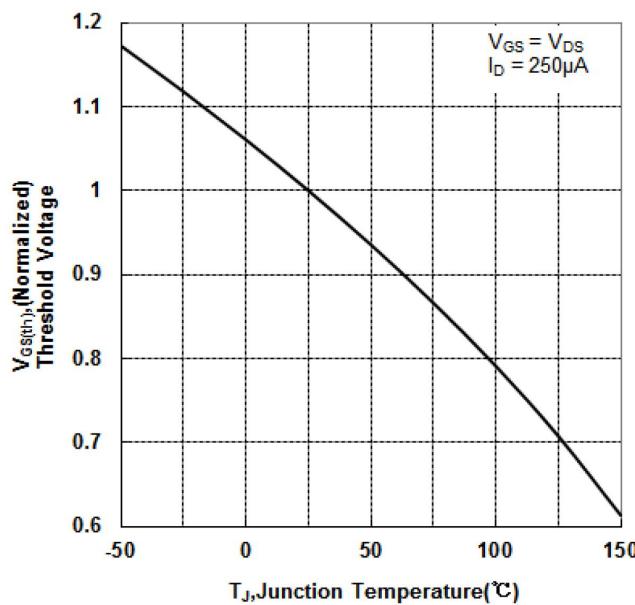


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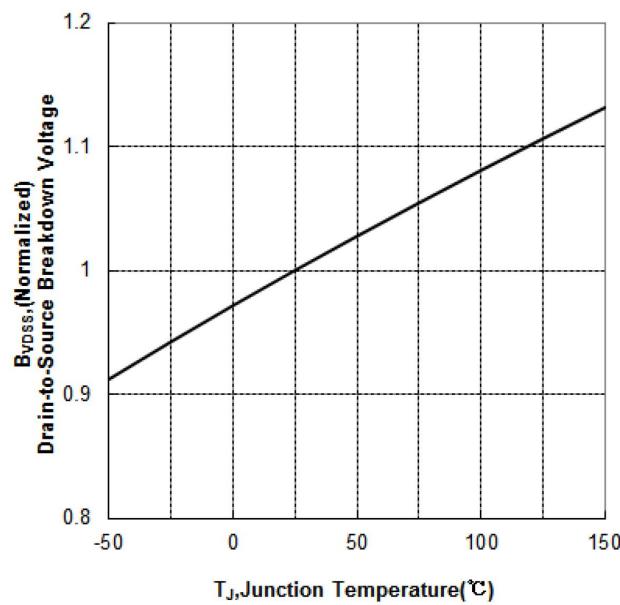
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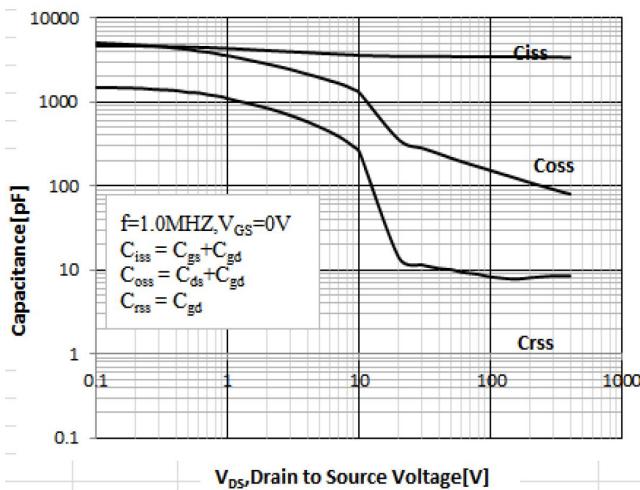
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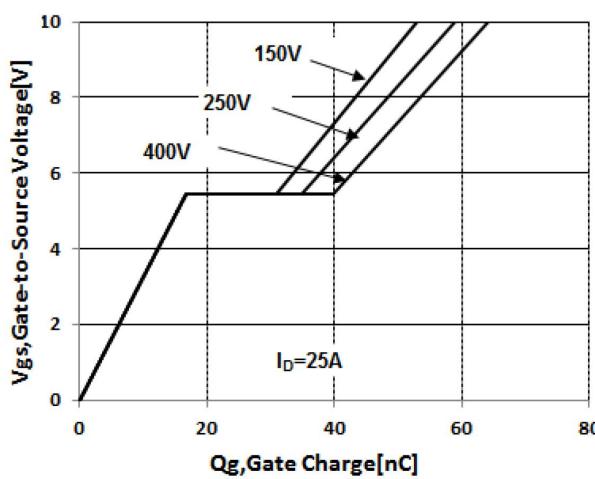
Typical Threshold Voltage vs Junction Temperature



Typical Breakdown Voltage vs Junction Temperature



Typical Capacitance vs Drain to Source Voltage



Typical Gate Charge vs Gate to Source Voltage



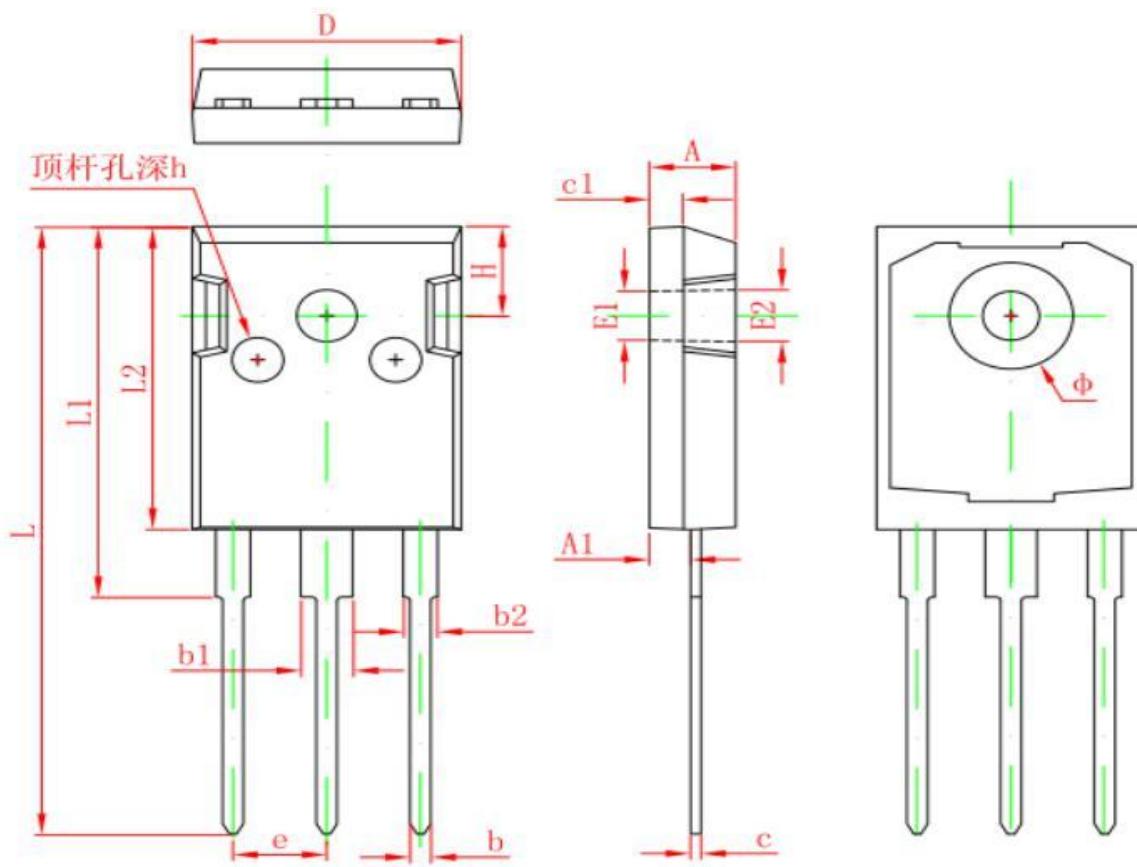
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TO-247 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.850	5.150	0.191	0.200
A1	2.200	2.600	0.087	0.102
b	1.000	1.400	0.039	0.055
b1	2.800	3.200	0.110	0.126
b2	1.800	2.200	0.071	0.087
c	0.500	0.700	0.020	0.028
c1	1.900	2.100	0.075	0.083
D	15.450	15.750	0.608	0.620
E1	3.500 REF.		0.138 REF.	
E2	3.600 REF.		0.142 REF.	
L	40.900	41.300	1.610	1.626
L1	24.800	25.100	0.976	0.988
L2	20.300	20.600	0.799	0.811
Φ	7.100	7.300	0.280	0.287
e	5.450 TYP.		0.215 TYP.	
H	5.980 REF.		0.235 REF.	
h	0.000	0.300	0.000	0.012