MSKSEMI 美森科













ESD

TSS

MOV

GDT

PIFD

ESD5471X-MS

Product specification





Features

- 80Watts peak pulse power (tp = 8/20µs)
- Transient protection for high speed data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
 IEC 61000-4-4 (EFT) 40A (5/50ns)
- Working voltages : 5V
- Protects One Power or I/O Port
- Low operating and clamping voltages
- Solid-state silicon avalanche technology

Applications

- Notebooks, Desktops, Servers and Video Graphics Cards
- USB Power & Data Line Protection
- Monitors and Flat Panel Displays
- I²C Bus Protection
- Portable Instrumentation
- Set Top Box

Reference News

PACKAGE OUTLINE	Circuit Diagram	Marking
		AK*
DFN1006-2		

Maximum Rating @ Ta=25 $^{\circ}$ C unless otherwise specified

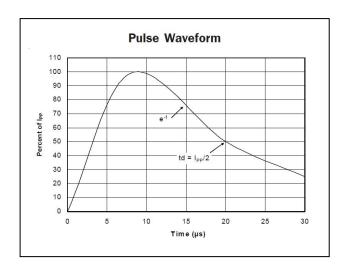
Symbol	Parameter	Ratings	Units
Рек	Peak Pulse Power (tp = 8/20μs)	80	Watts
TL	Lead Soldering Temperature	260(10sec.)	$^{\circ}$
TJ	Operating Temperature	-55 to +125	$^{\circ}$
Тѕтс	Storage Temperature	-55 to +150	$^{\circ}$

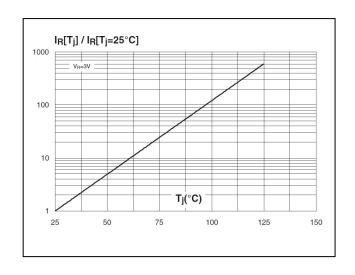


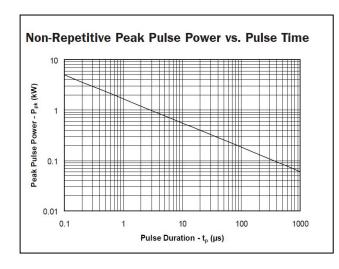
Electrical Characteristics@ Ta=25℃ unless otherwise

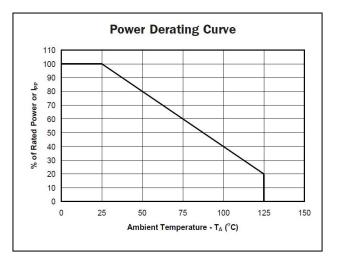
		VRW	м @ IR	VBR@ImA	Vc@1	V c@	DIPP	CJ	
	Parameter	V	μA	V	V	V	Α	pF	
			MAX	MIN	MAX	MAX		TYP	
	ESD5471X-MS	5	1	5.8	11.8	15	5	12	

Typical Characteristics@ Ta=25℃ unless otherwise specified





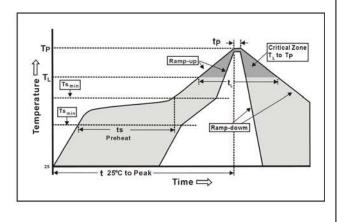






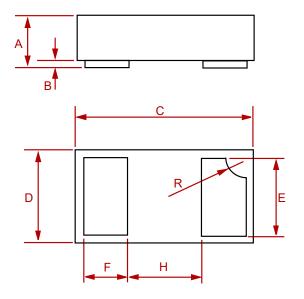
Soldering Parameters

Reflow Co	ondition	Fb – Free assembly	
Pre Heat	-Temperature Min (T _{s(Min)})	150°C	
	- Temperature Max (T _{s(Max)})	200°C	
	-Time (Min to max) (t _s)	60 - 180 secs	
Average ((T _L) to pe	ramp up rate (Liquidus) Temp ak	3°C/second Max	
T _{s (Max)} to T _L - Ramp-up Rate		3°C/second Max	
Reflow	-Temperature (T _L) (Liquidus)	217°C	
	-Temperature (t _L)	60 – 150 seconds	
Peak Tem	perature (T _p)	250+0/-5 °C	
Time within 5°C of actual peak Temperature (t _p)		20 – 40 seconds	
Ramp-dowm Rate		6°C/second Max	
Time 25°	C to peak Temperature (Tp)	8 minutes Max.	
Do not exceed		260°C	



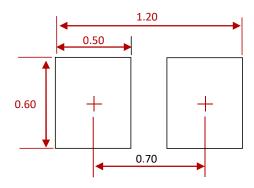


PACKAGE MECHANICAL DATA



Dim	Inc	hes	Millimeters		
Dim	MIN	MAX	MIN	MAX	
Α	0.0125	0.02	0.32	0.52	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
E	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
Н	0.015Typ.		0.40	Тур.	
R	0.001	0.005	0.05	0.15	

Suggested Pad Layout



NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

REEL SPECIFICATION

P/N	PKG	QTY
ESD5471X-MS	DFN1006-2	10000



Attention

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents—or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, referto the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.