

TM05N06MI
N-Channel Enhancement Mosfet

General Description <ul style="list-style-type: none"> Low $R_{DS(ON)}$ RoHS and Halogen-Free Compliant Applications <ul style="list-style-type: none"> Load switch PWM 	General Features <p> $V_{DS} = 60V$ $I_D = 5.0A$ $R_{DS(ON)} = 40\text{ m}\Omega(\text{Typ.}) @ V_{GS}=10V$ </p> <p> 100% UIS Tested 100% R_g Tested </p> 
	

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)				
Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	60	V	
V_{GS}	Gate-Source Voltage	± 20	V	
$I_D @ T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	5.0	A	
$I_D @ T_A=100^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V^1$	3.8	A	
I_{DM}	Pulsed Drain Current ²	18	A	
$P_D @ T_c=25^\circ C$	Total Power Dissipation ³	3	W	
T_{STG}	Storage Temperature Range	-55 to 175	°C	
T_J	Operating Junction Temperature Range	-55 to 175	°C	

Thermal Data				
Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	48	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	---	°C/W

TM05N06MI
N-Channel Enhancement Mosfet
Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}, V_{GS}=0\text{V},$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1	1.6	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}, I_D=15\text{A}$	-	40	49	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=10\text{A}$	-	45	63	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$	-	625	-	pF
C_{oss}	Output Capacitance		-	49	-	pF
C_{rss}	Reverse Transfer Capacitance		-	41	-	pF
Q_g	Total Gate Charge	$V_{DS}=30\text{V}, I_D=4.5\text{A}, V_{GS}=10\text{V}$	-	14	-	nC
Q_{gs}	Gate-Source Charge		-	2.9	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	5.2	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=30\text{V}, I_D=2\text{A}, R_L=6.7\Omega, R_G=3\Omega, V_{GS}=10\text{V}$	-	5	-	ns
t_r	Turn-on Rise Time		-	2.6	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	16.1	-	ns
t_f	Turn-off Fall Time		-	2.3	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
I_{sM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	30	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}, I_s=15\text{A}$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	$T_J=25^\circ\text{C}, I_F=15\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	35	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	53	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}, V_{DD}=30\text{V}, V_G=10\text{V}, L=0.5\text{mH}, R_g=25\Omega, I_{AS}=6.1\text{A}$

3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

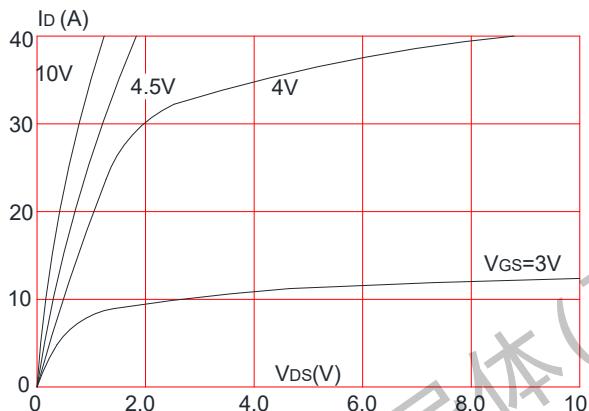


Figure 3: On-resistance vs. Drain Current

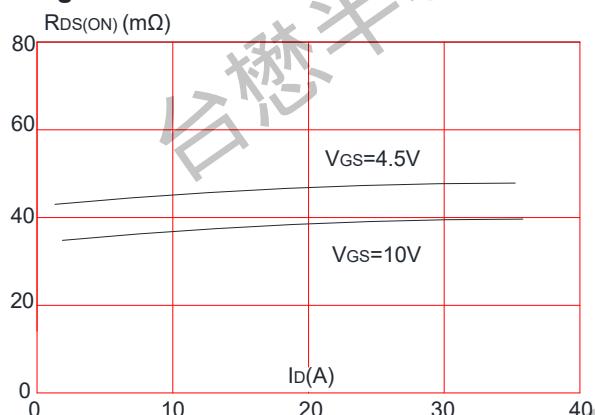


Figure 5: Gate Charge Characteristics

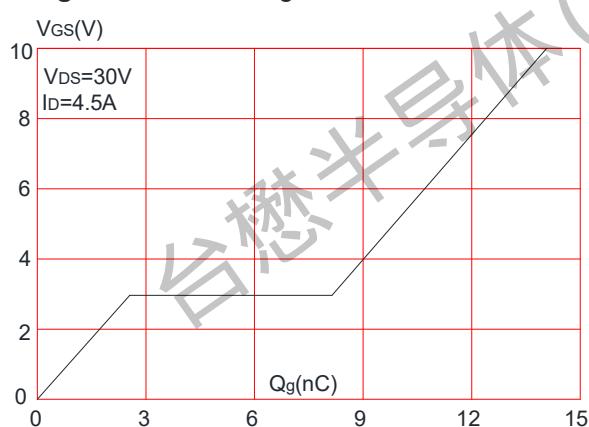


Figure 2: Typical Transfer Characteristics

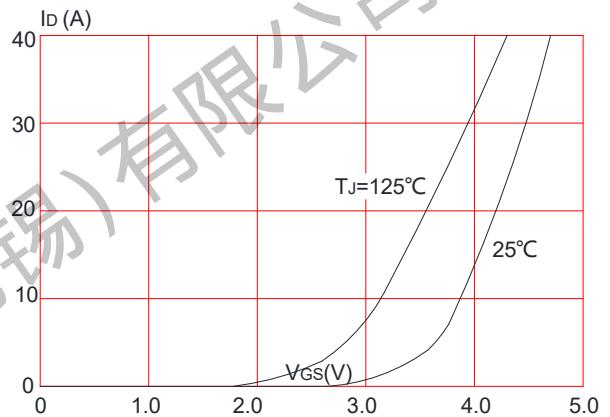


Figure 4: Body Diode Characteristics

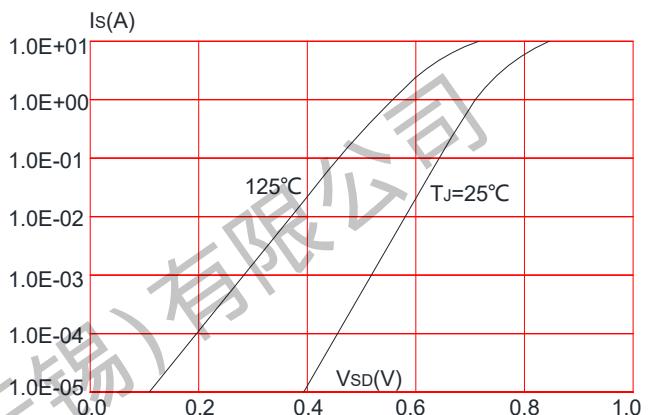
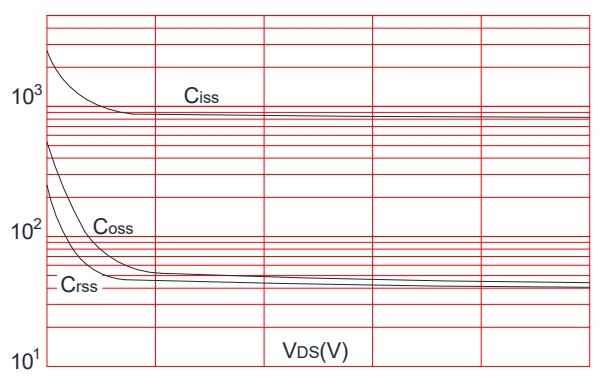


Figure 6: Capacitance Characteristics



TM05N06MI

N-Channel Enhancement Mosfet

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

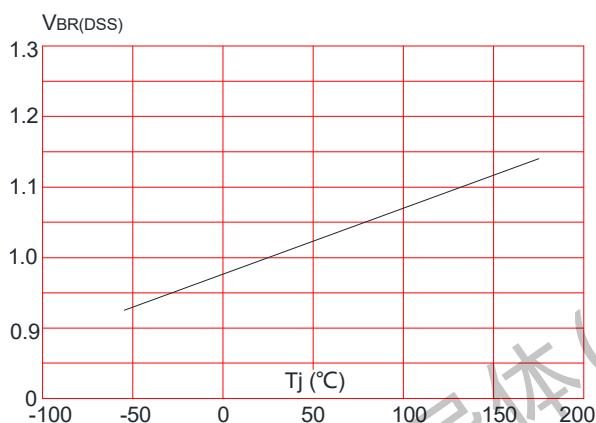


Figure 8: Normalized on Resistance vs. Junction Temperature

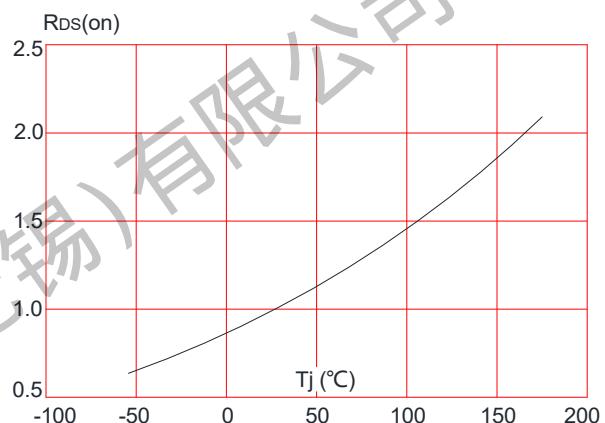


Figure 9: Maximum Safe Operating Area

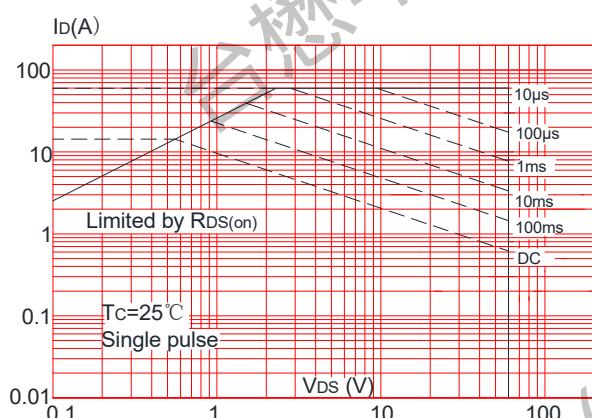


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

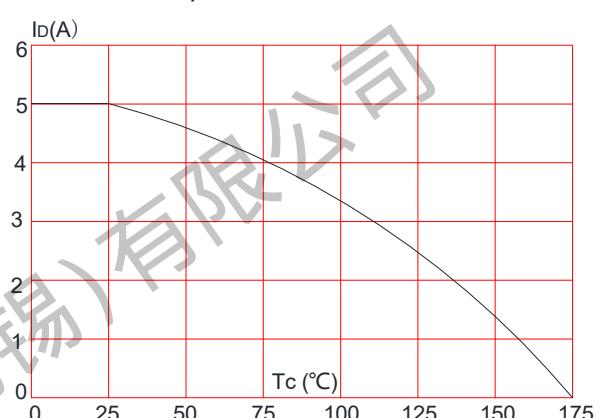
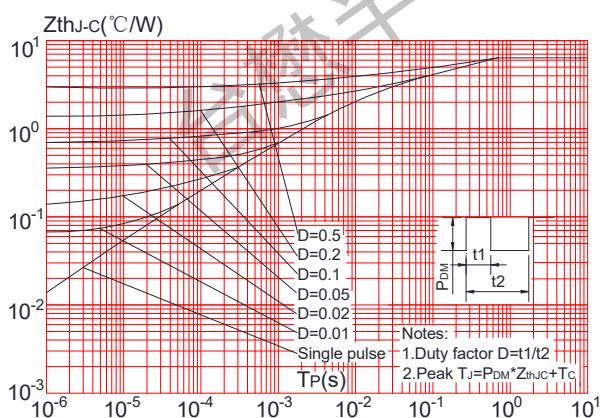
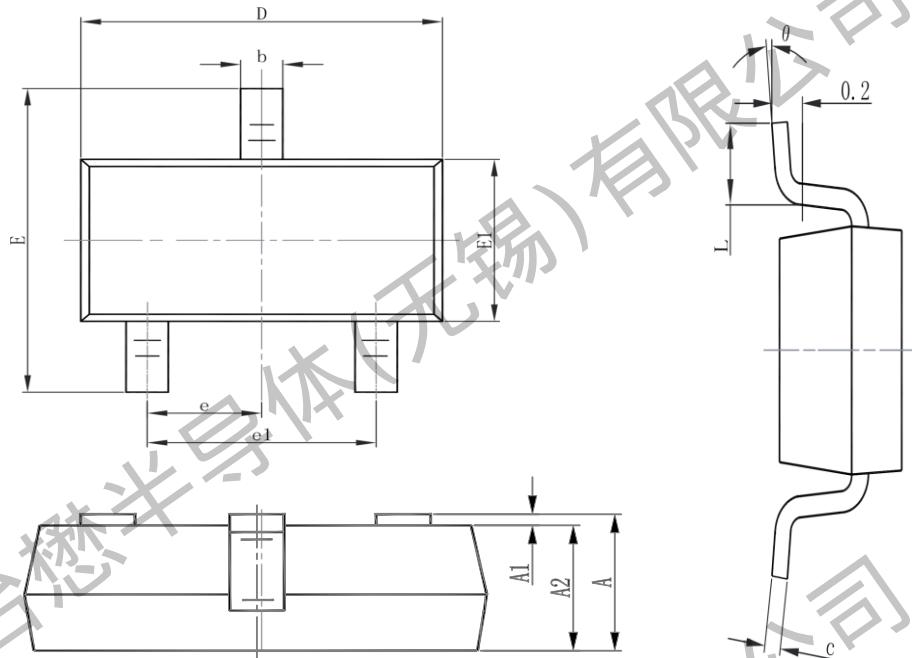


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



Package Mechanical Data:SOT-23-3L



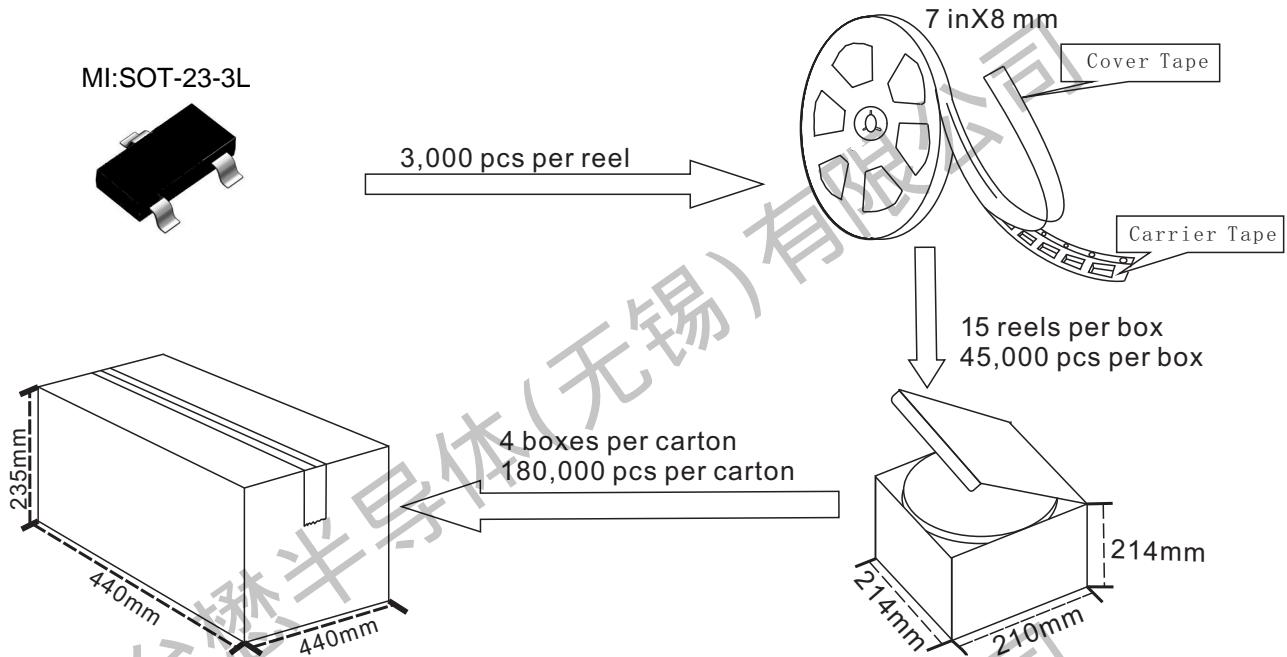
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

TM05N06MI

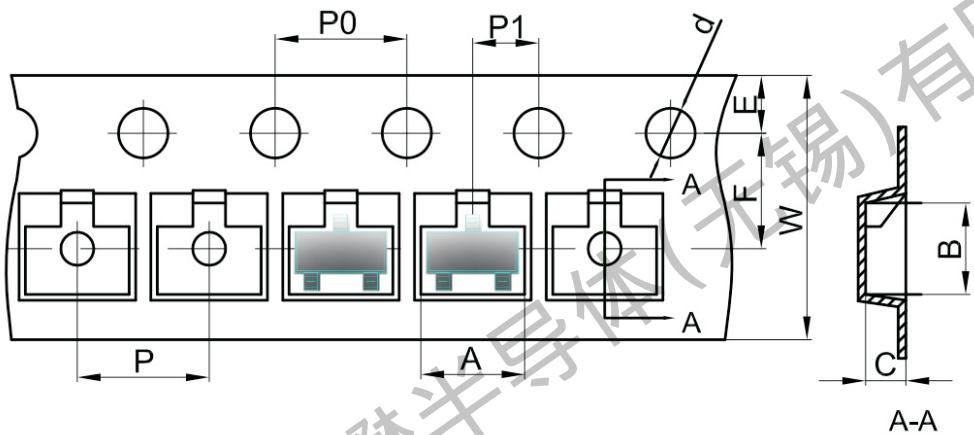
N-Channel Enhancement Mosfet

SOT-23-3L Packing

1. The method of packaging and dimension are shown as below figure. (Dimension in mm)

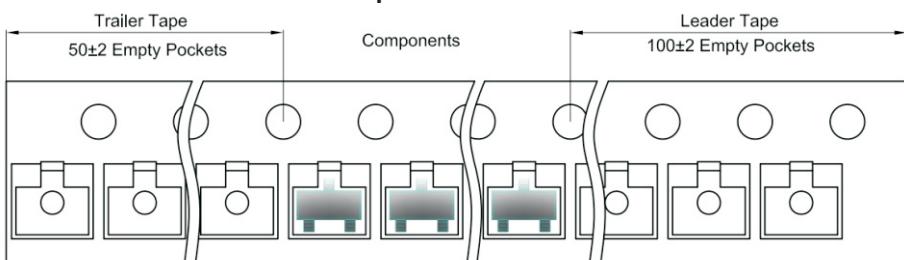


SOT-23-3L Embossed Carrier Tape



Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
SOT-23	3.15	2.77	1.22	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00

SOT-23-3L Tape Leader and Trailer



Important Notices and Disclaimers

- Tritech-MOS Technology Corp. reserves the right to change this document, its products, and specifications at any time without prior notice.
- Before final design, purchase, or use, customers should obtain and confirm the latest product information and specifications.
- Tritech-MOS Technology Corp. makes no warranties, representations or warranties regarding the suitability of its products for any specific purpose, and Tritech-MOS Technology Corp. does not assume any responsibility for application assistance or customer product design.
- Tritech-MOS Technology Corp. does not guarantee or assume any responsibility for the purchase or use of any unexpected or unauthorized products.
- Any intellectual property rights of Tritech-MOS Technology Corp. are not licensed through implication or other means.
- Products of Tritech-MOS Technology Corp. are not included as critical components in life support equipment or systems without explicit written approval from Tritech-MOS Technology Corp.

Revision history:

Date	Rev	Description	Page
2023.08.23	23.08	Original	