

ZHHXDZ 珠海海芯电子有限公司

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HX33151-S/HX33151-P Dual 1.5A-Peak Low-Side MOSFET

General Description

The HX33151-S/HX33151-P series of dual low-side MOSFET drivers are manufactured on a BiCMOS/DMOS process, providing efficient power usage and high reliability. These drivers translate input logic levels from TTL or CMOS to output voltage levels that swing within 25 mV of the positive supply or ground. This is in contrast to bipolar devices, which can only swing to within 1V of the supply. The HX33151-S/HX33151-P drivers are available in three configurations: dual inverting, dual non-inverting, and one inverting plus one non-inverting output. They are designed to replace the HX33151-S/HX33151-P, offering improved electrical performance ruggedness.

The HX33151-S/HX33151-P drivers are highly durable, capable of withstanding up to 500 mA of reverse current (either polarity) without latching and up to 5V noise spikes (either polarity) on the ground pins. These drivers are primarily intended for driving power MOSFETs, but are also suitable driving other loads that require low-impedance, high peak current, and fast switching time. Other potential applications include driving heavily loaded clock lines, coaxial cables, or piezoelectric transducers. It is important to note that the total driver power dissipation must not exceed the package's limits. For high power and narrow pulse applications.

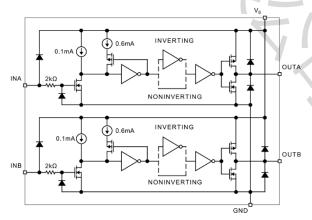
Features

- 1.5A-Peak Output Current
- Latch-Up Protected to >500 mA Reverse Current
- 4.5V to 18V Operating Range
- Switches 1000 pF in 25 ns
- Low Quiescent Supply Current
 - 4 mA at Logic 1 Input
 - 400 µA at Logic 0 Input
- Matched Rise and Fall Times
- 7Ω Output Impedance
- <40 ns Typical Delay
- Logic-Input Protection to -5V
- Logic-Input Threshold Independent of Supply Voltage
- 6 pF Typical Equivalent Input Capacitance
- 25 mV Max. Output Offset from Supply or Ground

Applications

- MOSFET Driver
- Piezoelectric Transducer Driver
- Coax Cable Driver
- Clock Line Driver

Pin Configuration and Functions



Inverting

Functional Block Diagram

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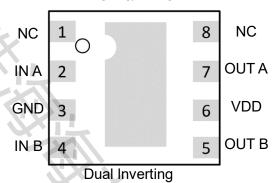


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Drivers

SOP-8/DIP-8



Pin Diagram

	Pin Diagra	<u>m</u>						
ELECTRICAL CHARACTERISTICS								
Parameter	Sym.	Min.	Тур.	Max.	Units			
Input								
Logic 1 Input Voltage		2.4	1.4	_	V			
	VIH	2.4	1.5	_	V			
Laria Olimust Valtaria			1.1	0.8	V			
Logic 0 Input Voltage	VIL		1.0	0.8				
Input Current	IIN	-1		1	μΑ			
Output								
High Output Voltage	VOH	V _s –		_	V			
Low Output Voltage	VOL	_	ZAY	0.025	V			
Output Resistance		_	6	10	Ω			
Output Resistance	Ro	_	8	12	12			
Peak Output Current	IPK	_	1.5		Α			
Latch-Up Protection	I	>500	1	4	mA			
Switching Time								
Rise Time		_	18	30	no			
	t _r	_	20	40	ns			
Fall Time		_	15	20	ns			
	t_{f}	_	29	40				
Delay Time		_	17	30	ns			
	tD1	_	19	40	115			
Delay Time	tD2	_	23	50	ns			

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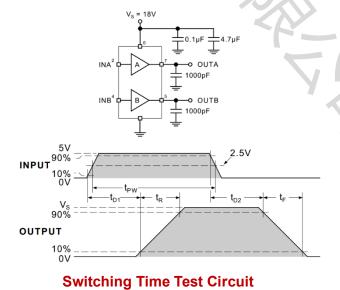
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ELECTRICAL CHARACTERISTICS (CONTINUED)								
Parameter	Sym.	Min.	Тур.	Max.	Units			
Pulse Width	tPW	400	_		ns			
Power Supply								
Power Supply Current		0.6	1.4	4.5	mA			
	IS	_	1.5	8				
Power Supply Current		_	0.18	0.4	mA			
	IS	_	0.19	0.6				

TEMPERATURE SPECIFICATIONS								
Temperature Ranges								
Maximum Junction Temperature	TJ	_	_	+150	°C			
Storage Temperature Range	T _S	-65	_	+150	°C			
Lead Temperature		_	_	+300	°C			
Junction Operating Temperature	TJ	0	_	+70	°C			
Package Thermal Resistances	//							
Thermal Resistance, PDIP 8-Ld	θЈА	_	130	_	°C/W			
Thermal Resistance, PDIP 8-Ld	θЈС	_	42	_	°C/W			
Thermal Resistance, SOIC 8-Ld	θЈА		120	_	°C/W			
Thermal Resistance, SOIC 8-Ld	θЈС		75	_	°C/W			
Thermal Resistance, MSOP 8-Ld	θЈА	-	250	_	°C/W			

Aplication Informa



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