MSKSEMI 美森科







TVC



TSS



MOV



GDT



PIFF

2N7002DW

Product specification





General Features

- 60V,0.3A,RDS(ON)=1.8Ω@VGS=10V
 Improved dv/dt capability
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded

Application

- Motor Drive
- Power Tools
- LED Lighting

Reference News

PACKAGE OUTLINE	Pin Configuration	Marking
SOT-363	G1 G2 G2 S1 S2	K72** KΔ2**



Absolute Maximum Ratings (TA=25℃ unless otherwise

Symbol	Parameter	Rating	Units
Vos	Drain- Source Voltage	60	V
Vgs	Gate- Source Voltage	±20	V
lo	Drain Current - Continuous (T _A =25)	0.3	А
li D	Drain Current - Continuous (T _A =70)	0.24	Α
Ірм	Drain Current – Pulsed ¹	1.2	Α
Po	Power Dissipation (T _A =25)	0.28	W
PU	Power Dissipation – Derate above 25	0.002	W/°C
Тѕтс	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient		450	°C/W

ElectricalCharacteristics(TJ=25 °C, unless otherwise noted) Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	VGS=0V , ID=250uA	60			V
△BVDSS/ △TJ	BVDSS Temperature Coefficient	Reference to 25 , ID=1 mA		0.04		V/℃
IDSS	Drain-Source Leakage Current	VDS=60V , VGS=0V , TJ=25			1u	А
		VDS=48V , VGS=0V , TJ=125			100u	Α
IGSS	Gate-Source Leakage Current	VGS= ±20V , VDS=0V			±10	А

On Characteristics

RDS(ON)	Static Drain-Source On-Resistance	VGS=10V , ID=0.3A		1.8	2.8	Ω
,		VGS=4.5V , ID=0.2A		2.2	3	Ω
VGS(III)	Gate Threshold Voltage	VGS=VDS , ID =250uA	1	1.6	2.5	V
△VGS(th	VGS(th) Temperature Coefficient			-4		Mv/℃
)						
gfs	Forward Transconductance	VDS=10V , ID=0. 1A		0.24		S



Dynamic and switching Characteristics

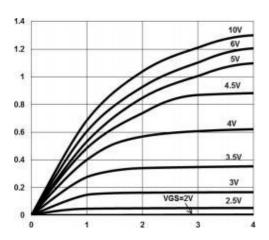
Qg	Total Gate Charge ² , 3		 1.1	
Qgs	Gate-Source Charge ² , 3	VDS=30V , VGS=10V , ID=0.2A	 0.1	nC
Qgd	Gate-Drain Charge ² , 3	ID-0.2A	 0.23	
Td(on)	Turn-On Delay Time ² , 3		 3	
Tr	Rise Time ² , ³	VDD=30V , VGS=10V ,	 5	~C
Td(off)	Turn-Off Delay Time ² , 3	RG=6Ω ID=0.2A	 14	nS
Tf	Fall Time ² , 3	10 0-2 ID 0.2/	 9	
Ciss	Input Capacitance		 30.6	
Coss	Output Capacitance	VDS=10V , VGS=0V , F=1MHz	 5.5	pF
Crss	Reverse Transfer Capacitance		 4	

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
IS	Continuous Source Current	VG=VD=0V , Force Current			0.3	Α
ISM	Pulsed Source Current				0.6	Α
VSD	Diode Forward Voltage	VGS=0V , IS=1A , TJ=25			1.2	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.





 $\ensuremath{\mathsf{VDS}}$, Drain to Source Voltage (V)

Fig. 1 Output Characteristics

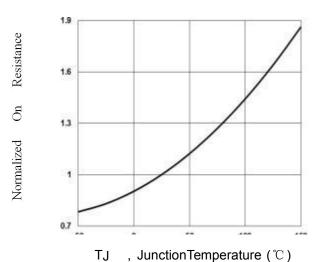


Fig. 3 Normalized RDSON vs. TJ

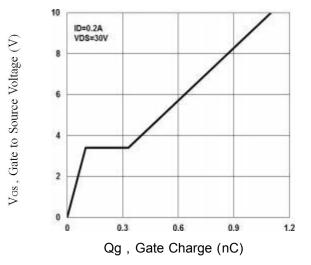
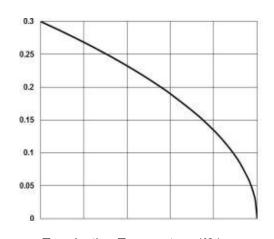


Fig. 5 Gate Charge Waveform



TJ , Juction Temperature ($^{\mathbb{C}}$) Fig. 2 Continuous Drain Current vs. TJ

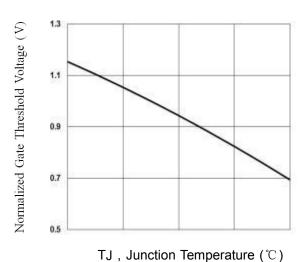
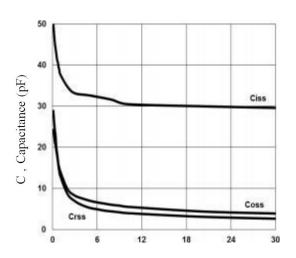
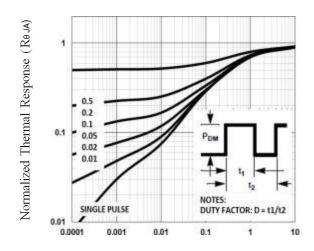


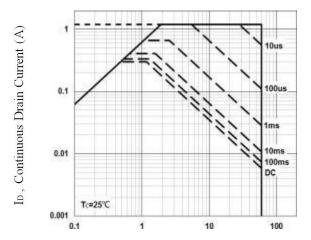
Fig. 4 Normalized Vth vs. TJ



VDS , Drain to Source Voltage (V) Fig. 6 Capacitance Characteristics



Square Wave Pulse Duration (s) Fig. 7 Normalized Transient Impedance



VDS ,Drain to Source Voltage(V)
Fig. 8 Maximum Safe Operation Area

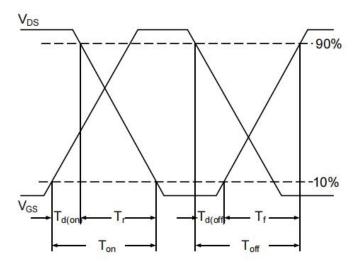
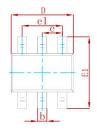


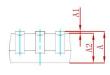
Fig.9 Switching Time Waveform



PACKAGE MECHANICAL DATA

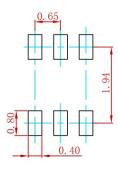






Ohal	Dimensions In Millimeters		Dimensions	In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
С	0.100	0.150	0.004	0.006
D	2.000	2.200	0.079	0.087
Е	1.150	1.350	0.045	0.053
E1	2.150	2.400	0.085	0.094
е	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525	REF	0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance: ± 0.05mm.
- 3. The pad layout is for reference purposes only.

REEL

P/N	PKG	QTY
2N7002DW	SOT-363	3000



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