



### ■ Features

- Electrostatic discharge (ESD) test under IEC6100-4-2 standard >16KV(SS22LA~SS26LA).  
standard >10KV(SS210LA~SS220LA).
- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction. Suffix
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

### ■ Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMA
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Weight : 0.002 ounce, 0.055 gram

### ■ Maximum ratings and electrical characteristics

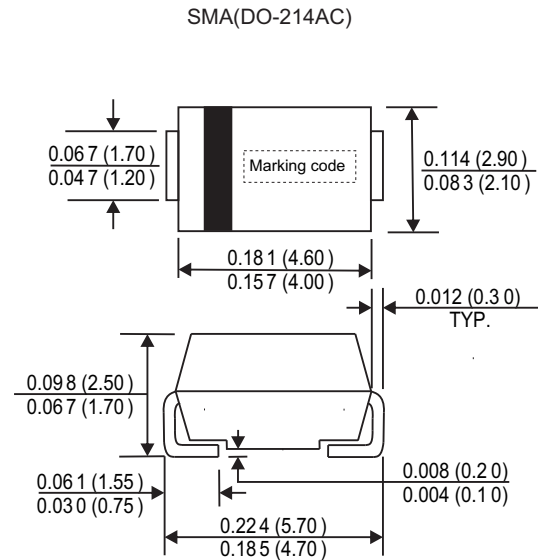
Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Parameters                                                                                       | Symbol             | SK<br>24L   | SK<br>25L | SK<br>26L | SK<br>210L | SK<br>215L  | SK<br>220L |      |
|--------------------------------------------------------------------------------------------------|--------------------|-------------|-----------|-----------|------------|-------------|------------|------|
| Marking code                                                                                     |                    | SK<br>24L   | SK<br>25L | SK<br>26L | SK<br>210L | SK<br>215L  | SK<br>220L | UNIS |
| Maximum Recurrent Peak Reverse Voltage                                                           | V <sub>RRM</sub>   | 40          | 50        | 60        | 100        | 150         | 200        | V    |
| Maxium RMS Voltage                                                                               | V <sub>RMS</sub>   | 28          | 35        | 42        | 70         | 105         | 140        | V    |
| Maxium DC Blocking Voltage                                                                       | V <sub>DC</sub>    | 40          | 50        | 60        | 100        | 150         | 200        | V    |
| Average Rectified Output Current (Note 1) @T <sub>L</sub> =100℃                                  | I <sub>F(AV)</sub> | 2.0         |           |           |            |             |            | A    |
| Peak forward surge current:8.3ms single half-sine-wave superimposed on rated load (JEDEC Method) | I <sub>FSM</sub>   | 50          |           |           |            |             |            | A    |
| Forward Voltage @I <sub>F</sub> =2.0A                                                            | V <sub>FM</sub>    | 0.45        | 0.55      |           | 0.75       | 0.85        |            | V    |
| Peak Reverse Current @T <sub>A</sub> =25℃                                                        | I <sub>R</sub>     | 0.5         |           |           |            |             |            | mA   |
| At Rated DC Blocking Voltage @T <sub>A</sub> =100℃                                               | I <sub>R</sub>     | 20          |           |           |            |             |            | mA   |
| Typical Junction Capacitance (Note 2)                                                            | C <sub>J</sub>     | 180         |           |           |            |             |            | pF   |
| Typical Thermal Resistance Junction to Ambient(Note 1)                                           | R <sub>θJA</sub>   | 50          |           |           |            |             |            | ℃/W  |
| Operating Temperature Range                                                                      | T <sub>J</sub>     | -55 to +150 |           |           |            | -55 to +175 |            | ℃    |
| Storage Temperature Range                                                                        | T <sub>STG</sub>   | -55 to +175 |           |           |            |             |            | ℃    |

Note: 1.Leads maintained at ambient temperature at a distance of 9.5mm from the case.

2.Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

Dimensions in inches and (millimeters)



### Rating and characteristic curves

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

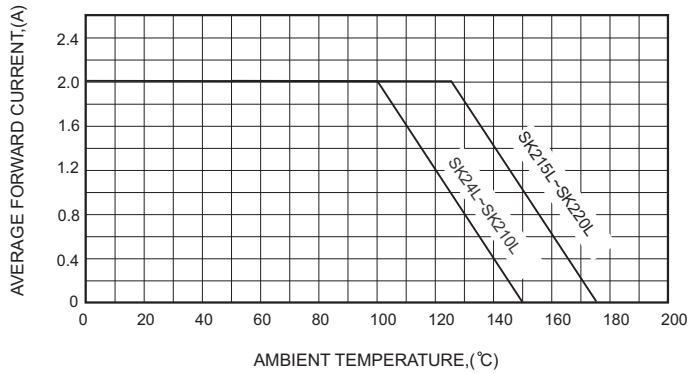


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

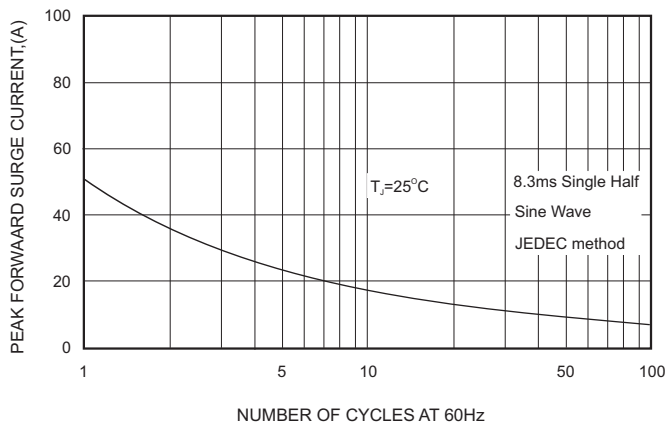


FIG.4-TYPICAL JUNCTION CAPACITANCE

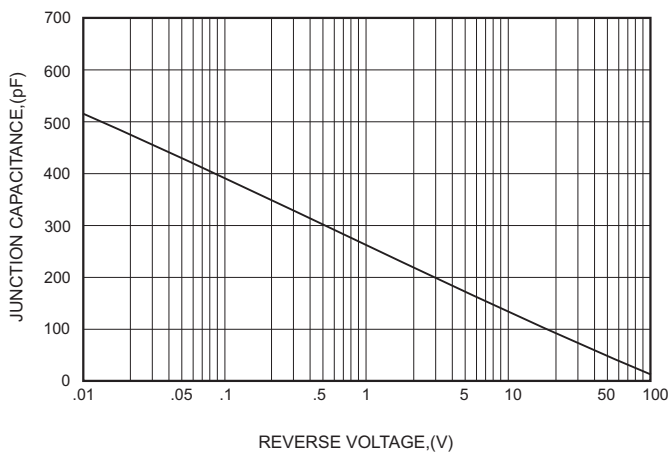


FIG.2-TYPICAL FORWARD CHARACTERISTICS

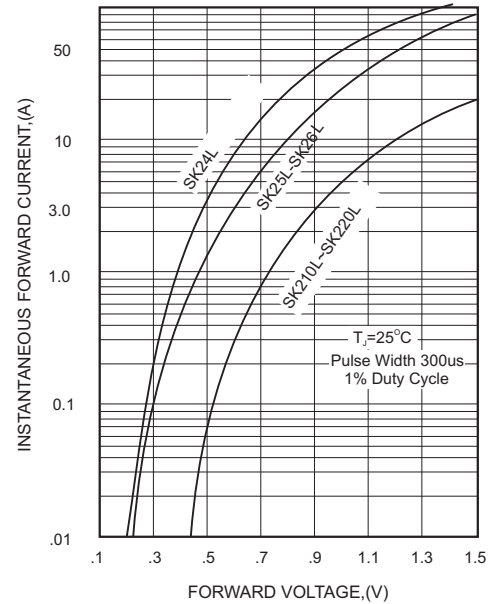
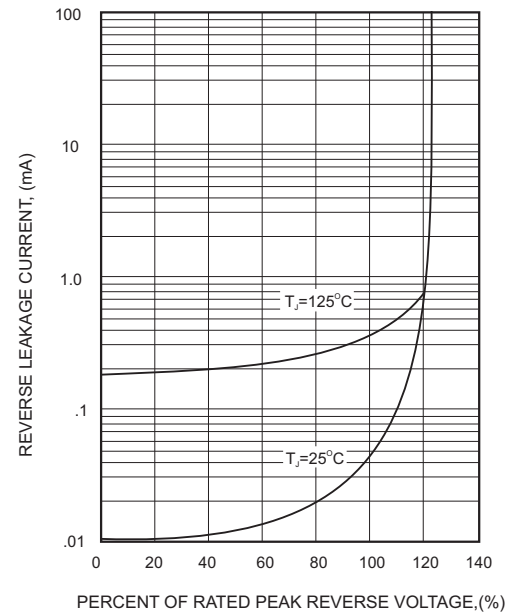
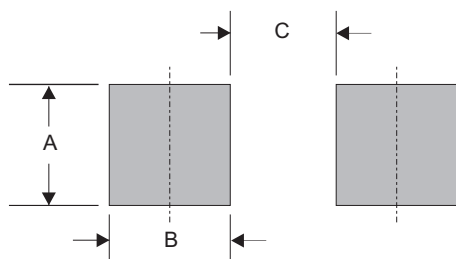


FIG.5 - TYPICAL REVERSE CHARACTERISTICS





### ■ SMA foot print



| A            | B            | C            |
|--------------|--------------|--------------|
| 0.068 (1.70) | 0.104 (2.60) | 0.060 (1.50) |

Dimensions in inches and (millimeters)

| PACKAGE | SPQ/PCS   | CARTON<br>SPQ/PCS | CARTON<br>SIZE/CM | CARTON<br>GW/KG | CARTON<br>NW/KG |
|---------|-----------|-------------------|-------------------|-----------------|-----------------|
| SMA     | 5000/REEL | 80000             | 36X30.6X31        | 12.00           | 11.00           |