



Features

- 3rd generation SiC MOSFET technology
- Optimized package with separate driver source pin
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Halogen free, RoHS compliant

Benefits

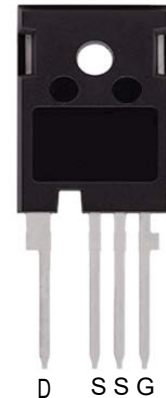
- Reduce switching losses and minimize gate ringing
- Higher system efficiency
- Reduce cooling requirements
- Increase power density
- Increase system switching frequency

Applications

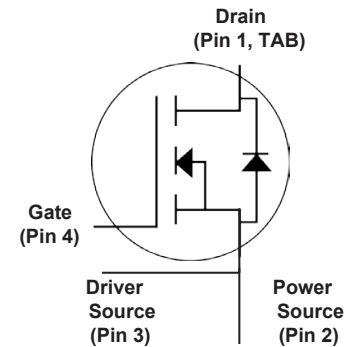
- Renewable energy
- EV battery chargers
- High voltage DC/DC converters
- Switch Mode Power Supplies



Ordering Part Number	Package	Marking
HC1M45065J	TO-247-4L	HC1M45065J



D S S G
TO247-4L
Package



Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current $V_{GS}=20\text{V } T_C = 25^\circ\text{C}$ $V_{GS}=20\text{V } T_C = 100^\circ\text{C}$	I_D	49 35	A
Pulsed drain current ($T_C = 25^\circ\text{C}$, t_p limited by T_{jmax})	$I_{D \text{ pulse}}$	123	A
Avalanche energy, single pulse ($L=10\text{mH}$)	E_{AS}	1000	mJ
Gate-Source voltage	V_{GSOP}	-5/+20	V
Gate-Source voltage (dynamic, Absolute maximum values)	V_{GSmax}	-10/+25	V
Power dissipation ($T_C = 25^\circ\text{C}$)	P_{tot}	85	W
Operating junction and storage temperature	T_J, T_{stg}	-55...+175	$^\circ\text{C}$



Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	1.77	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	62	

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=7mA$
Zero gate voltage drain current	I_{DSS}	-	1	100	μA	$V_{DS}=650V, V_{GS}=0V$ $T_C=25^\circ C$ $T_C=175^\circ C$
Gate-source leakage current	I_{GSS}	-		250	nA	$V_{GS}=20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	45	-	m Ω	$V_{GS}=18V, I_D=17.6A,$
		-	33	49		$V_{GS}=20V, I_D=17.6A,$
		-	50	-		$T_J=25^\circ C$
		-				$T_J=175^\circ C$
Transconductance	g_{fs}	-	5.6	-	S	$V_{DS}=20V, I_D=17.6A$

Dynamic Characteristic

Input Capacitance	C _{iss}	-	1823	-	pF	V _{DS} = 650V V _{GS} = 0V T _J = 25°C
Output Capacitance	C _{oss}	-	190	-		V _{AC} = 25mV
Reverse Transfer Capacitance	C _{rss}	-	19	-		f = 1MHz
Gate Total Charge	Q _G	-	96	-	nC	V _{DS} = 400V V _{GS} = -5/20V I _D = 17.6A
Gate-Source charge	Q _{gs}	25		-		
Gate-Drain charge	Q _{gd}	-	26	-		
Turn-On Switching Energy	E _{ON}	-	188	-	μJ	V _{DD} = 400V V _{GS} = -5/+20V I _D =17.6A R _G =10Ω L = 100uH
Turn-Off Switching Energy	E _{OFF}	-	19	-		
Turn-on delay time	t _{d(on)}	-	20	-	ns	
Rise time	t _r	-	26	-		
Turn-off delay time	t _{d(off)}	-	48 -	-		
Fall time	t _f	-	15	-		
Gate resistance	R _G	-	1.7	-	Ω	V _{AC} = 25mV, f=1MHz



Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}		3.2		V	$V_{GS}=0V, I_{SD}=8.8A, T_J=25^{\circ}C$
			2.6			$V_{GS}=0V, I_{SD}=8.8A, T_J=175^{\circ}C$
Body Diode Reverse Recovery Time	t_{rr}	-	40	-	ns	$V_R = 400V, I_D = 17.6A, di/dt = 1000A/\mu S$
Body Diode Reverse Recovery Charge	Q_{rr}	-	156	-	nC	

Typical Performance Characteristics

Fig 1. Output Characteristic ($T_J=-55^{\circ}C$)

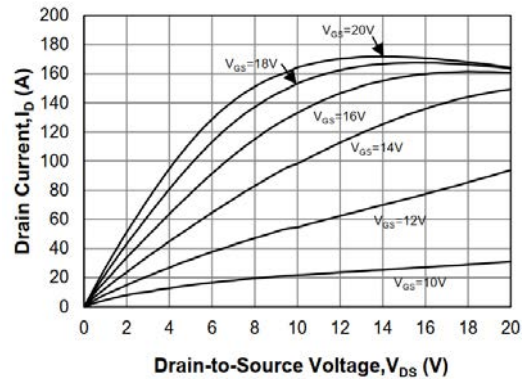


Fig 2. Output Characteristic ($T_J=25^{\circ}C$)

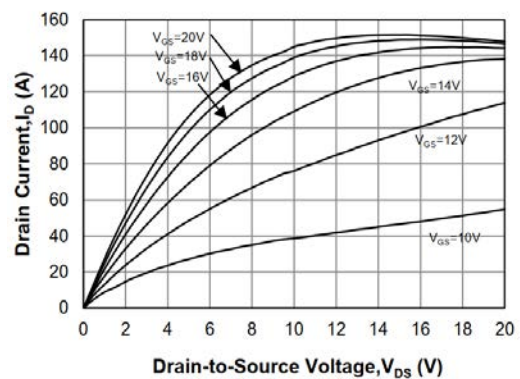


Fig 3. Output Characteristic ($T_J=175^{\circ}C$)

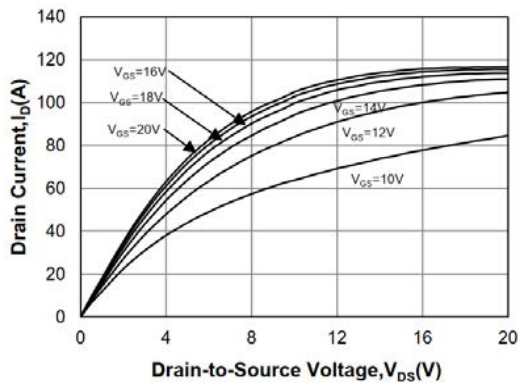


Fig 4: $R_{DS(on)}$ Vs I_{DS} Characteristic

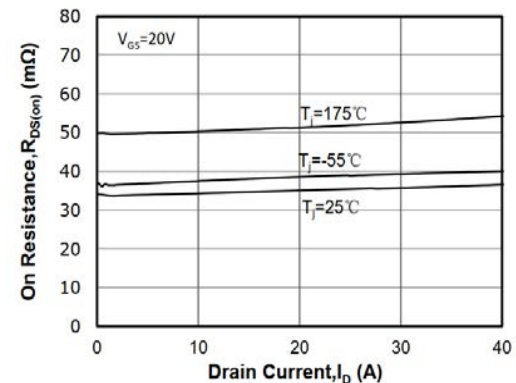




Fig 5: $R_{ds(on)}$ vs. Temperature

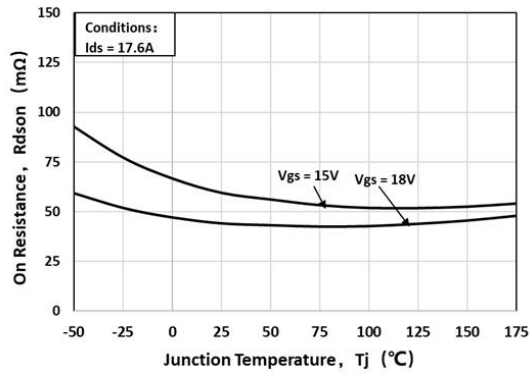


Fig 6: Transfer Characteristic

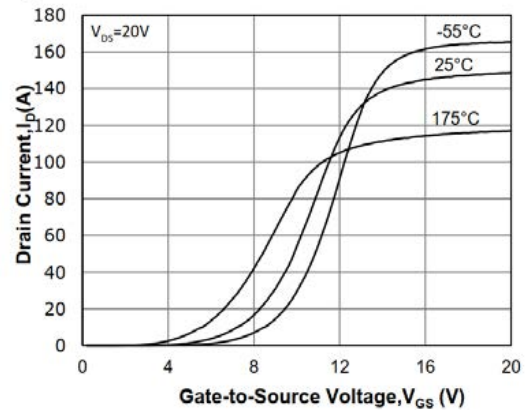


Fig 7: Body-diode Characteristic

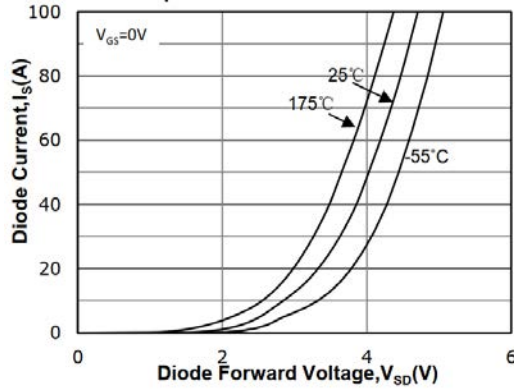


Fig 8: V_{th} Vs T_j Temperature Characteristic

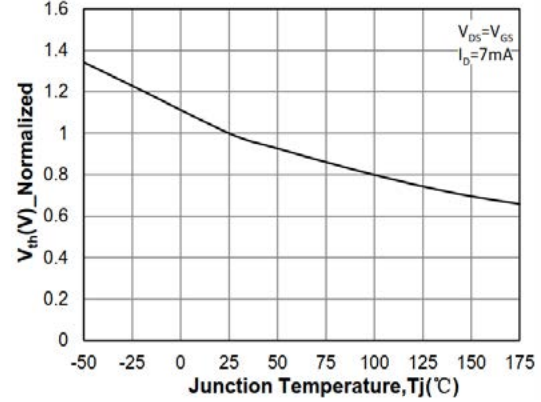


Fig 9: Gate Charge Characteristics

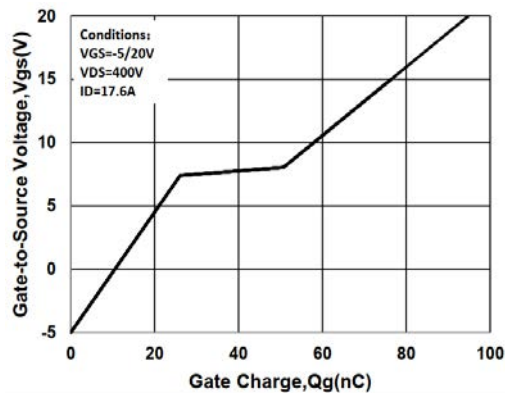


Fig 10: Continuous Drain Current vs. Case Temperature

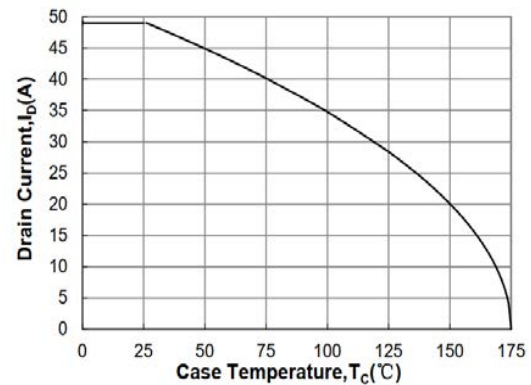




Fig 11: Safe Operating Area

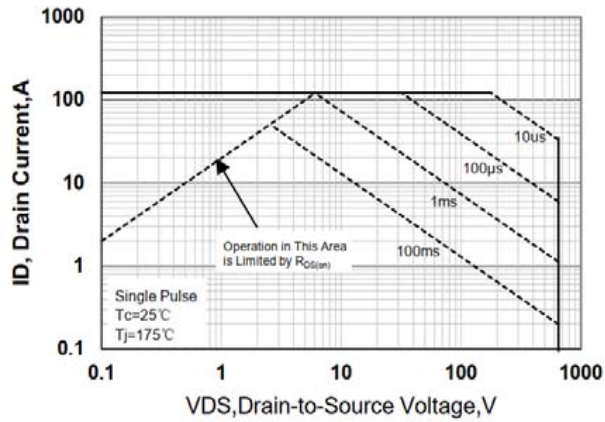


Fig 12: Capacitance Characteristics

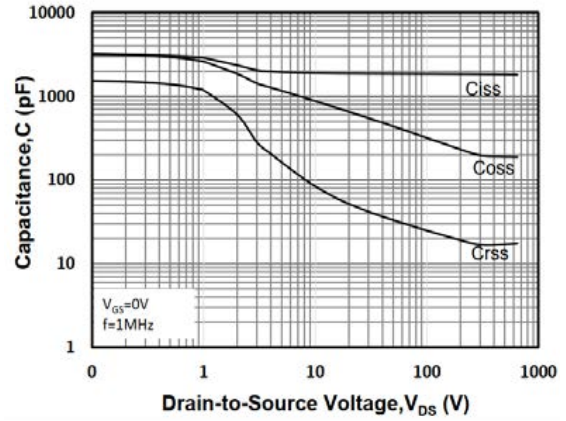
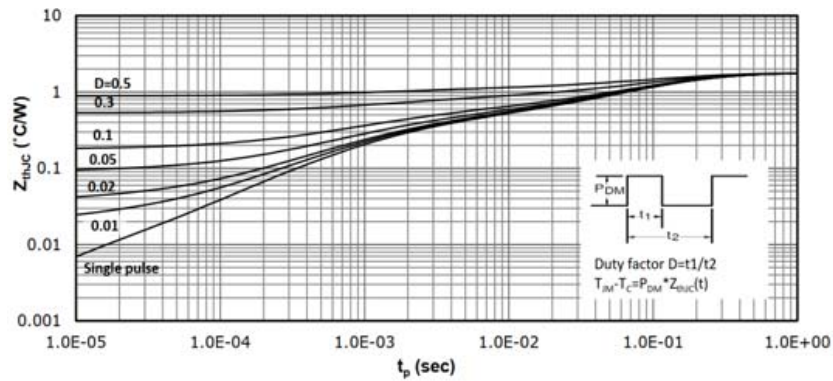


Fig 13: Transient Thermal Impedance





Test Circuit & Waveform

Figure A. Definition of switching times

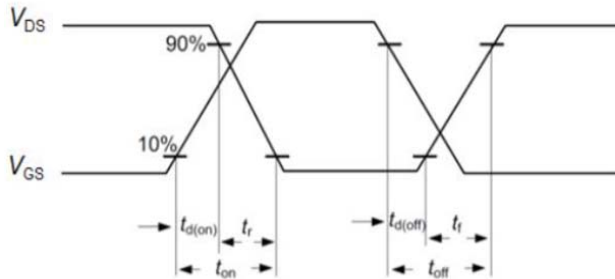


Figure B. Dynamic test circuit

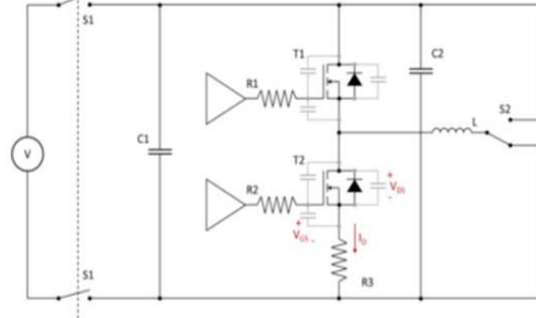
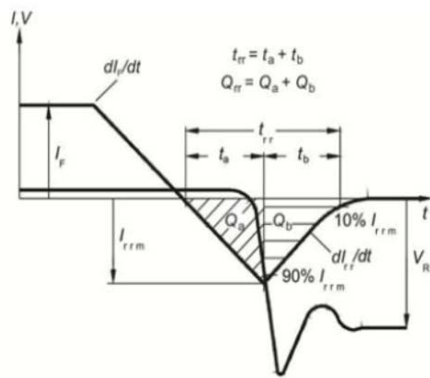


Figure C. Definition of body diodeswitching characteristics

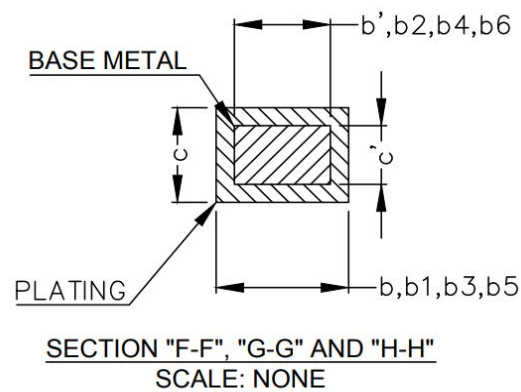
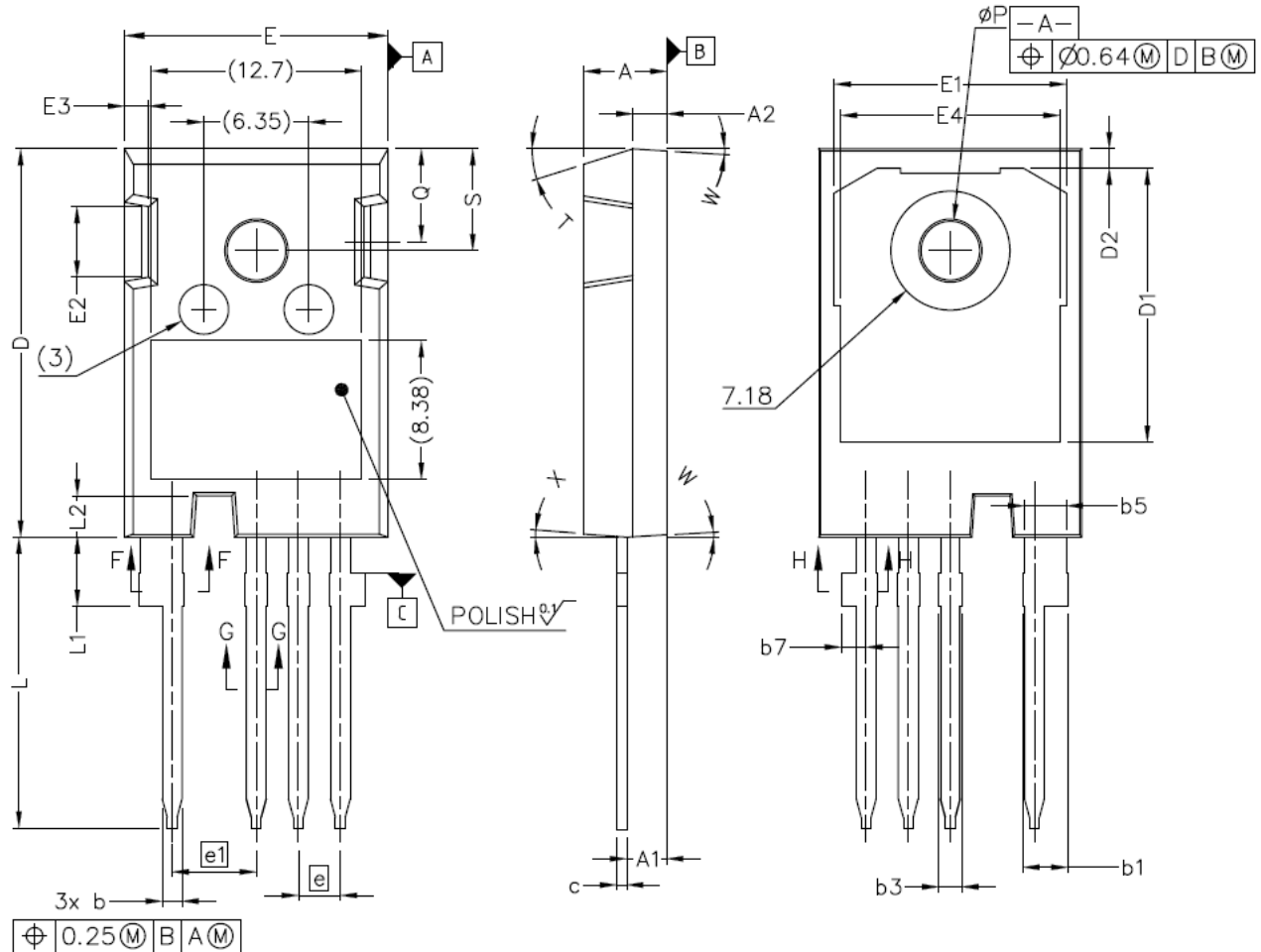




Package

Dimensions Package

TO-247-4L



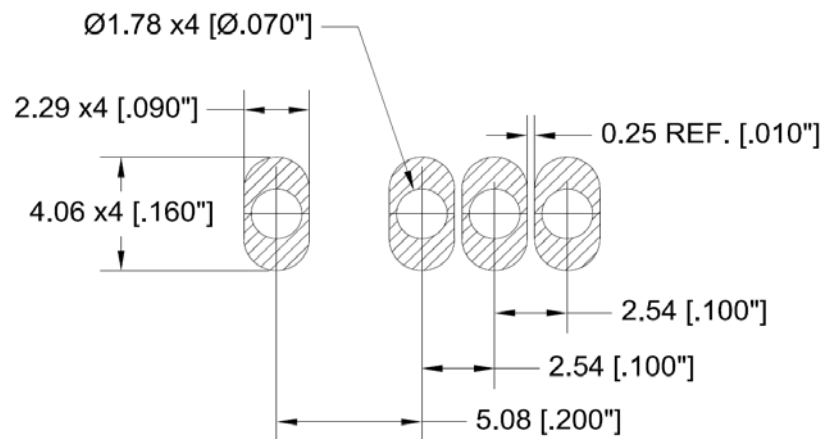


NOTE ;

- 1. ALL METAL SURFACES: TIN PLATED, EXCEPT AREA OF CUT**
- 2. DIMENSIONING & TOLERANCEING CONFIRM TO ASME Y14.5M-1994.**
- 3. ALL DIMENSIONS ARE IN MILLIMETERS.ANGLES ARE IN DEGREES.**
- 4. 'N' IS THE NUMBER OF TERMINAL POSITIONS**

SYM	MILLIMETERS	
	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b`	1.07	1.28
b	1.07	1.33
b1	2.39	2.94
b2	2.39	2.84
b3	1.07	1.60
b4	1.07	1.50
b5	2.39	2.69
b6	2.39	2.64
b7	1.30	1.70
c`	0.55	0.65
c	0.55	0.68
D	23.30	23.60
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.13

SYM	MILLIMETERS	
	MIN	MAX
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	2.54 BSC	
e1	5.08 BSC	
N*	4	
L	17.31	17.82
L1	3.97	4.37
L2	2.35	2.65
Ø P	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5° REF.	
W	3.5° REF.	
X	4° REF.	





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