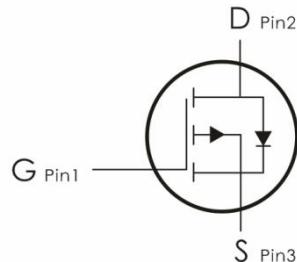
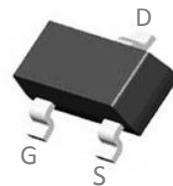


## Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=-60V, I_D=-3.5A, R_{DS(on)}<100m\Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.



## Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DO4P06SA	4P06S	SOT-23-3	3000pcs/Reel

## Absolute Maximum Ratings: ( $T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current $T_A=25^\circ C$	-3.5	A
	Continuous Drain Current $T_A=100^\circ C$	-2.7	
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	-16	
$P_D$	Power Dissipation	2	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55-+150	$^\circ C$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{Theta c}$	Thermal Resistance, Junction to case	3.7	$^\circ C/W$

**Electrical Characteristics:** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
<b>BV<sub>DSS</sub></b>	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250 \mu\text{A}$	-60	-72	---	V
<b>I<sub>DSS</sub></b>	Zero Gate Voltage Drain Current	$V_{GS}=0\text{V}, V_{DS}=-60\text{V}$	---	---	-1	$\mu\text{A}$
<b>I<sub>GSS</sub></b>	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
<b>V<sub>GS(th)</sub></b>	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250 \mu\text{A}$	-1	-1.8	-2.5	V
<b>R<sub>DS(on)</sub></b>	Drain-Source On Resistance	$V_{GS}=-10\text{V}, I_D=-10\text{A}$	---	90	100	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}, I_D=-8\text{A}$	---	100	130	
<b>Dynamic Characteristics</b>						
<b>C<sub>iss</sub></b>	Input Capacitance	$V_{DS}=-25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	---	1449	---	$\text{pF}$
<b>C<sub>oss</sub></b>	Output Capacitance		---	47	--	
<b>C<sub>rss</sub></b>	Reverse Transfer Capacitance		---	34	---	
<b>Switching Characteristics</b>						
<b>t<sub>d(on)</sub></b>	Turn-On Delay Time	$V_{DS}=-30\text{V}, R_{ENG}=3 \Omega, V_{GS}=-10\text{V}$	---	9.6	---	ns
<b>t<sub>r</sub></b>	Rise Time		---	5.4	---	ns
<b>t<sub>d(off)</sub></b>	Turn-Off Delay Time		---	28	---	ns
<b>t<sub>f</sub></b>	Fall Time		---	5	---	ns
<b>Q<sub>g</sub></b>	Total Gate Charge	$V_{GS}=-10\text{V}, V_{DS}=-30\text{V}, I_D=-10\text{A}$	---	23.6	---	nc
<b>Q<sub>gs</sub></b>	Gate-Source Charge		---	2	---	nc
<b>Q<sub>gd</sub></b>	Gate-Drain "Miller" Charge		---	7.1	---	nc
<b>Drain-Source Diode Characteristics</b>						
<b>I<sub>s</sub></b>	Continuous Drain Current	$VD=VG=0\text{V}$	---	---	-3.5	A
<b>I<sub>SM</sub></b>	Pulsed Drain Current		---	---	-16	A
<b>T<sub>rr</sub></b>	Reverse Recovery Time	$I_F=-10\text{A}, dI/dt=100\text{A/us}$	---	34	---	ns
<b>Q<sub>rr</sub></b>	Reverse Recovery Charge		---	37	---	nc
<b>V<sub>SD</sub></b>	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0\text{V}, I_{SD}=-10\text{A}$	---	---	-1.2	V

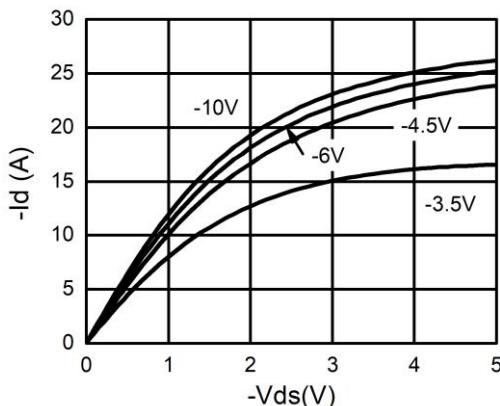
**Notes:**

1.Repetitive Rating: Pulse width limited by maximum junction temperature.

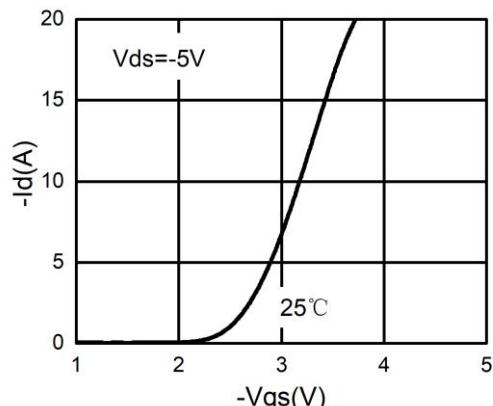
2.EAS condition:  $T_j=25^\circ\text{C}$ ,  $V_{DD}=40\text{V}$ ,  $V_G=-10\text{V}$ ,  $R_g=25\Omega$ ,  $L=0.5\text{mH}$ .

3.Repetitive Rating: Pulse width limited by maximum junction temperature.

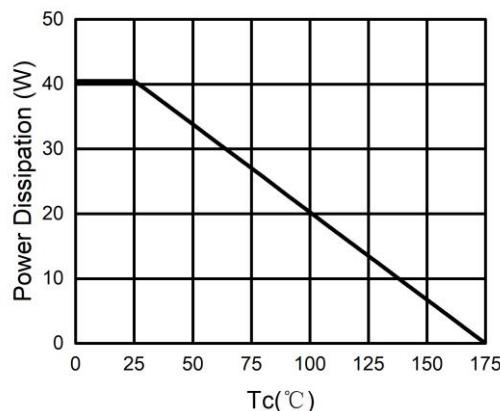
**Typical Characteristics:** ( $T_A=25^\circ\text{C}$  unless otherwise noted)



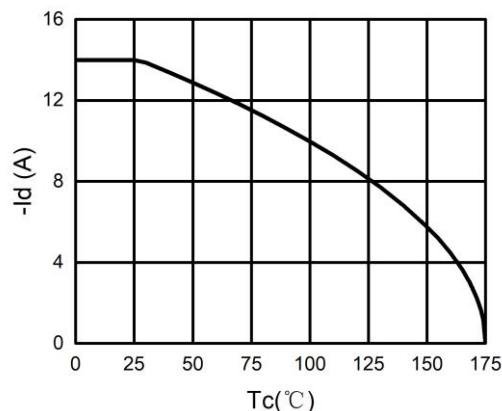
**Figure 1. Output Characteristics**



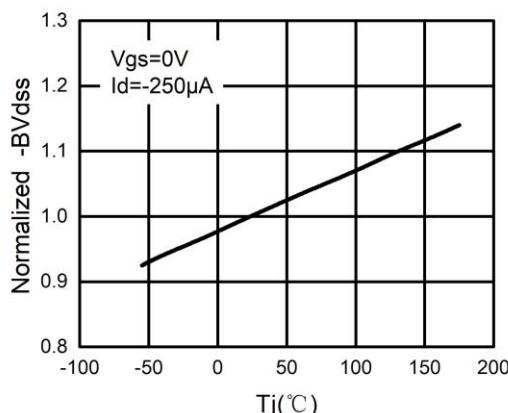
**Figure 2. Transfer Characteristics**



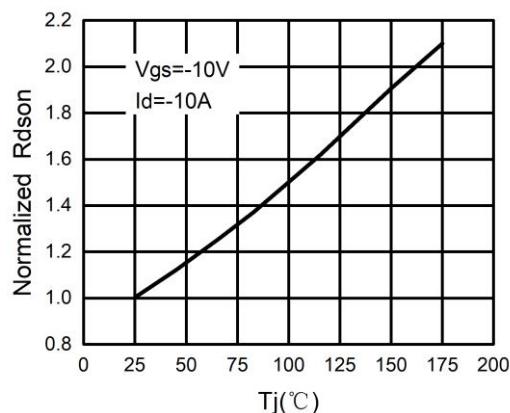
**Figure 3. Power Dissipation**



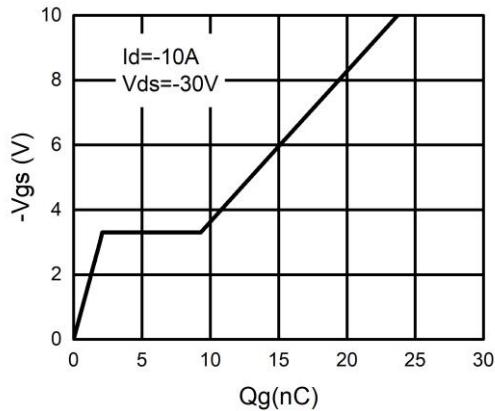
**Figure 4. Drain Current**



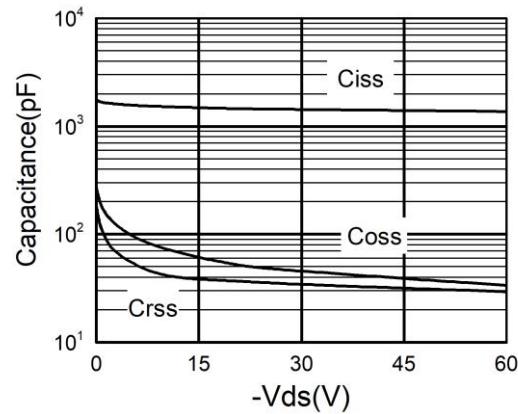
**Figure 5.  $\text{BV}_{\text{DSS}}$  vs Junction Temperature**



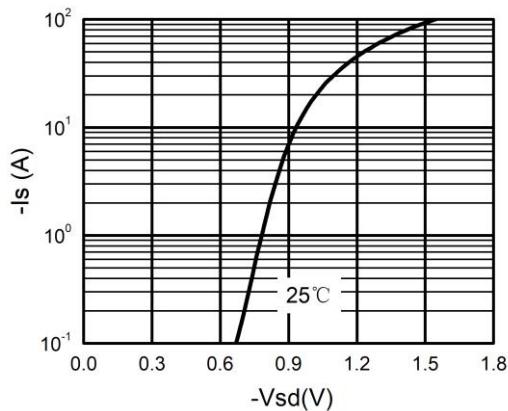
**Figure 6.  $R_{DS(\text{ON})}$  vs Junction Temperature**



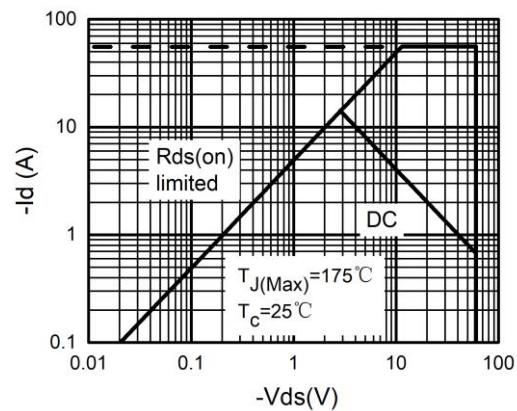
**Figure 7. Gate Charge Waveforms**



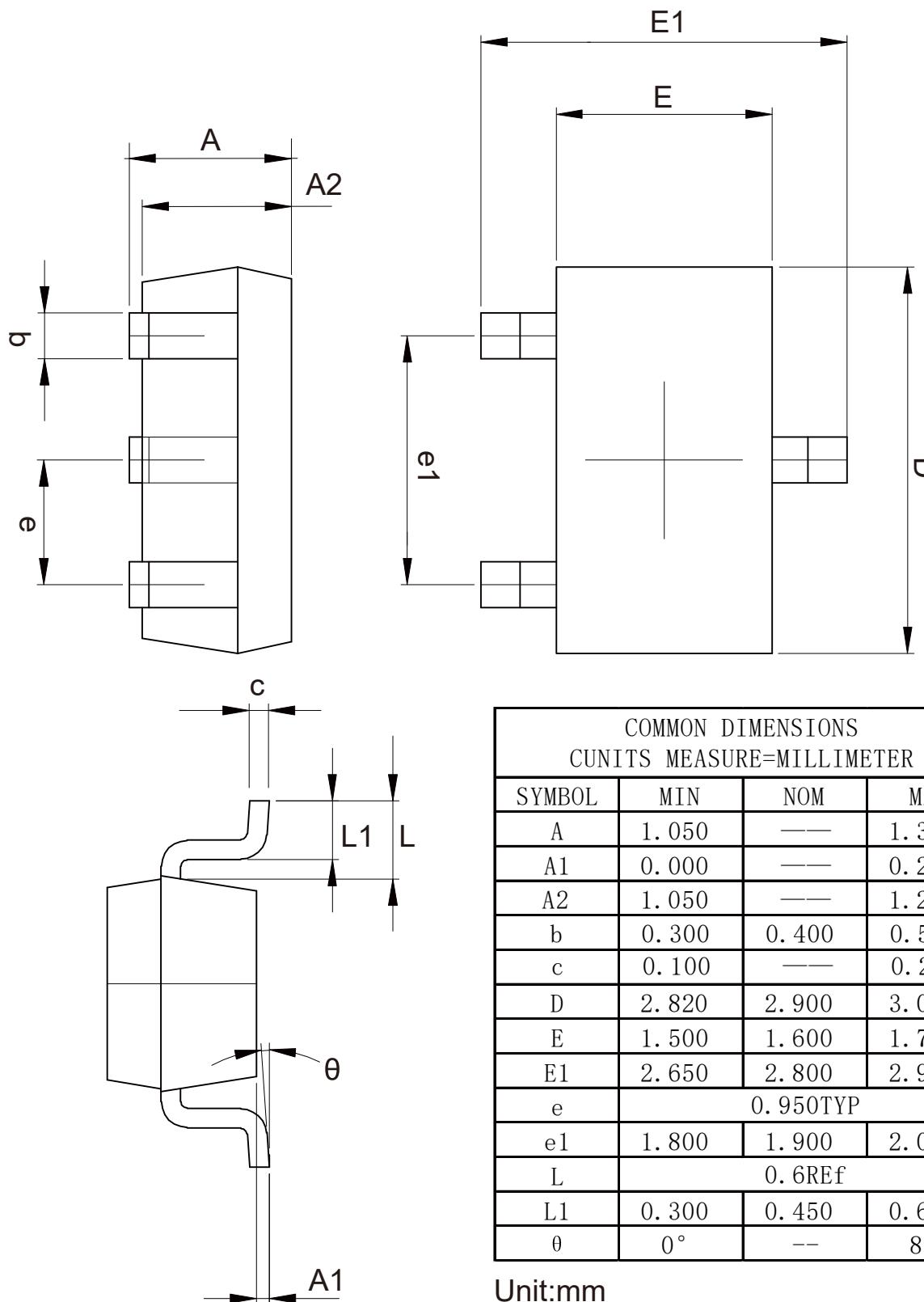
**Figure 8. Capacitance**



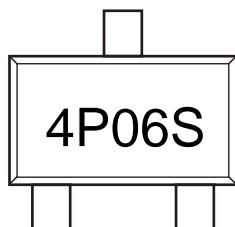
**Figure 9. Body-Diode Characteristics**



**Figure 10. Maximum Safe Operating Area**

**SOT-23-3Package Outline Data**


## Marking Information:



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