

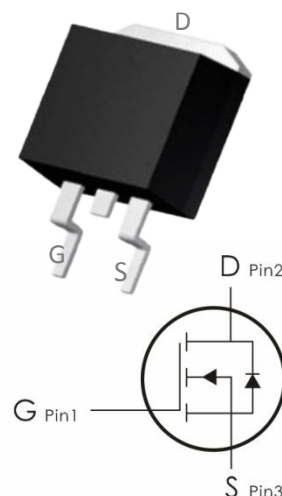
## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge.

It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=60V, I_D=80A, R_{DS(on)} < 7.5m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.



## Package Marking and Ordering Information:

| Part NO. | Marking | Package | Packing      |
|----------|---------|---------|--------------|
| DOB80N06 | 80N06   | TO- 263 | 800 pcs/Reel |

## Absolute Maximum Ratings: ( $T_C=25^\circ C$ unless otherwise noted)

| Symbol         | Parameter                                                | Ratings  | Units      |
|----------------|----------------------------------------------------------|----------|------------|
| $V_{DS}$       | Drain-Source Voltage                                     | 60       | V          |
| $V_{GS}$       | Gate-Source Voltage                                      | $\pm 20$ | V          |
| $I_D$          | Continuous Drain Current <sup>2</sup>                    | 80       | A          |
|                | Continuous Drain Current- $T_C=100^\circ C$ <sup>2</sup> | 51       |            |
| $I_{DM}$       | Pulsed Drain Current <sup>3</sup>                        | 320      |            |
| $P_D$          | Power Dissipation                                        | 83       | W          |
| $E_{AS}$       | Single pulse avalanche energy <sup>1</sup>               | 244      | mJ         |
| $T_J, T_{STG}$ | Operating and Storage Junction Temperature Range         | -55-+150 | $^\circ C$ |

## Thermal Characteristics:

| Symbol          | Parameter                               | Max | Units        |
|-----------------|-----------------------------------------|-----|--------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case    | 1.5 | $^\circ C/W$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | 62  | $^\circ C/W$ |

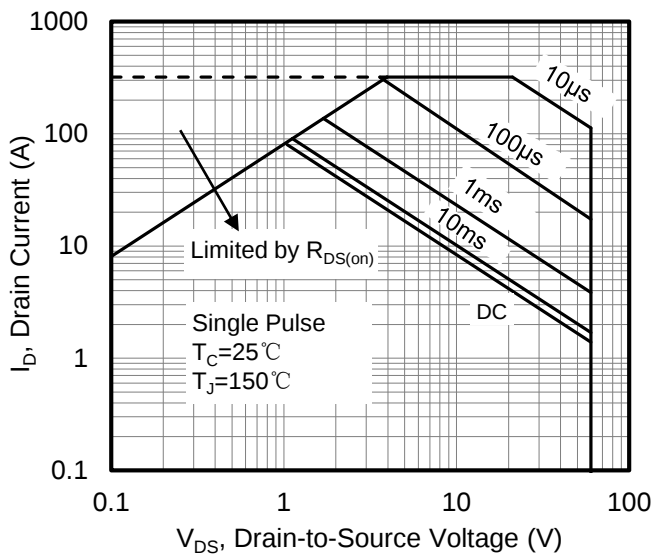
**Electrical Characteristics:** ( $T_C=25^{\circ}\text{C}$  unless otherwise noted)

| Symbol                             | Parameter                       | Conditions                                                                                | Min | Typ  | Max   | Units |
|------------------------------------|---------------------------------|-------------------------------------------------------------------------------------------|-----|------|-------|-------|
| Off Characteristics                |                                 |                                                                                           |     |      |       |       |
| BV <sub>DSS</sub>                  | Drain-Source Breakdown Voltage  | V <sub>GS</sub> =0V, I <sub>D</sub> =250 μ A                                              | 60  | ---  | ---   | V     |
| I <sub>DSS</sub>                   | Zero Gate Voltage Drain Current | V <sub>GS</sub> =0V, V <sub>DS</sub> =60V                                                 | --- | ---  | 1     | μ A   |
| I <sub>GSS</sub>                   | Gate-Source Leakage Current     | V <sub>GS</sub> =± 20V, V <sub>DS</sub> =0A                                               | --- | ---  | ± 100 | nA    |
| On Characteristics                 |                                 |                                                                                           |     |      |       |       |
| V <sub>GS(th)</sub>                | GATE-Source Threshold Voltage   | V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250 μ A                                | 2   | 2.8  | 4     | V     |
| R <sub>DS(ON)</sub>                | Drain-Source On Resistance      | V <sub>GS</sub> =10V, I <sub>D</sub> =20A                                                 | --- | 5.8  | 7.5   | m Ω   |
| Dynamic Characteristics            |                                 |                                                                                           |     |      |       |       |
| C <sub>iss</sub>                   | Input Capacitance               | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz                                         | --- | 4008 | ---   | pF    |
| C <sub>oss</sub>                   | Output Capacitance              |                                                                                           | --- | 242  | --    |       |
| C <sub>rss</sub>                   | Reverse Transfer Capacitance    |                                                                                           | --- | 200  | ---   |       |
| Switching Characteristics          |                                 |                                                                                           |     |      |       |       |
| t <sub>d(on)</sub>                 | Turn-On Delay Time              | V <sub>DS</sub> =30V, I <sub>D</sub> =20A,<br>R <sub>ENG</sub> =3 Ω ,V <sub>GS</sub> =10V | --- | 18   | ---   | ns    |
| t <sub>r</sub>                     | Rise Time                       |                                                                                           | --- | 41   | ---   | ns    |
| t <sub>d(off)</sub>                | Turn-Off Delay Time             |                                                                                           | --- | 47   | ---   | ns    |
| t <sub>f</sub>                     | Fall Time                       |                                                                                           | --- | 28   | ---   | ns    |
| Q <sub>g</sub>                     | Total Gate Charge               | V <sub>GS</sub> =10V, V <sub>DS</sub> =30V,<br>I <sub>D</sub> =20A                        | --- | 75   | ---   | nc    |
| Q <sub>gs</sub>                    | Gate-Source Charge              |                                                                                           | --- | 18   | ---   | nc    |
| Q <sub>gd</sub>                    | Gate-Drain “Miller” Charge      |                                                                                           | --- | 4.2  | ---   | nc    |
| Drain-Source Diode Characteristics |                                 |                                                                                           |     |      |       |       |
| V <sub>SD</sub>                    | Diode Forward Voltage           | V <sub>GS</sub> =0V, I <sub>SD</sub> =20A                                                 | --- | ---  | 1.2   | V     |
| I <sub>S</sub>                     | Continuous Drain Curren         | V <sub>D</sub> =V <sub>G</sub> =0V                                                        | --- | ---  | 80    | A     |
| I <sub>SM</sub>                    | Pulsed Drain Current            |                                                                                           | --- | ---  | 320   | A     |
| T <sub>rr</sub>                    | Reverse Recovery Time           | I <sub>F</sub> =20A, T <sub>J</sub> =25 °C                                                | --- | 28   | ---   | ns    |
| Q <sub>rr</sub>                    | Reverse Recovery Charge         | dI/dt=100A/us                                                                             | --- | 52   | ---   | nc    |

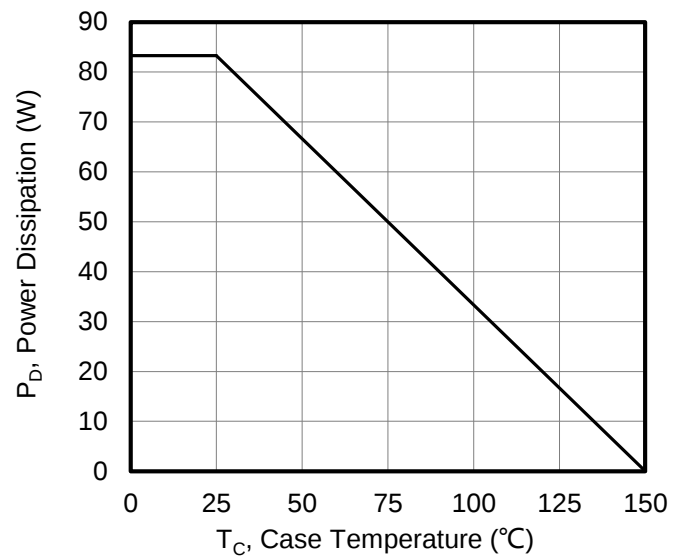
**Notes:**

1.  $L=0.5mH, V_{DD}=30V$ , Start  $T_J=25^{\circ}\text{C}$ .
2. Limited by maximum junction temperature.
3. Repetitive Rating: Pulse width limited by maximum junction temperature.

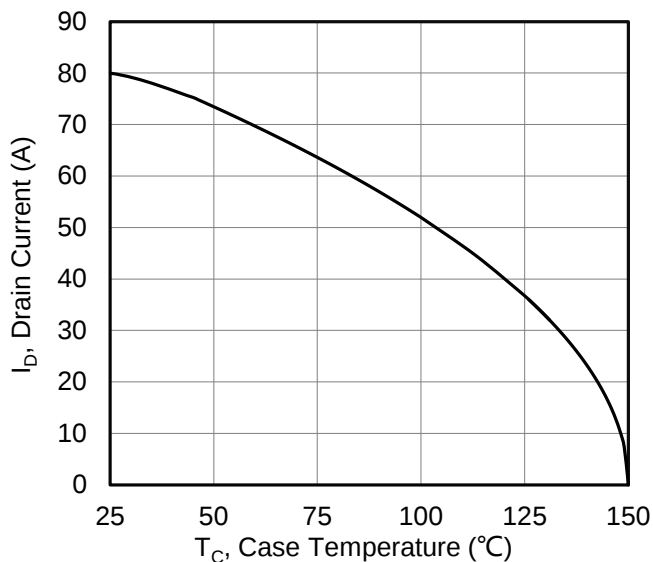
**Typical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)



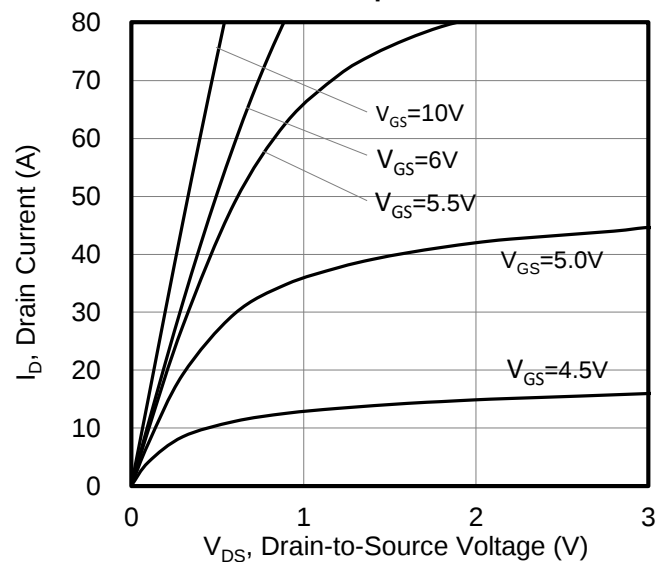
**Figure 1. Maximum Safe Operating Area**



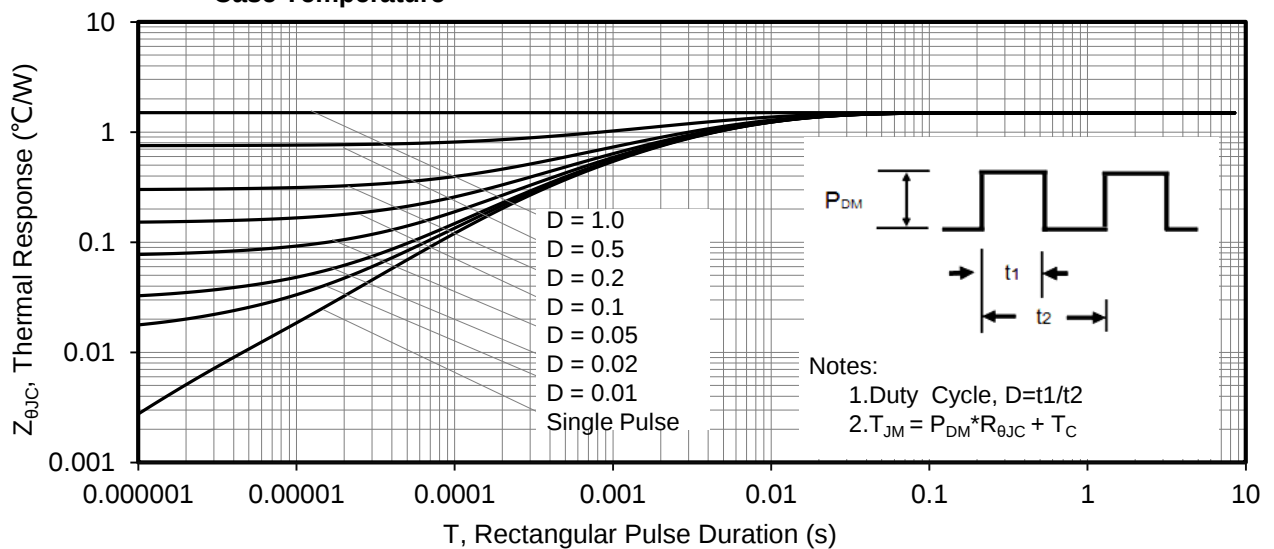
**Figure 2. Maximum Power Dissipation vs. Case Temperature**



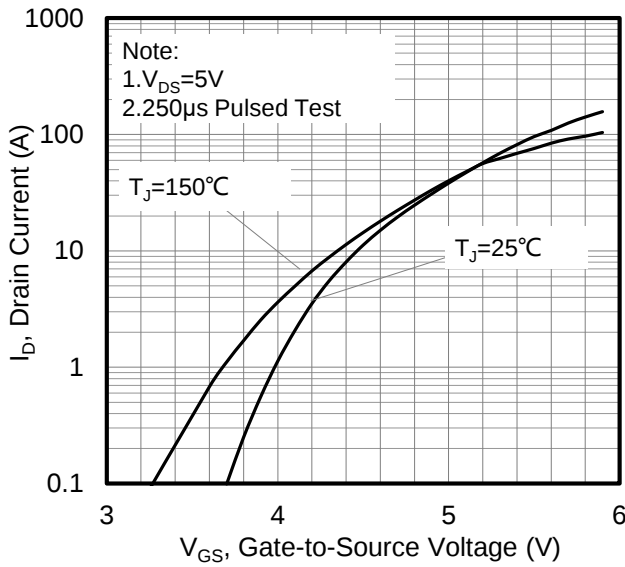
**Figure 3. Maximum Continuous Drain Current vs. Case Temperature**



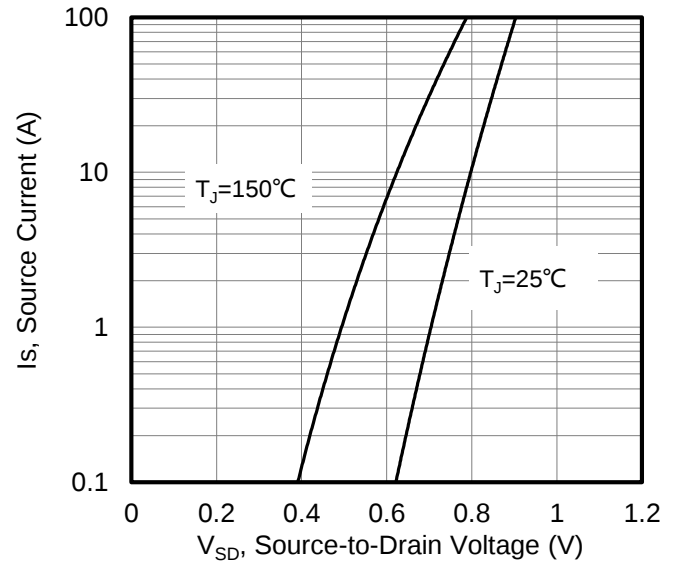
**Figure 4. Typical output Characteristics**



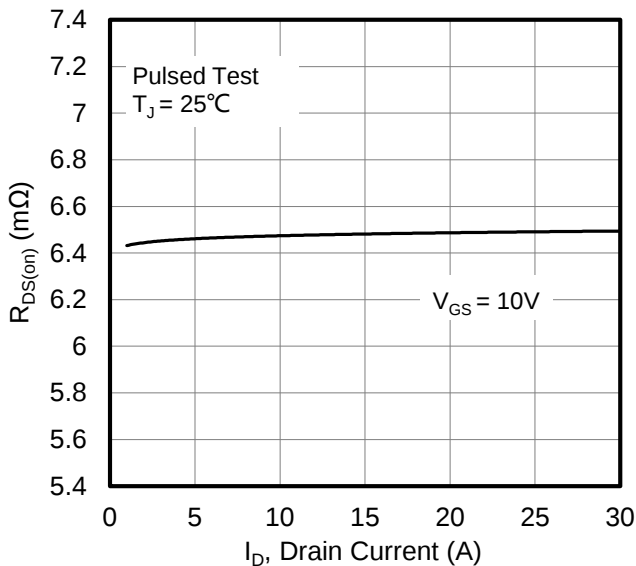
**Figure 5. Maximum Effective Thermal Impedance, Junction to Case**



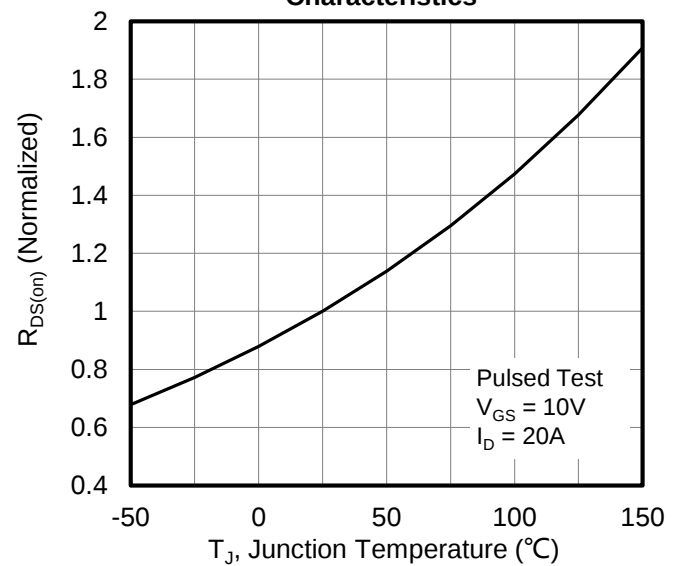
**Figure 6. Typical Transfer Characteristics**



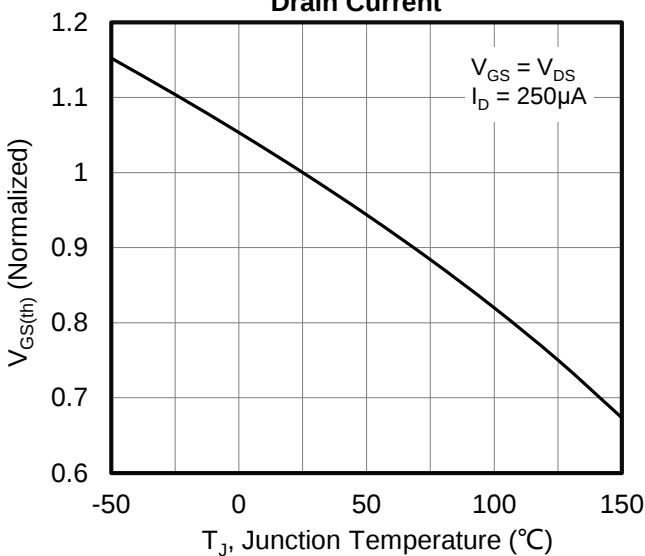
**Figure 7. Typical Body Diode Transfer Characteristics**



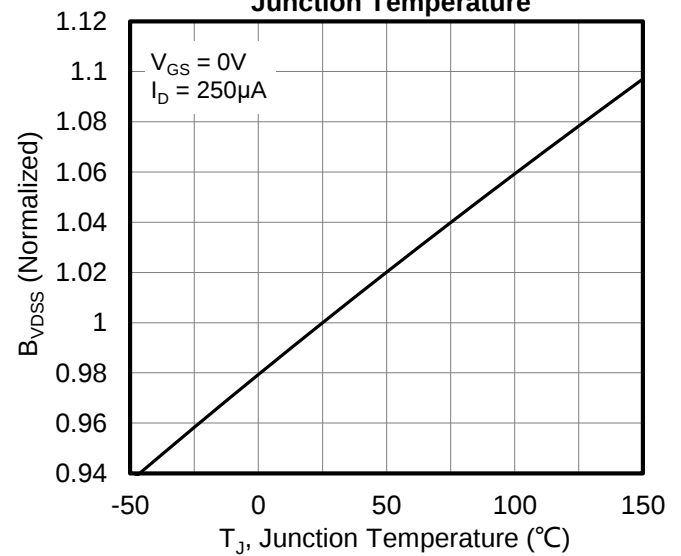
**Figure 8. Drain-to-Source On Resistance vs. Drain Current**



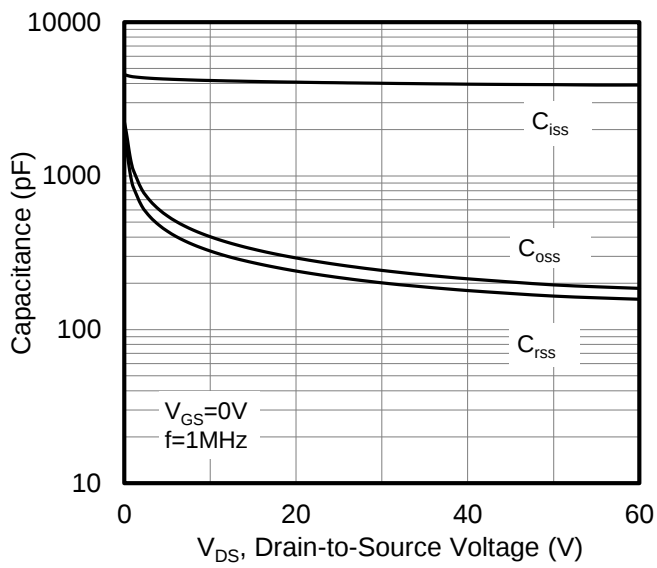
**Figure 9. Normalized On Resistance vs. Junction Temperature**



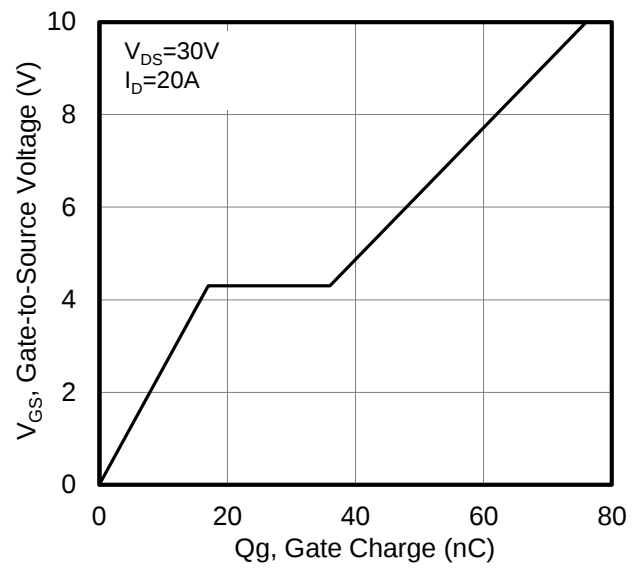
**Figure 10. Normalized Threshold Voltage vs. Junction Temperature**



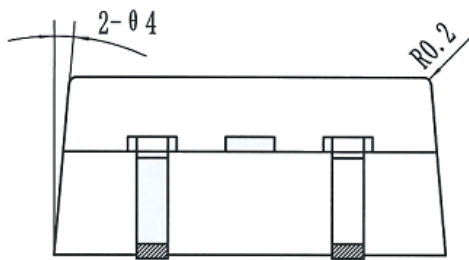
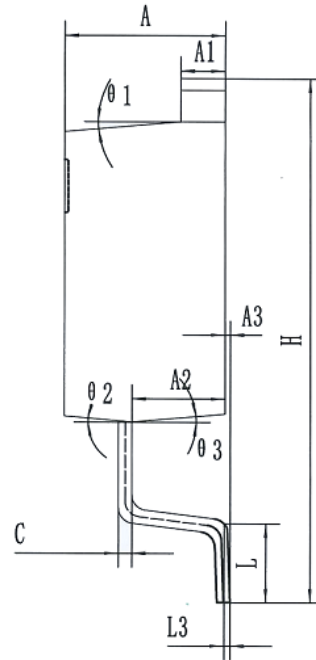
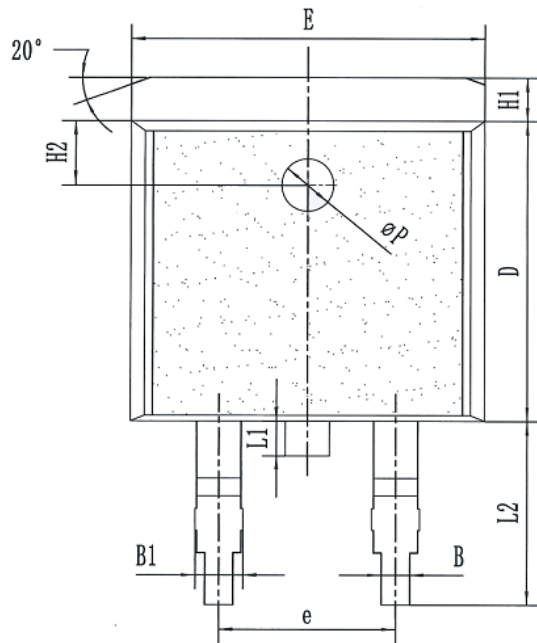
**Figure 11. Normalized Breakdown Voltage vs. Junction Temperature**



**Figure 12. Capacitance Characteristics**



**Figure 13. Typical Gate Charge vs Gate to Source Voltage**

**TO-263 Package Information:**

**COMMON DIMENSIONS**

| SYMBOL | MM       |       |       |
|--------|----------|-------|-------|
|        | MIN      | NOM   | MAX   |
| A      | 4.50     | 4.60  | 4.70  |
| A1     | 1.22     | 1.27  | 1.32  |
| A2     | 2.57     | 2.67  | 2.77  |
| A3     | 0.00     |       | 0.15  |
| B      | 0.76     | 0.81  | 0.87  |
| B1     | 1.32     | 1.37  | 1.42  |
| C      | 0.33     | 0.38  | 0.43  |
| D      | 8.55     | 8.65  | 8.75  |
| e      | 5.08 BSC |       |       |
| E      | 10.06    | 10.16 | 10.26 |
| H      | 14.80    | 15.00 | 15.20 |
| H1     | 1.17     | 1.27  | 1.37  |
| H2     | 1.85 REF |       |       |
| L      | 2.09     | 2.39  | 2.69  |
| L1     | 0.80     | 1.00  | 1.20  |
| L2     | 4.88     | 5.08  | 5.28  |
| L3     | 0.25 REF |       |       |
| φP     | 1.40     | 1.50  | 1.60  |
| θ 1    | 3°       | 5°    | 7°    |
| θ 2    | 3°       | 5°    | 7°    |
| θ 3    | 3°       | 5°    | 7°    |
| θ 4    | 3°       | 5°    | 7°    |

Unit:mm

**Marking Information:**

①. Doingter LOGO

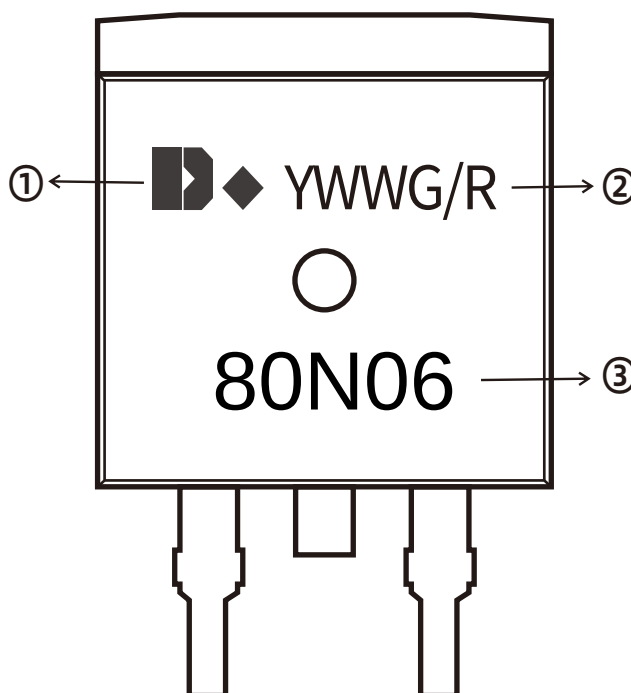
②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)

③. Part NO.

**Attention :**

- Information furnished in this document is believed to be accurate and reliable. However, Shenzhen Doingter Semiconductor Co.,Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.
  - Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Shenzhen Doingter complies with the agreement. Products and information provided in this document have no infringement of patents.
  - Shenzhen Doingter assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.
-  Is a registered trademark of Shenzhen Doingter Semiconductor Co., Ltd. Copyright © 2013 Shenzhen Doingter Semiconductor Co.,Ltd. Printed All rights reserved.