HSK83

Silicon Epitaxial Planar Diode for High Voltage Switching

REJ03G0191-0400Z
(Previous: ADE-208-169C)
Rev.4.00
Mar.22.2004

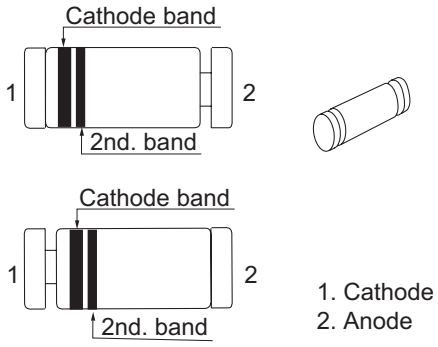
Features

- High reverse voltage. (V_R = 250 V)
- LLD package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Cathode band	2nd band	Package Code
HSK83	White	Verdure	LLD

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	300	V
Reverse voltage	V_R	250	V
Peak forward current	I_{FM}	625	mA
Non-Repetitive peak forward surge current	I_{FSM} *1	1	A
Average rectified current	I_O	150	mA
Junction temperature	Tj	175	°C
Storage temperature	Tstg	–65 to +175	°C

Note: 1. Value at duration of 1s.

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	1.0	V	I_F = 100 mA
Reverse current	I_{R1}	—	—	0.1	µA	V_R = 250 V
	I_{R2}	—	—	100		V_R = 300 V
Capacitance	C	—	1.5	—	pF	V_R = 0 V, f = 1 MHz
Reverse recovery time	t_{rr}	—	—	100	ns	I_F = I_R = 30 mA, I_{rr} = 3 mA, R_L = 100 Ω

Main Characteristics

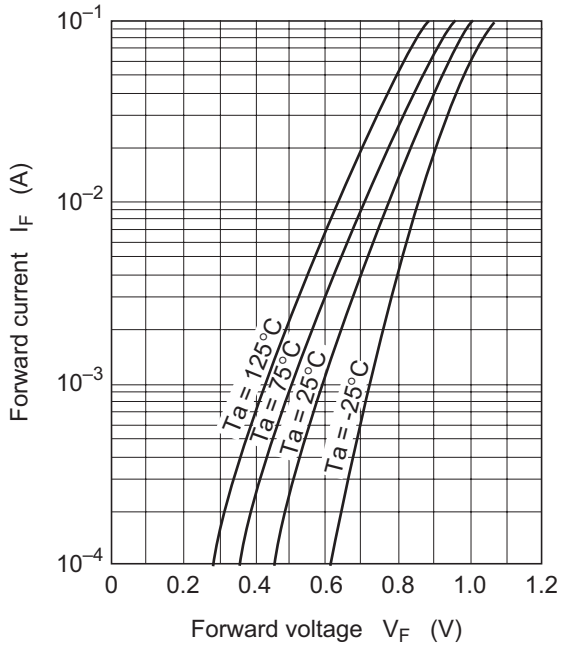


Fig.1 Forward current vs. Forward voltage

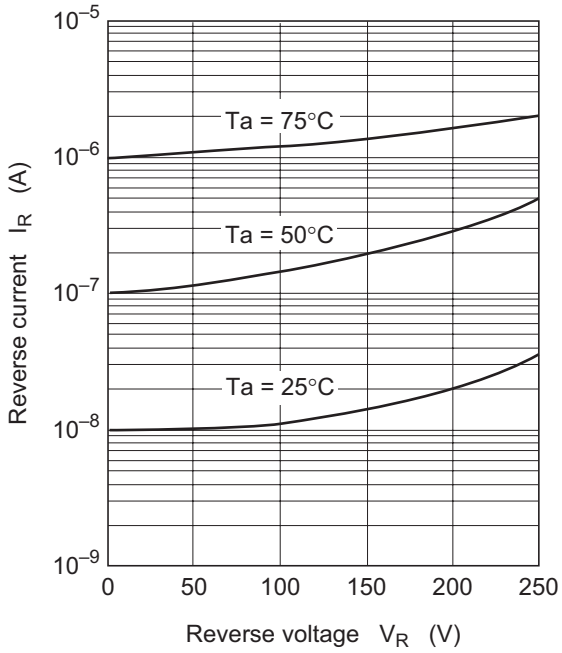


Fig.2 Reverse current vs. Reverse voltage

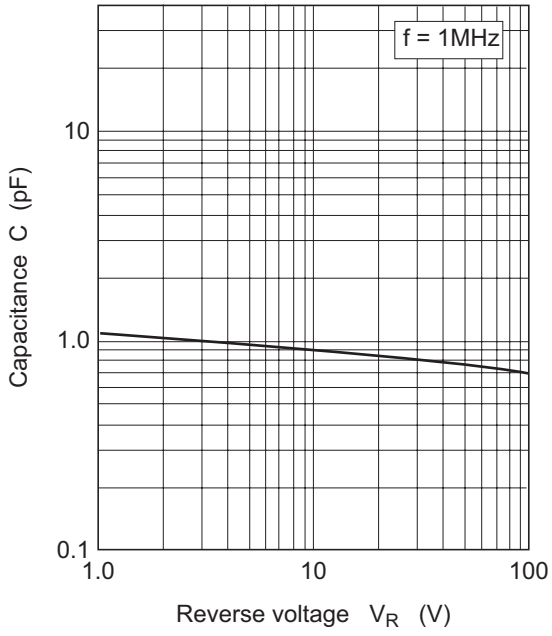
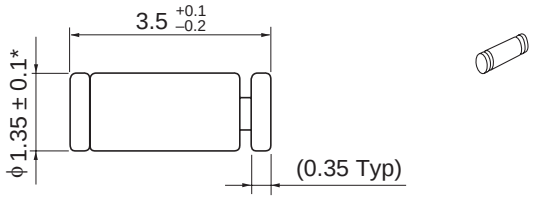


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

As of January, 2003

Unit: mm



Package Code	LLD
JEDEC	—
JEITA	—
Mass (reference value)	0.027 g

* HSK122: φ 1.4 ± 0.1 type

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