M65845AFP

Digital Echo with Microphone Mixing Circuit

REJ03F0170-0201
Rev.2.01
Jan 25, 2007

Description

The M65845AFP is a CMOS IC built-in digital echo function with microphone peripheral circuits for Karaoke equipment packed in a single chip.

It is suitable for Karaoke equipments such as video CD player, mini stereo, CD-radio cassette, TV or VCR.

Being pin compatible with the M65845FP, the M65845AFP is suitable for upgrading the series.

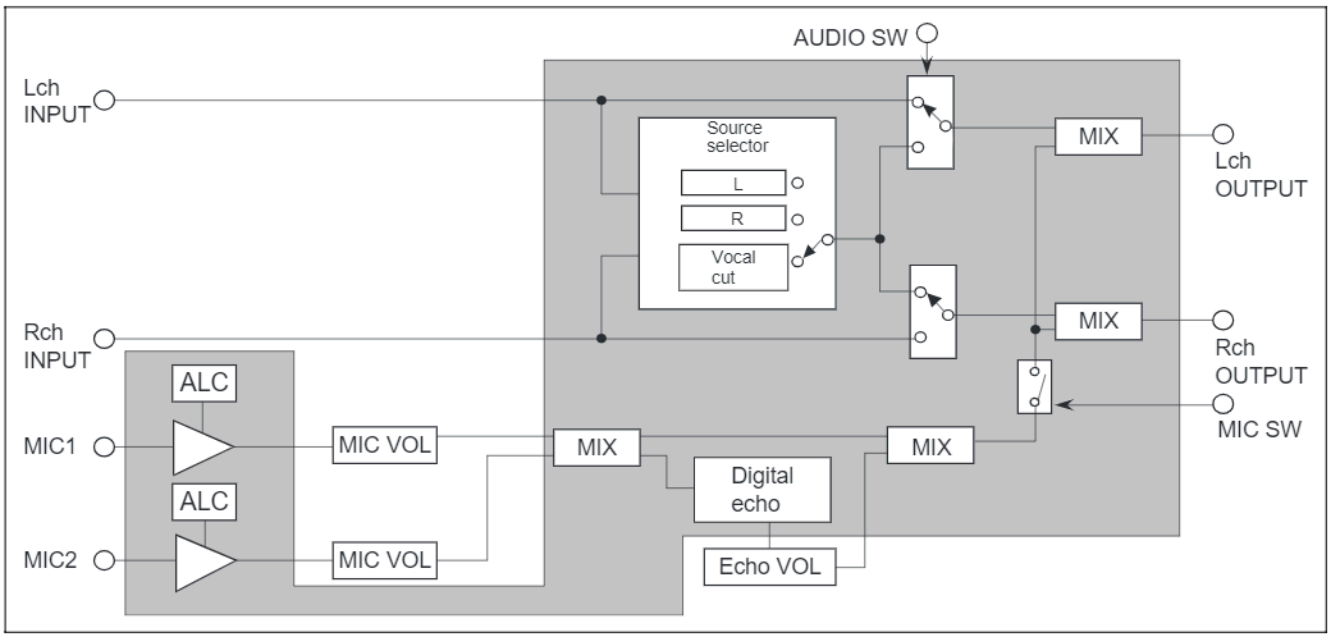
Features

- High performance digital echo circuit thanks to 16 Kbit memory
- Two microphone-mixing lines, vocal cut circuit, digital echo, and line-mixing amplifier are contained, enabling single-chip package of microphone peripheral circuit of Karaoke equipment.
- ALC-equipped microphone amplifiers permit excessively high-input. ALC operating voltage can be set as desired.
- Vocal cut circuit of complete stereo construction
- Compatibility with the M65845FP
- Built-in current-control oscillation circuit
- Built-in automatic reset circuit activated with power on
- Single power supply (5 V)

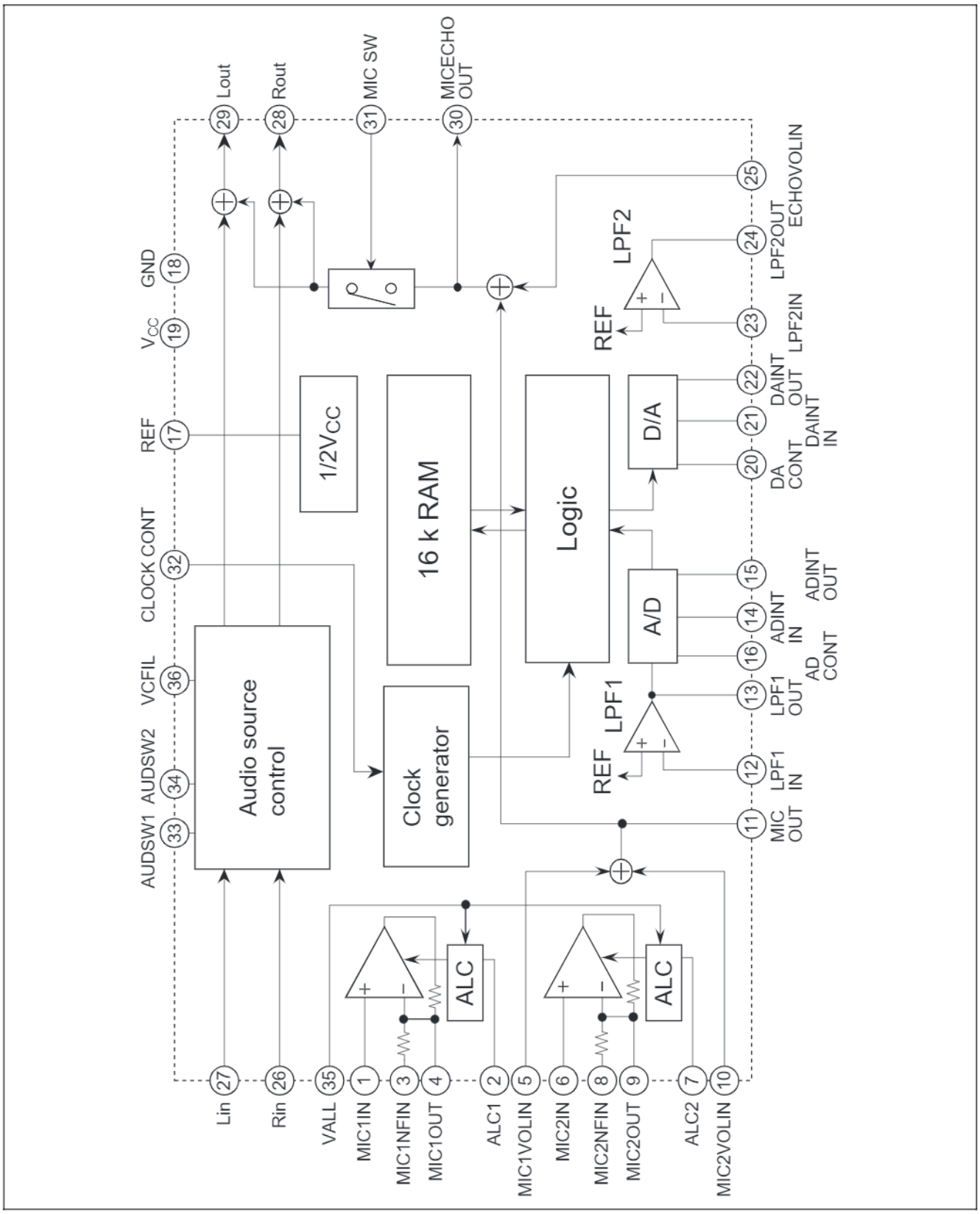
Recommended Operating Condition

- Supply voltage range: V_{CC} = 4.5 V to 5.5 V
- Rated supply voltage: V_{CC} = 5 V

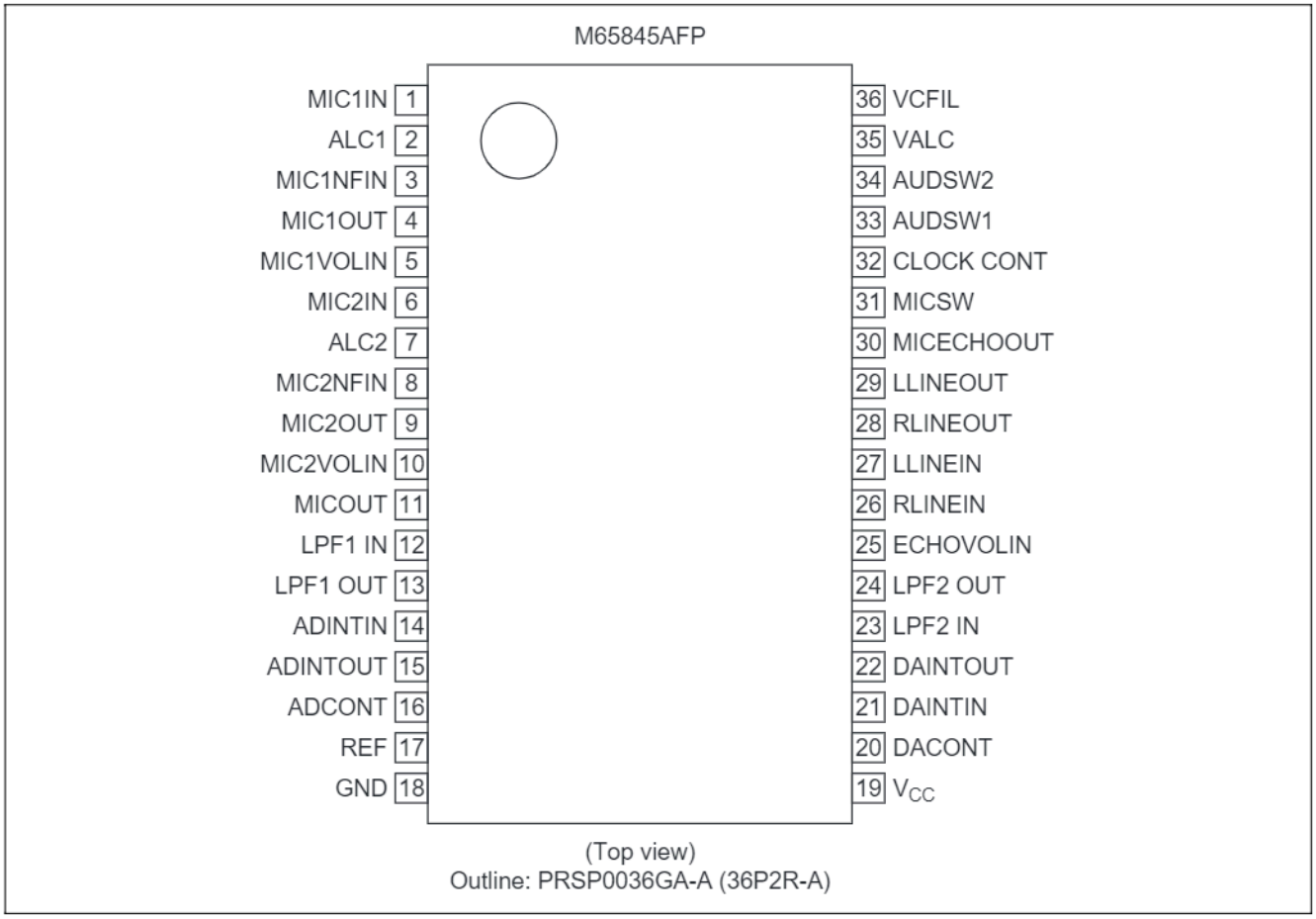
System Configuration



Block Diagram



Pin Arrangement



(Top view)
Outline: PRSP0036GA-A (36P2R-A)

Pin Description

Pin No.	Symbol	Pin Name	Function
1	MIC1IN	MIC1 input	Connect MIC1
2	ALC1	ALC1 control	Connect C which determine recovery time
3	MIC1NFIN	MIC1 NF input	Set up MIC1 amp gain for feedback circuit
4	MIC1OUT	MIC1 output	
5	MIC1VOLIN	MIC1 volume input	Connect microphone volume which turn down input signal
6	MIC2IN	MIC2 input	Connect MIC2
7	ALC2	ALC2 control	Connect C which determine ALC attack, recovery time
8	MIC2NFIN	MIC2 NF input	Forms MIC2 amp gain with feedback
9	MIC2OUT	MIC2 output	
10	MIC2VOLIN	MIC2 volume input	Connect microphone volume which turn down input signal
11	MICOUT	MIC output	Mixing output with MIC1 and MIC2
12	LPF1 IN	Low pass filter 1 input	Forms the front low pass filter with external CR for digital echo
13	LPF1 OUT	Low pass filter 1 output	
14	ADINTIN	A/D integral input	Forms integrator with external C and R
15	ADINTOUT	A/D integral output	
16	ADCONT	A/D control	ADM A/D adaptive control
17	REF	Reference	1/2 V_{CC}, connect filter C
18	GND	GND	
19	V_{CC}	Power supply	
20	DACONT	D/A control	ADM A/D adaptive control
21	DAINTIN	D/A integral input	Forms integrator with external C
22	DAINTOUT	D/A integral output	
23	LPF2 IN	Low pass filter 2 input	Forms post low pass filter with external CR for digital echo
24	LPF2 OUT	Low pass filter 2 output	
25	ECHOVOLIN	Echo volume input	Connect microphone volume which turn down input signal
26	RLINEIN	Rch line input	
27	LLINEIN	Lch line input	
28	RLINEOUT	Rch line output	Mixing output with line and microphone
29	LLINEOUT	Lch line output	
30	MICECHOOUT	MIC echo output	Mixing output with microphone and echo
31	MICSW	MIC SW	L: Microphone OFF, H: Microphone ON
32	CLOCK CONT	Clock control	Controls built-in clock generation circuit with external R
33	AUDSW1	Audio SW1	Changing source sound signal
34	AUDSW2	Audio SW2	
35	VALC	ALC supply voltage control	Form ALC operation voltage with control voltage
36	VCFIL	Vocal cut filter	Through frequency under vocal level

Absolute Maximum Ratings

(Ta = 25°C, unless otherwise noted)

Item	Symbol	Rations	Unit	Conditions
Supply voltage	V_{CC}	6.0	V	
Circuit current	I_{CC}	85	mA	
Input voltage	Vi	–0.3 to V_{CC} + 0.3	V	
Power dissipation	Pd	860	mW	
Operating temperature	Topr	–20 to +75	°C	
Storage temperature	Tstg	–40 to +125	°C	

Recommended Operating Condition

Item	Symbol	Limits			Unit	Conditions
		Min	Typ	Max		
Supply voltage	V_{CC}	4.5	5	5.5	V	
L input voltage	V_{IL}	0	—	1	V	Pin 33, 34
H input voltage	V_{IH}	4	—	V_{CC}	V	

Electrical Characteristics

(V_{CC} = 5 V, f = 1 kHz, Vi = 100mVrms, fck = 2 MHz, Ta = 25°C, unless otherwise noted)

Item		Symbol	Limits			Unit	Conditions
			Min	Typ	Max		
Total	Circuit current	I_{CC}	25	34	70	mA	No signal
Microphone amplifier	Voltage gain	G_{VO}	44	47	50	dB	Vo = –17 dBV
	Distortion 1	THD1	—	0.5	1.5	%	Vo = –17 dBV, without ALC
	Distortion 2	THD2	—	3.0	6.0	%	Vi = –27 dBV, ALC operate
	ALC voltage	Vo_{ALC}	–3	0	+3	dB	at –10 to +3 dBV
	ALC attack time	T_{ALCAT}	25	40	55	ms	at C = 4.7 µF
	ALC recovery time	T_{ALCRE}	1.0	1.5	2.0	s	at C = 4.7 µF
	Maximum output voltage	Vo_{MAX}	–1	2	—	dBV	THD = 10%
	Noise voltage	No	—	–68	–57	dBV	G_V = 47 dB, JIS-A, VI = 0 Vrms
	Input impedance	Zi	5	10	20	kΩ	
Echo	Delay time	Td	167	197	226	ms	R_C = 51 kΩ
	Voltage gain	Gv	–3	0	+3	dB	
	Distortion	THD	—	2.0	4.0	%	
	Maximum output voltage	Vo_{MAX}	–3	+1	—	dBV	THD = 10%
	Noise voltage	No	—	–82	–67	dBV	JIS–A
Line	Voltage gain	Gv	–3	0	+3	dB	
	Distortion	THD	—	0.02	0.1	%	
	Maximum output voltage	Vo_{MAX}	1	4	—	dBV	THD = 10%
	Noise voltage	No	—	–97	–88	dBV	JIS-A, MICSW = OFF
	Input impedance	Zi	10	20	40	kΩ	
Vocal cut	Noise voltage	No	—	–95	–72	dBV	JIS-A, Vocal cut ON
	Voltage gain	Gv	–3	0	+3	dB	Input one side channel
	Maximum output voltage	Vo_{MAX}	1	4	—	dBV	THD = 10%
	Vocal rejection ratio	G_{REJ}	14	18	—	dB	

Function Description

Microphone Amplifier

The gain (G_V) and low cut-off frequency (fcl) of microphone amplifier are expressed as follows.

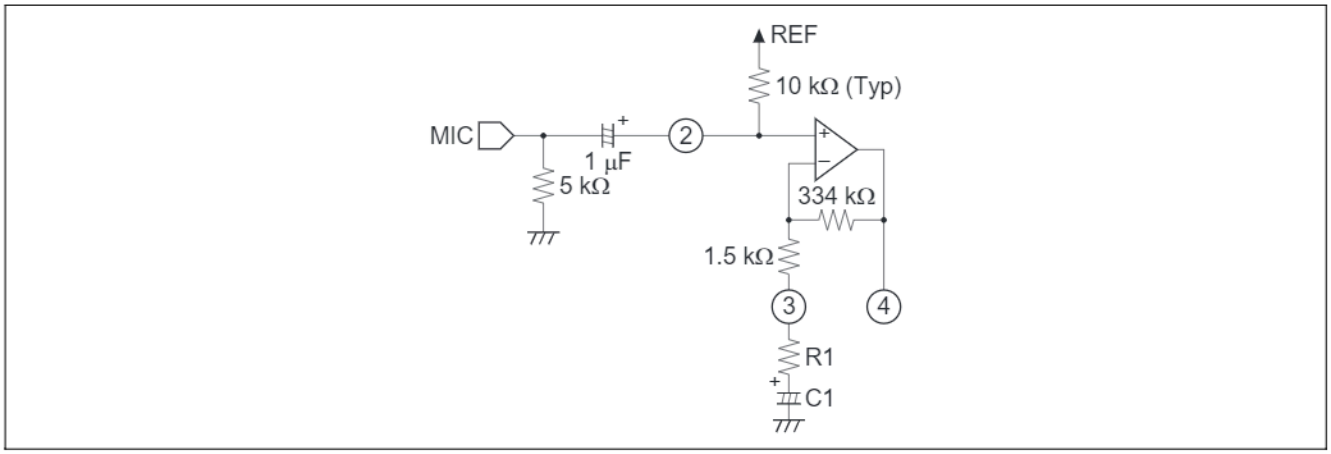
$$G_V = 20\log \bullet \frac{R1 + 1.5\,k + 334\,k}{R1 + 1.5\,k} \qquad fcl = \frac{1}{2\pi \bullet (R1 + 1.5\,k) \bullet C1}$$

G_V (max) = 47 dB, fcl = 50 Hz

R1 = 0 Ω, C1 = 2.2 μF

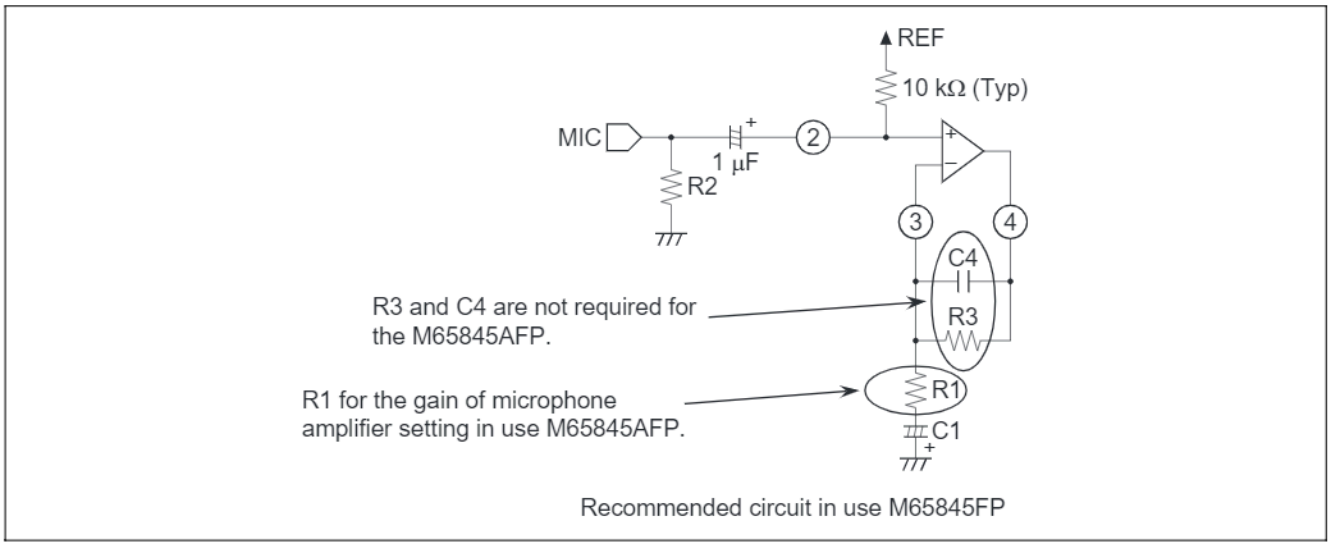
Assuming G_V = 37 dB, fcl = 15 Hz, for instance, the constants take the following values.

R1 = 3.3 kΩ, C1 = 2.2 μF



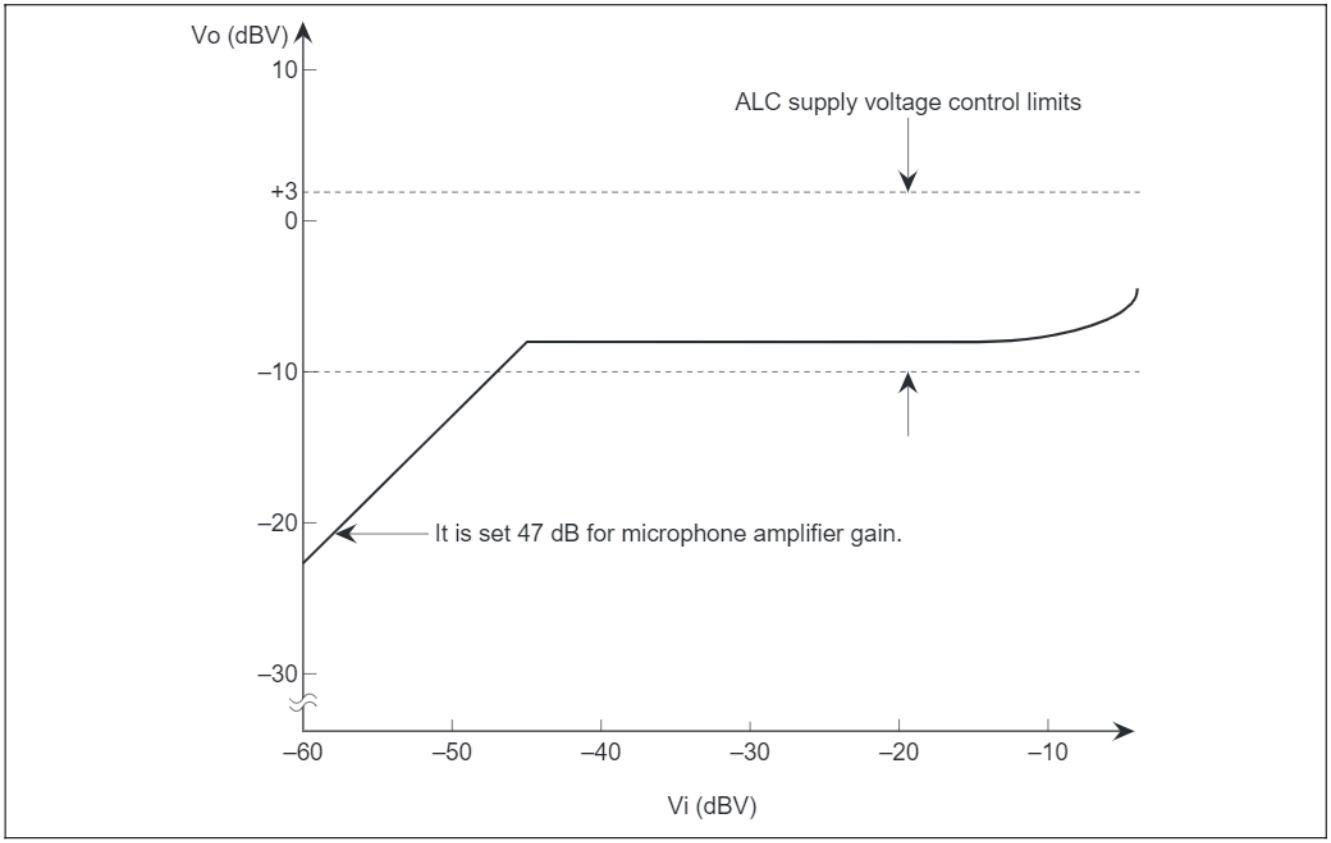
<Attention point when M65845FP is replaced with M65845AFP>

R3 and C4 are required for the M65845FP, not for the M65845AFP. As mentioned above, the gain of microphone amplifier can set it up with R1.



Recommended circuit in use M65845FP

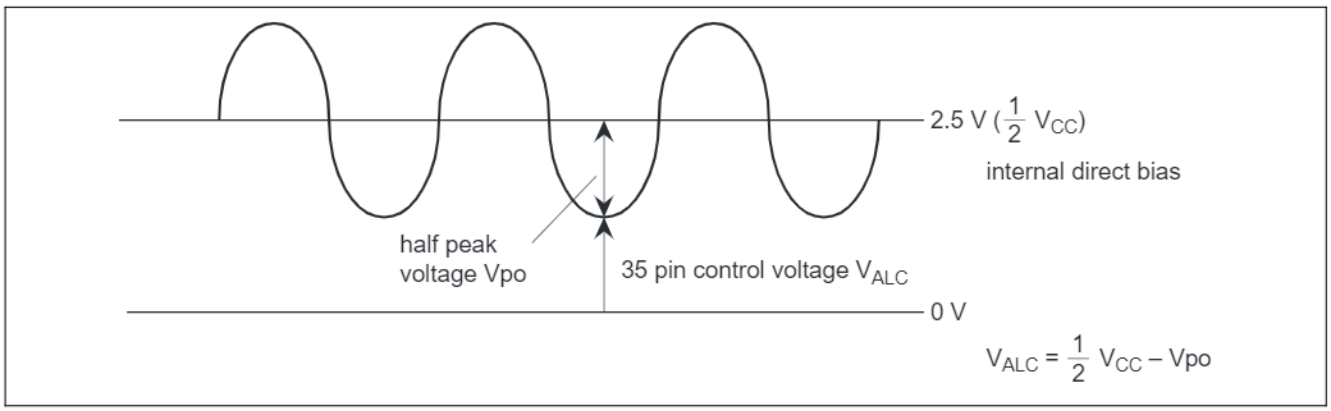
ALC Level Block Diagram



ALC Operation Voltage Control

ALC operation voltage can be formed within the limits of –10 to +3 dBV controlled by DC control voltage which connect pin 35.

(Setting up forms)



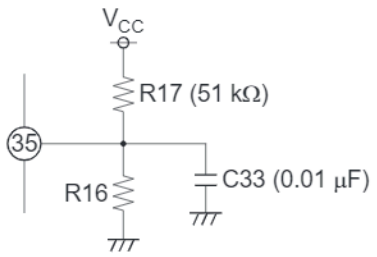
When ALC operation voltage is –5 dBV (at V_{CC} = 5 V)

–5 dBV = 0.56 Vrms = 1.59 Vp-p = 0.80 Vp-o

V_{ALC} = 2.5 – 0.8 = 1.7 V

are concerned.

Input impedance to pin 35 is so high (1 MΩ) that ALC base voltage can be determined by division resistance.



at V_{CC} = 5 V

ALC Operation Voltage (dBV)	Pin 35 Control Voltage VALC (V)	Resistance R16 (Ω)
+3	0.50	5.6 k
0	1.09	15 k
–2	1.38	20 k
–4	1.61	24 k
–6	1.79	27 k
–8	1.94	33 k
–10	2.05	36 k

MIC SW

Input low level to pin 31 (MIC SW), then microphone and echo signal can be cut.

Pin 31 (MIC SW)	MIC SW	Echo Signal Output
H or Open	On	On
L	Off	Mute

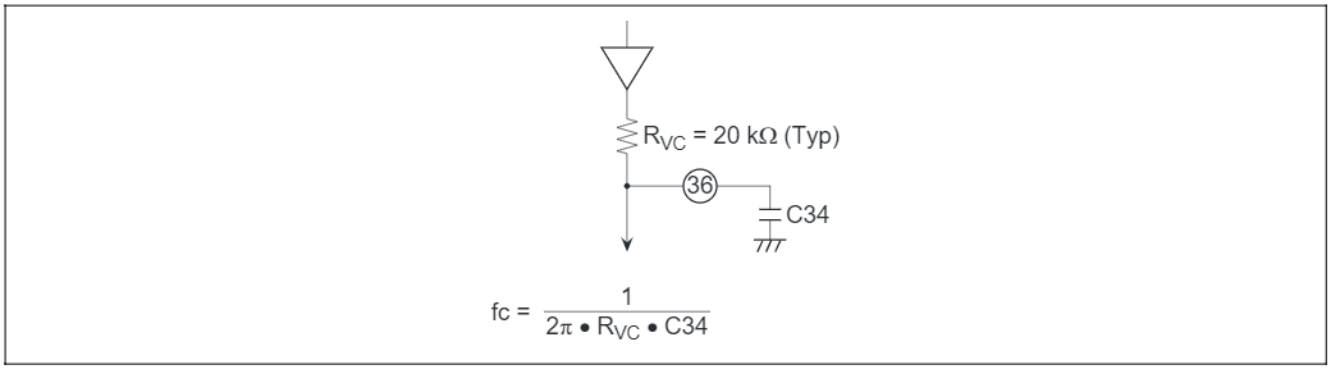
Audio Source Select

Changing the switch, sound source changes four patterns matching with Karaoke soft.

Pin 33 AUDSW1: D1	Pin 34 AUDSW2: D2	Movements
L	L	Stereo
L	H	Lch monaural
H	L	Rch monaural
H	H	Vocal cut

1. Stereo
   Under the conditions usual 2ch are played back to each outputs.
2. Lch monaural
   Under the conditions Lch source is played back to 2ch outputs and suitable for Karaoke reproduction of multiple Karaoke soft and main sound reproduction of laser disks.
3. Rch monaural
   Under the conditions Rch source is played back to 2ch outputs and suitable for reference vocal reproduction of multiple Karaoke soft and sub sound reproduction of laser disks.

4. Vocal cut
   It is a method turned down Lch and Rch input having the same phase and sound.
   Low pass cut off frequency fc is determined by a capacitance which connect to pin 36 (vocal cut filter).
   It is also having a function which through frequency under vocal level for supplying a lack of low level sound.

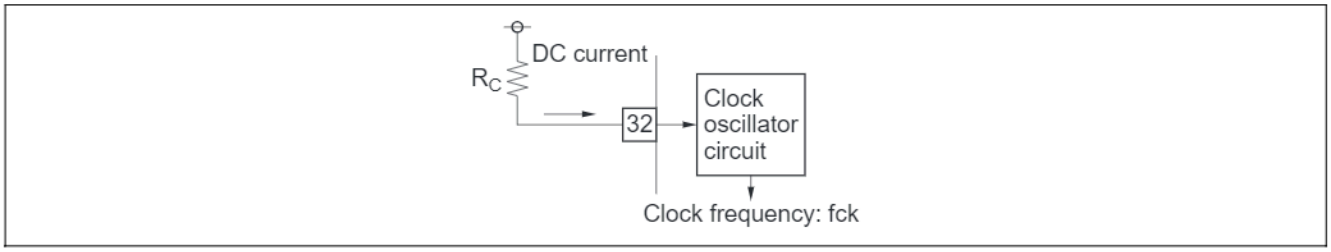


   at fc = 50 Hz, C34 = 0.15 µF is determined.

Caution: Inside resistance is changeable one by one which rate is ±30%.

Digital Echo

1. Clock oscillator circuit
   This IC incorporates a current control type clock oscillator circuit in it, thus providing circuit configuration just by connecting an R_C for current control pin 32 (CLOCK CONT).
   Fully internal clock supply prevents occurrence of undesired radiation without affecting any external circuit.
   The oscillator frequency fck is following.
   fck = 2 MHz (R_C = 51 kΩ)

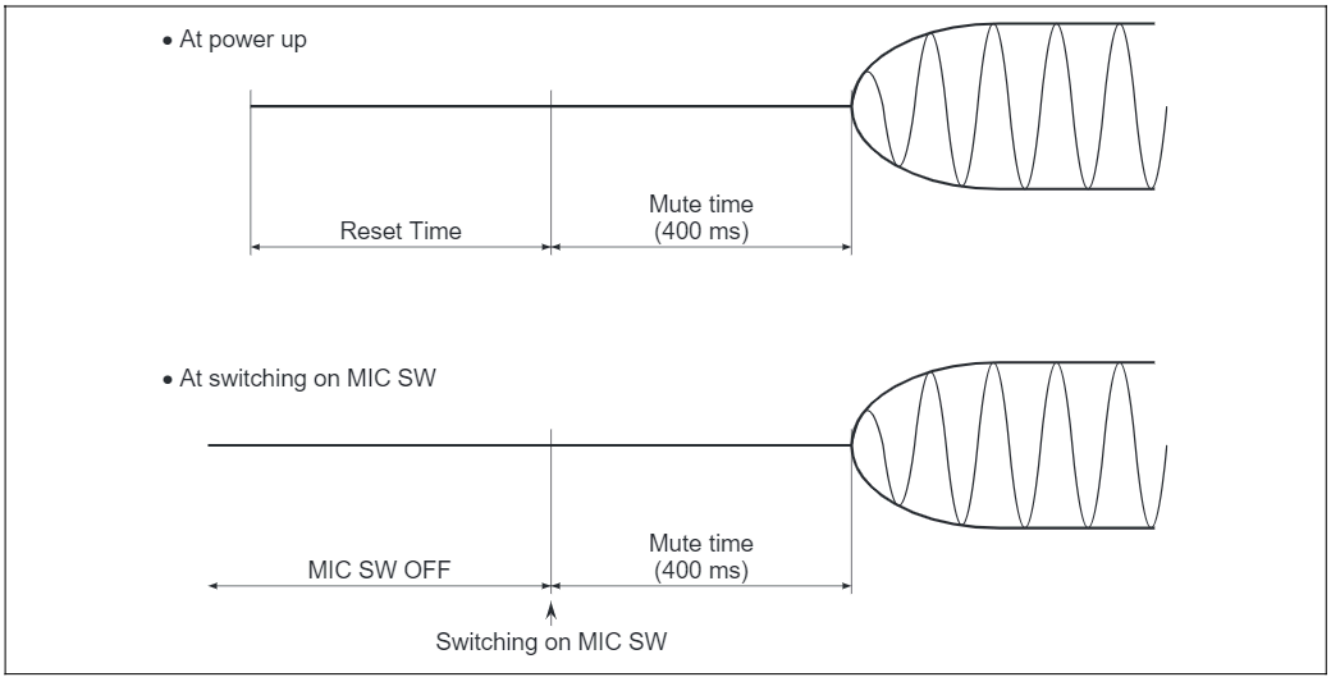


Note: The delay time (Td) for echo is determined by the clock frequency (fck).
Delay time = 1/fck × 24 × N
(N = the number of memory bits = 16384)

fck = 2 MHz (Rc = 51 kΩ): Delay time = 197 ms
fck = 2.6 MHz (Rc = 39 kΩ): Delay time = 150 ms
fck = 3.9 MHz (Rc = 24 kΩ): Delay time = 100 ms

2. Auto mute function

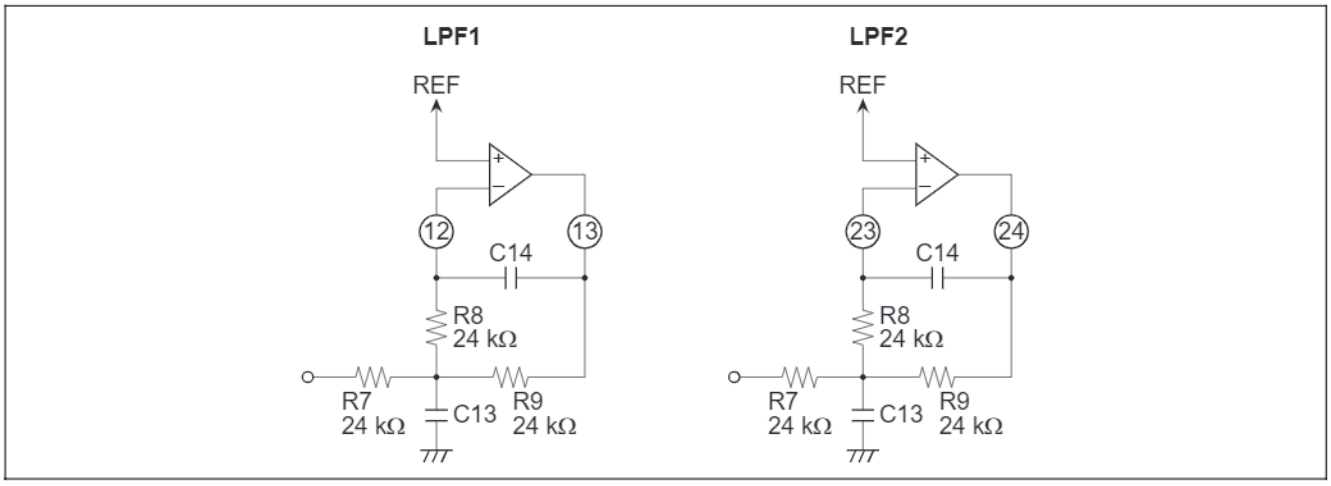
The IC carries out auto mute function at the time of powering up and switching on MIC SW in order to suppress shock noise that the digital delay may produce.



3. Input and output LPF

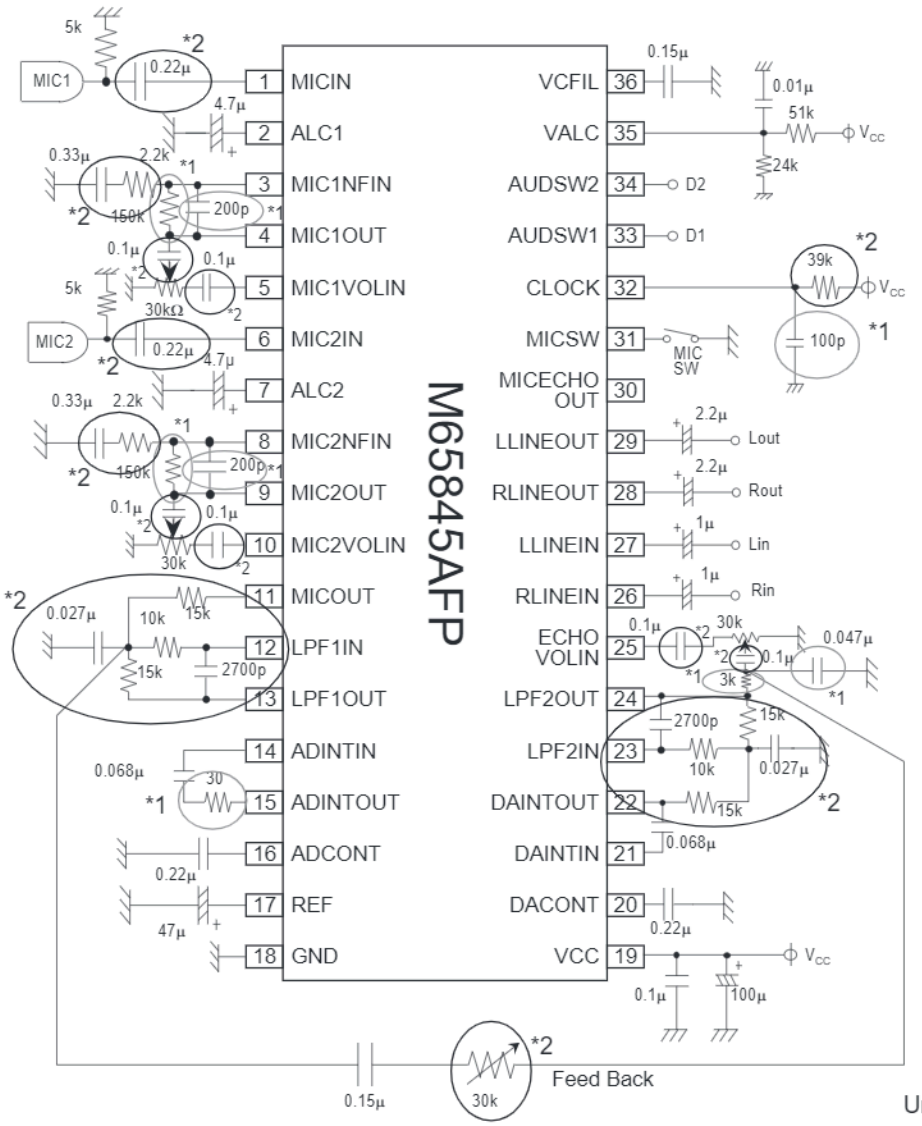
Signal through frequency fsig is also determined by LPF of digital echo cut off frequency.
2 degree LPF of digital echo is formed by external resistance and capacitor. (refer to next figure)
So, cut off frequency is determined by next formula.

$$fsig = \frac{1}{2\pi\sqrt{R8 \bullet R9 \bullet C13 \bullet C14}}$$



Compatibility with M65845FP

<Application Example in Use M65845FP>

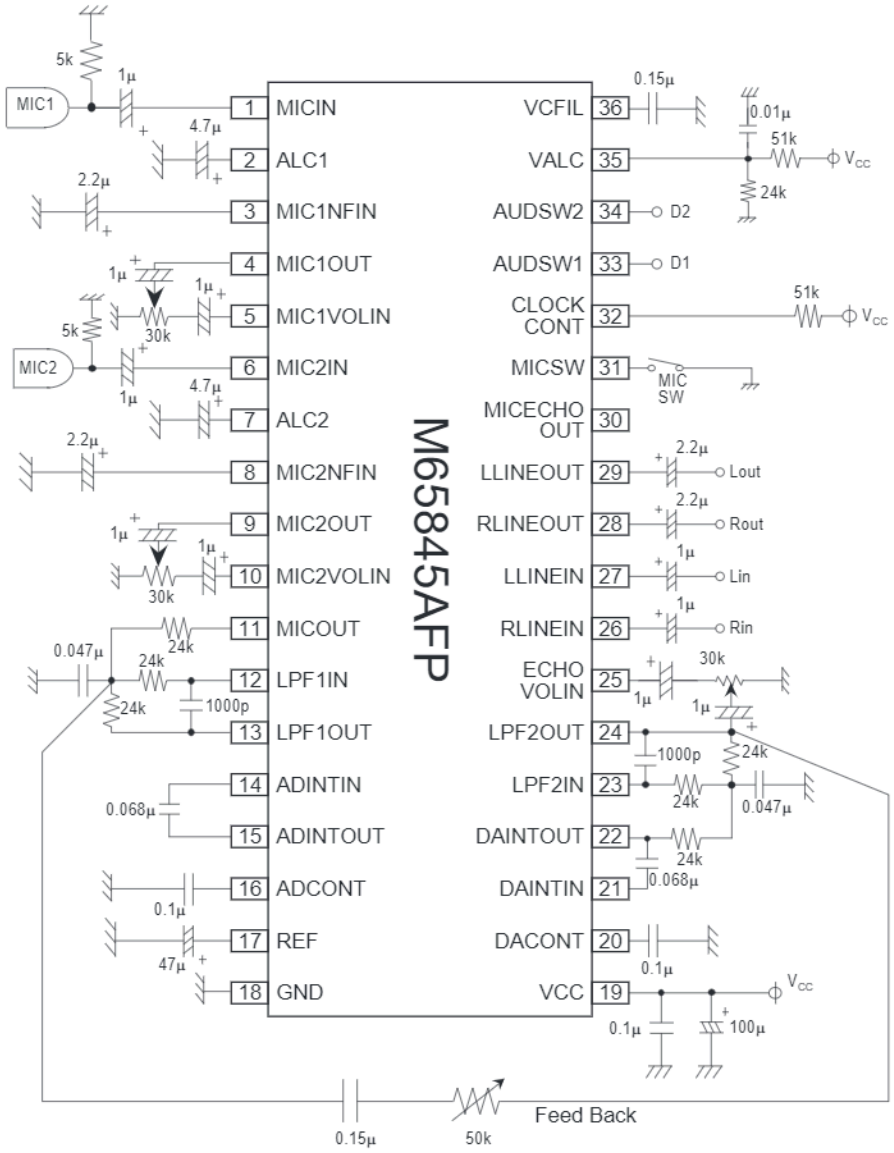


As mentioned above, the M65845AFP can be replaced with the M65845FP without changing the board.

Notes: *1. The components marked with a circle are required for the M65845FP, not for the M65845AFP.
*2. The M65845AFP is different from the M65845FP a part of the components marked with a circle.

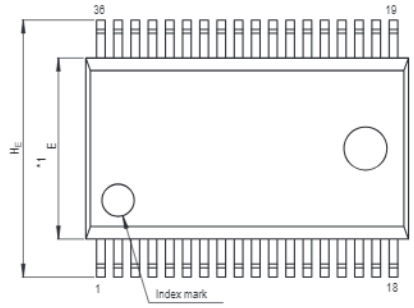
Application Example

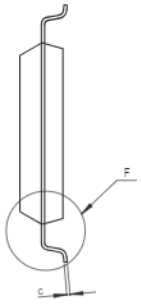
Microphone amplifier : G_V = 47 dB, fcl = 50 Hz
ALC operating voltage –4 dBV
ECHO : Delay time 197 ms (fs = 83.3 kHz)
Cut-off frequency 3.1 kHz



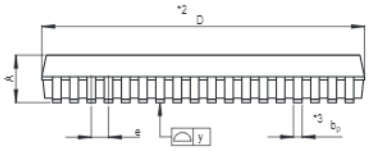
Package Dimensions

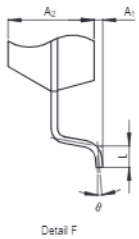
JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SSOP36-8.4x15-0.80	PRSP0036GA-A	36P2R-A	0.5g





NOTE)
1. DIMENSIONS "*1" AND "*2" DO NOT INCLUDE MOLD FLASH.
2. DIMENSION "*3" DOES NOT INCLUDE TRIM OFFSET.





Detail F

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	14.8	15.0	15.2
E	8.2	8.4	8.6
A_2	—	2.0	—
A	—	—	2.4
A_1	0.05	—	—
b_p	0.35	0.4	0.5
c	0.13	0.15	0.2
θ	0°	—	10°
H_E	11.63	11.93	12.23
e	0.65	0.8	0.95
y	—	—	0.15
L	0.3	0.5	0.7

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

RENESAS SALES OFFICES

http://www.renesas.com

Refer to "**http://www.renesas.com/en/network**" for the latest and detailed information.

Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510

Colophon .7.2