

HRW0202A

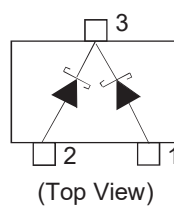
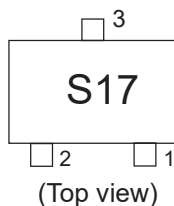
Silicon Schottky Barrier Diode for Rectifying

Features

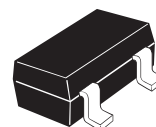
- Low forward voltage drop and suitable for high efficiency rectifying.

Ordering Information and Pin Arrangement

HRW0202ATR, HRW0202ATL
 Package Name : MPAK
 Package Code: PLSP0003ZC-A
 Taping Abbreviation (Quantity)
 TR(3,000 pcs / reel)
 TL(3,000 pcs / reel)



1. Anode
2. Anode
3. Cathode



Absolute Maximum Ratings *1

(Ta = 25°C)

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RMM}^{*1}	20	V
Average rectified current	I_o^{*2}	200	mA
Non-Repetitive peak forward surge current	I_{FSM}^{*3}	3	A
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

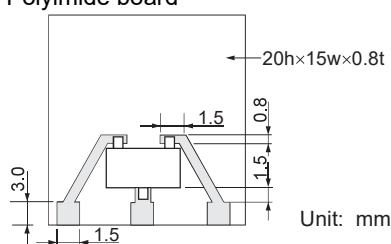
- Notes: 1. Two device total.
 2. See from Fig.4 to Fig.7
 3. 10ms sine wave 1 pulse.

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_F	—	—	0.40	V	$I_F = 100 \text{ mA}$
Reverse current	I_R	—	—	50	μA	$V_R = 20 \text{ V}$
Thermal resistance	$R_{th(j-a)}$	—	350	—	°C/W	Polyimide board *1

- Notes: 1. Per one device
 2. Polyimide board



Main Characteristics

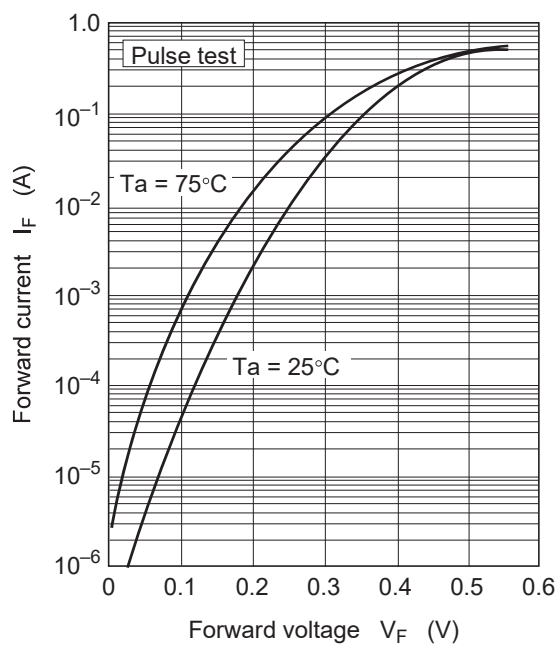


Fig.1 Forward current vs. Forward voltage

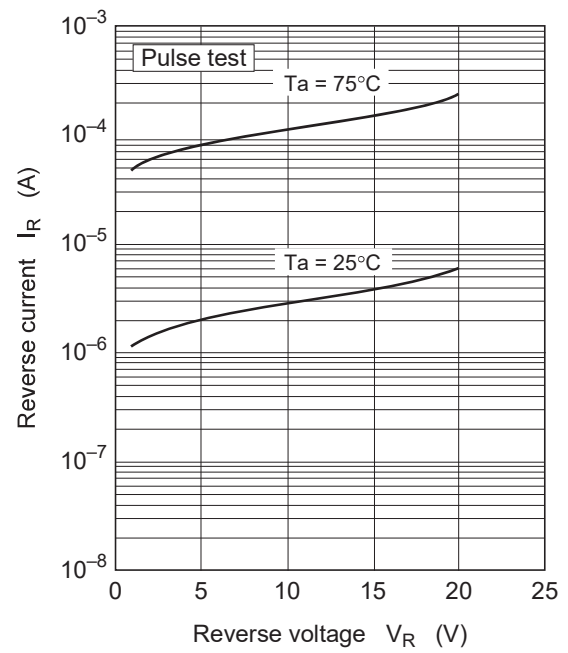


Fig.2 Reverse current vs. Reverse voltage

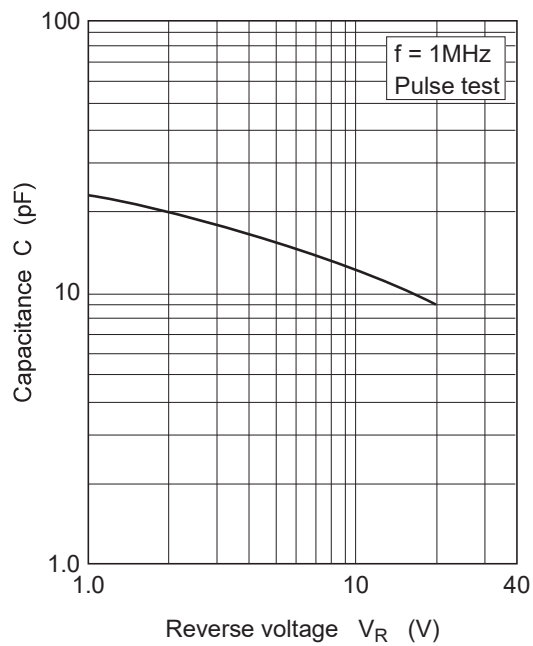


Fig3. Capacitance vs. Reverse voltage

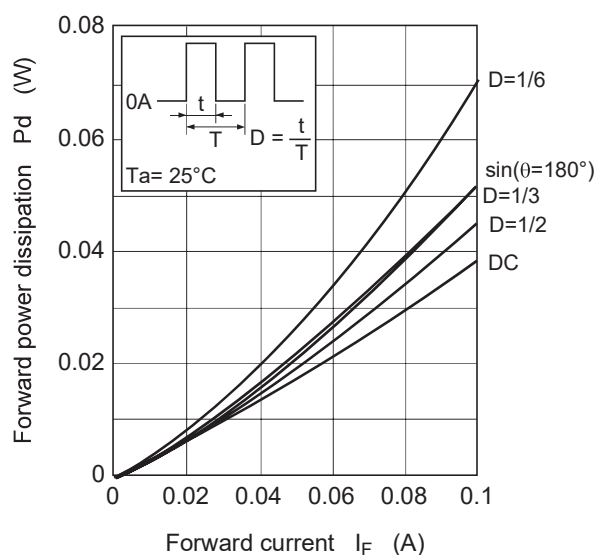


Fig4. Forward power dissipation vs. Forward current

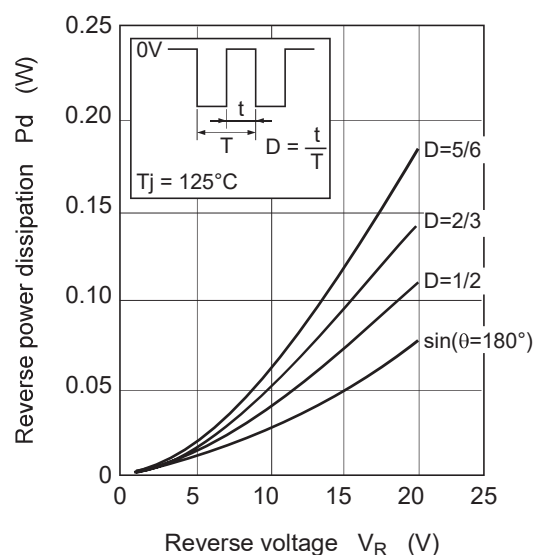


Fig5. Reverse power dissipation vs. Reverse voltage

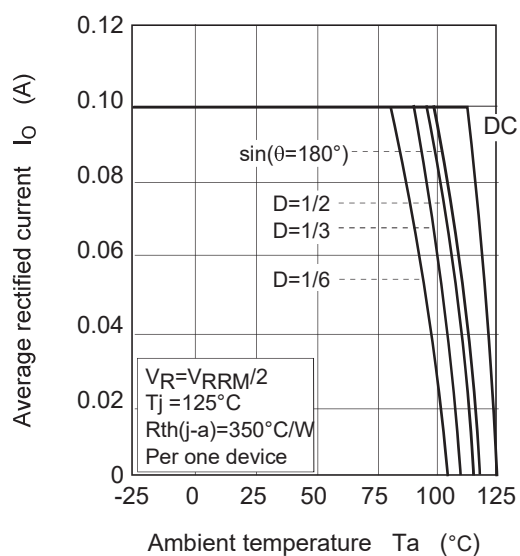


Fig.6 Average rectified current vs. Ambient temperature

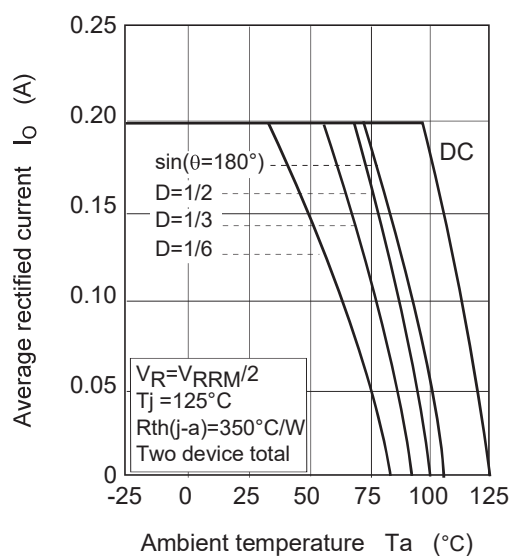


Fig.7 Average rectified current vs. Ambient temperature