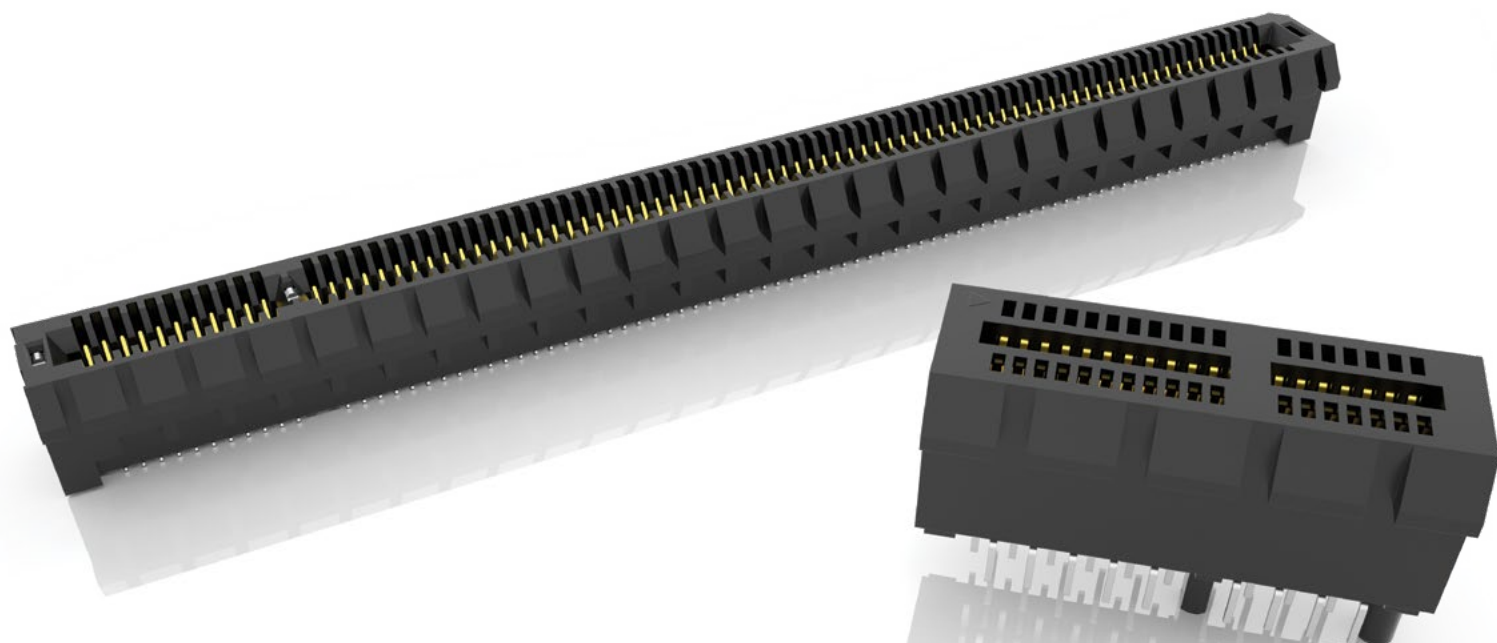


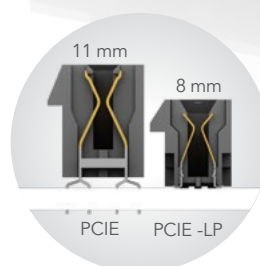
PCI EXPRESS® EDGE CARD SOCKETS

(1.00 mm) .0394" PITCH



FEATURES & BENEFITS

- 1.00 mm pitch
- Supports 1, 4, 8 and 16 PCI Express® links
- PCIe® 3.0 Solution (PCIe)
- PCIe® 4.0 Solution with low profile design for space savings (PCIe-LP)
- PCIe® 4.0 with slim body design (PCIe-G4)
- PCIe® 5.0 Solution with differential pair signaling (PCIe-G5)
- Vertical, right-angle and edge mount
- Mates with .062" (1.60 mm) thick cards
- Alignment pin and weld tab options



PCIe® 3.0 & 4.0
Edge Card
connectors



PCIe® 4.0 slim body
socket with Edge Rate®
contacts (PCIe-G4)

KEY SPECIFICATIONS

SERIES	TOTAL PINS (LANES)	INSULATOR MATERIAL	CONTACT MATERIAL	OPERATING TEMP RANGE	CURRENT RATING	VOLTAGE RATING	PCIe® COMPATIBILITY
PCIe	36 (x1), 64 (x4), 98 (x8), 164 (x16)	-TH = Black Nylon -EMS2 & -TH = LCP	Phosphor Bronze	-55 °C to +125 °C	2.4 A (2 pins)	215 VAC	3.0
PCIe-LP	36 (x1), 64 (x4), 98 (x8), 164 (x16)	LCP	Phosphor Bronze	-55 °C to +125 °C	2.1 A (2 pins)	215 VAC	4.0
PCIe-G4	36 (x1), 64 (x4), 98 (x8), 164 (x16)	LCP	Copper Alloy	-55 °C to +125 °C	2.2 A (2 pins)	300 VAC	4.0
PCIe-G5	36 (x1), 64 (x4), 98 (x8), 164 (x16)	LCP	Copper Alloy	-55 °C to +125 °C	3.2 A (2 pins)	235 VAC	5.0

(1.00 mm) .0394" PITCH • PCI EXPRESS® CARD SOCKETS

PCIE

NUMBER OF POSITIONS

02

PLATING OPTION

D

TAIL OPTION

-036, -064, -098, -164

-F
= Gold flash on contact,
Tin on tail

-EMS2
= Edge Mount

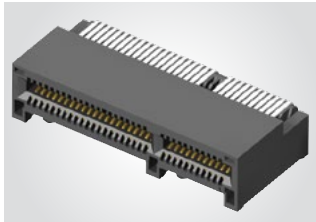
-TH
= Through-hole

-RA
= Right-angle

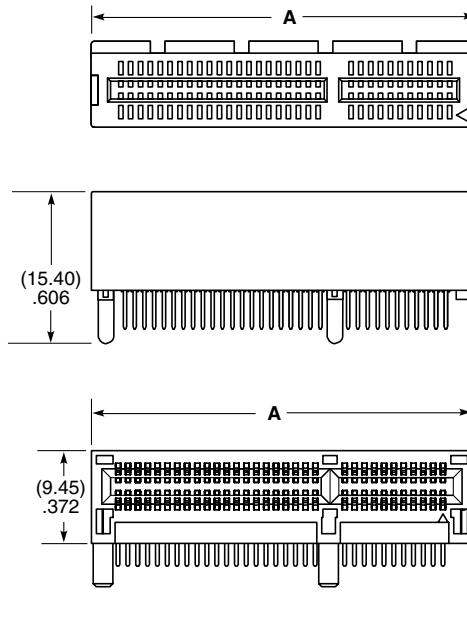
PCIE

Card Mates:
(1.60 mm)
.062" card

Cable Mates:
PCIEC



POSITIONS	A
-036 (x1)	(25.00) .984
-064 (x4)	(39.00) 1.535
-098 (x8)	(56.00) 2.205
-164 (x16)	(89.00) 3.504



View complete specifications at: samtec.com?PCIE

PCIE

LP

NUMBER OF LANES

01

PLATING OPTION

DV

A

OPTION

PACKAGING

**-01, -04,
-08, -16**

-F
= Gold Flash on
contact, Matte
Tin on tail

-S
= 30 μ" (0.75 μm)
Gold on contact,
Matte Tin on tail

-WT
= Weld Tab

-K
= Polyimide film
Pick & Place Pad

(For -16 lanes
only leave blank for
Tray Packaging)

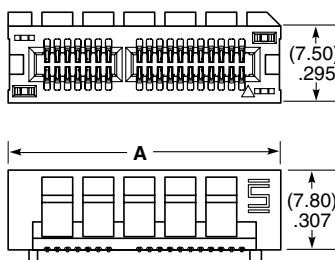
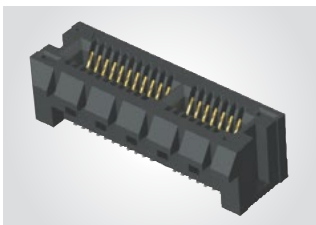
-TR
= Tape & Reel
("X"R required with
-01, -04, -08 lanes)

-FR
= Full Reel
Tape & Reel
(must order max.
quantity per reel;
contact Samtec for
quantity breaks)
("X"R required with
-01, -04, -08 lanes)

PCIE-LP

Card Mates:
(1.60 mm)
.062" card

Cable Mates:
PCIEC



NUMBER OF LANES	A
-01	(26.60) 1.047
-04	(40.60) 1.598
-08	(57.60) 2.268
-16	(90.60) 3.567

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Note:
Some lengths, styles and options are non-standard, non-returnable.

View complete specifications at: samtec.com?PCIE-LP

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Samtec:

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[PCIE-036-02-F-D-RA](#) [PCIE-098-02-F-D-EMS2](#) [PCIE-098-02-F-D-EMS2-BG](#) [PCIE-164-02-F-D-TH](#) [PCIE-064-02-F-D-](#)
[RA](#) [PCIE-098-02-F-D-TH](#) [PCIE-064-02-S-D-RA](#) [PCIE-036-02-S-D-EMS2](#) [PCIE-098-02-S-D-RA](#) [PCIE-036-02-F-D-](#)
[EMS2](#) [PCIE-164-02-F-D-EMS2-BG](#) [PCIE-164-02-F-D-RA](#) [PCIE-064-02-F-D-EMS2-BG](#) [PCIE-LP-16-01-F-DV-A](#) [PCIE-](#)
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[PCIE-LP-08-01-F-DV-A-K-TR](#) [PCIE-098-02-S-D-EMS2](#) [PCIE-098-02-S-D-EMS2-BG](#) [PCIE-036-02-F-D-EMS2-BG](#)
[PCIE-098-02-F-D-EMS3-BG](#) [PCIE-164-02-S-D-RA](#) [PCIE-064-02-F-D-EMS3](#) [PCIE-064-02-F-D-EMS3-BG](#) [PCIE-064-](#)
[02-S-D-EMS2](#) [PCIE-164-02-F-D-EMS3](#) [PCIE-164-02-F-D-EMS3-BG](#) [PCIE-064-02-S-D-EMS3](#) [PCIE-098-02-S-D-](#)
[EMS3](#) [PCIE-036-02-S-D-EMS2-BG](#) [PCIE-036-02-S-D-RA](#) [PCIE-098-02-F-D-EMS3](#) [PCIE-164-02-S-D-EMS2-BG](#)
[PCIE-164-02-S-D-EMS3-BG](#) [PCIE-064-02-S-D-EMS2-BG](#) [PCIE-036-02-S-D-EMS3](#) [PCIE-098-02-S-D-EMS3-BG](#)
[PCIE-164-02-S-D-EMS2](#) [PCIE-164-02-S-D-EMS3](#) [PCIE-LP-01-01-F-DV-A-K-TR](#) [PCIE-LP-01-01-S-DV-A-K-TR](#) [PCIE-](#)
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[PCIE-LP-04-01-F-DV-A-TR](#) [PCIE-LP-04-01-F-DV-A-WT-TR](#) [PCIE-LP-04-01-L-DV-A-WT-TR](#) [PCIE-LP-04-01-S-DV-A-](#)
[K-TR](#) [PCIE-LP-04-01-S-DV-A-TR](#) [PCIE-LP-04-01-S-DV-A-WT-K-TR](#) [PCIE-LP-04-01-S-DV-A-WT-TR](#) [PCIE-LP-08-01-](#)
[F-DV-A-TR](#) [PCIE-LP-08-01-F-DV-A-WT-K-TR](#) [PCIE-LP-08-01-F-DV-A-WT-TR](#) [PCIE-LP-08-01-S-DV-A-K-TR](#) [PCIE-](#)
[LP-08-01-S-DV-A-TR](#) [PCIE-LP-16-01-L-DV-A-WT](#) [PCIE-LP-16-01-S-DV-A](#) [PCIE-LP-16-01-S-DV-A-K](#) [PCIE-LP-16-01-](#)
[S-DV-A-WT](#) [PCIE-LP-16-01-S-DV-A-WT-K](#) [PCIE-LP-08-01-S-DV-A-WT-K-TR](#) [PCIE-LP-08-01-S-DV-A-WT-TR](#) [PCIE-](#)
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[PCIE-064-02-S-D-EMS3-BG](#) [PCIE-G4-16-01-F-DV-WT-K](#) [PCIE-G4-08-01-F-DV-WT-K-TR](#) [PCIE-G4-08-01-L-DV-A-K-](#)
[TR](#) [PCIE-G4-16-01-L-DV-WT-K](#) [PCIE-G4-01-01-L-DV-WT-K-TR](#) [PCIE-G4-04-01-F-DV-WT-K-TR](#) [PCIE-G4-01-01-F-](#)
[DV-WT-K-TR](#) [PCIE-G4-01-01-L-DV-A-K-TR](#) [PCIE-G4-08-01-S-DV-WT-K-TR](#) [PCIE-G4-01-01-F-DV-A-K-TR](#) [PCIE-G4-](#)
[04-01-L-DV-WT-K-TR](#) [PCIE-G4-08-01-L-DV-WT-K-TR](#) [PCIE-LP-04-01-L-DV-A-WT-K-TR](#) [PCIE-G4-16-01-F-DV-A-K](#)
[PCIE-G4-01-01-S-DV-A-K-TR](#) [PCIE-G4-08-01-F-DV-A-K-TR](#) [PCIE-G4-04-01-F-DV-A-K-TR](#) [PCIE-G4-16-01-S-DV-A-K](#)
[PCIE-G4-04-01-L-DV-A-K-TR](#) [PCIE-LP-08-01-L-DV-A-K-TR](#)