

## Features

- Split Gate Trench MOSFET Technology
- Excellent Package for Heat Dissipation
- Halogen Free. "Green" Device (Note 1)
- High Density Cell Design for Low  $R_{DS(on)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

## N-CHANNEL MOSFET

## Maximum Ratings

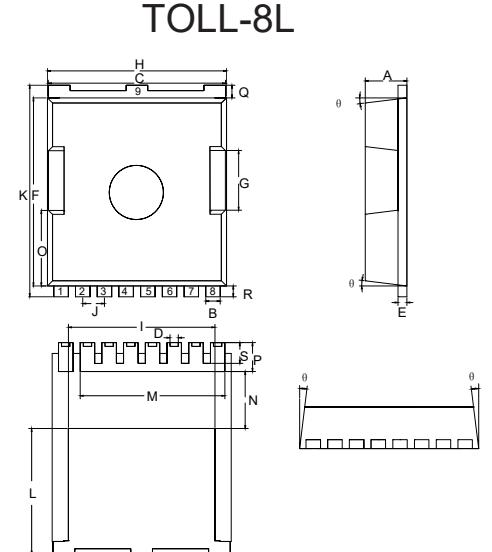
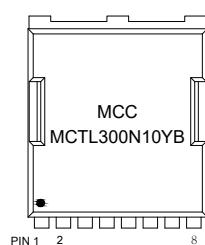
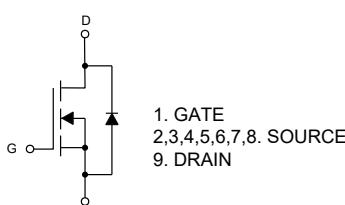
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 38°C/W Junction to Ambient (Note 2)
- Thermal Resistance: 0.4°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_J=25^\circ\text{C}$	$I_D$	300	A
$T_J=100^\circ\text{C}$		189	
Pulsed Drain Current (Note 3)	$I_{DM}$	1200	A
Total Power Dissipation (Note 4)	$P_D$	312	W
Single Pulsed Avalanche Energy (Note 5)	$E_{AS}$	2116	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ . The Power dissipation  $P_{DSM}$  is based on  $R_{\theta JA} \leq 10\text{s}$  and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
5.  $T_J=25^\circ\text{C}$ ,  $V_{DD}=80\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=2\text{mH}$ .

## Internal Structure and Marking Code



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.087	0.094	2.20	2.40	
B	0.028	0.035	0.70	0.90	
C	0.382	0.390	9.70	9.90	
D	0.017	0.020	0.42	0.50	
E	0.016	0.024	0.40	0.60	
F	0.405	0.417	10.28	10.58	
G	0.122	0.138	3.10	3.50	
H	0.382	0.398	9.70	10.10	
I	0.311	0.327	7.90	8.30	
J	0.047		1.20		BSC
K	0.452	0.468	11.48	11.88	
L	0.266	0.281	6.75	7.15	
M	0.315		8.00		
N	0.118	0.130	3.00	3.30	
O	0.157	0.172	3.98	4.38	
P	0.055	0.071	1.40	1.80	
Q	0.024	0.031	0.60	0.80	
R	0.020	0.028	0.50	0.70	
S	0.039	0.051	1.00	1.30	
θ	4°	10°	4°	10°	

**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =1mA	100			V
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V			1	uA
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.1	2.5	3.9	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A		1.2	1.55	mΩ
Gate Resistance	R <sub>g</sub>	F=1MHz, Open Drain		1.35		Ω
<b>Diode Characteristics</b>						
Continuous Body Diode Current	I <sub>S</sub>				300	A
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =30A			1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =30A, dI <sub>F</sub> /dt=100A/μs		92		ns
Reverse Recovery Charge	Q <sub>rr</sub>			167		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=100KHz		10051		pF
Output Capacitance	C <sub>oss</sub>			2015		
Reverse Transfer Capacitance	C <sub>rss</sub>			30		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =30A		166		nC
Gate-Source Charge	Q <sub>gs</sub>			34		
Gate-Drain Charge	Q <sub>gd</sub>			49		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, R <sub>G</sub> =4.5Ω, I <sub>DS</sub> =30A		30		ns
Turn-On Rise Time	t <sub>r</sub>			65		
Turn-Off Delay Time	t <sub>d(off)</sub>			121		
Turn-Off Fall Time	t <sub>f</sub>			107		

## Curve Characteristics

Fig. 1 - Typical Output Characteristics

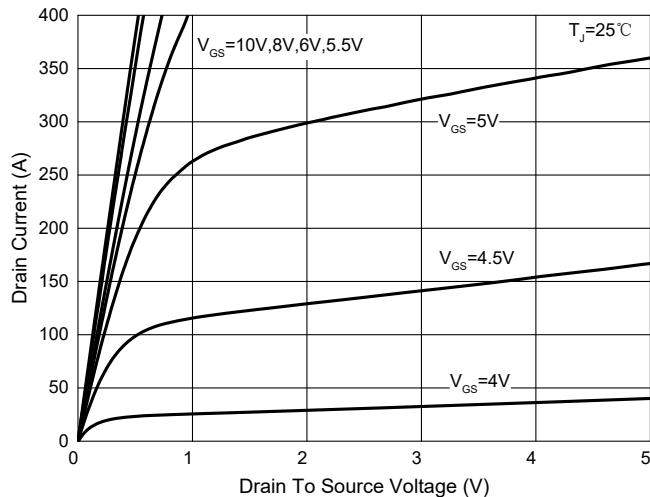


Fig. 2 - Transfer Characteristics

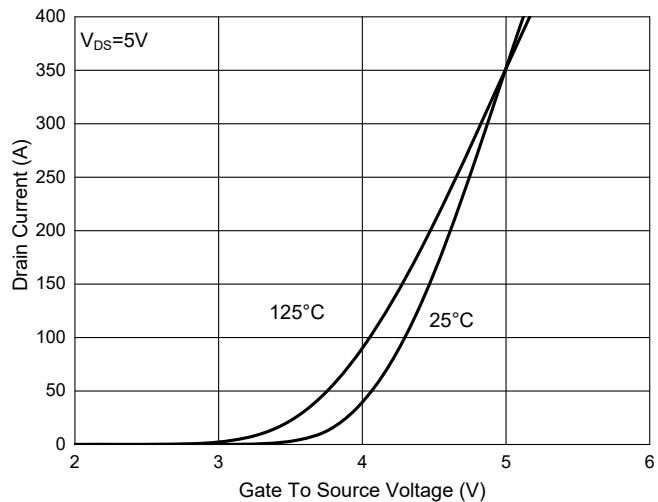


Fig. 3 -  $R_{DS(ON)}$ — $V_{GS}$

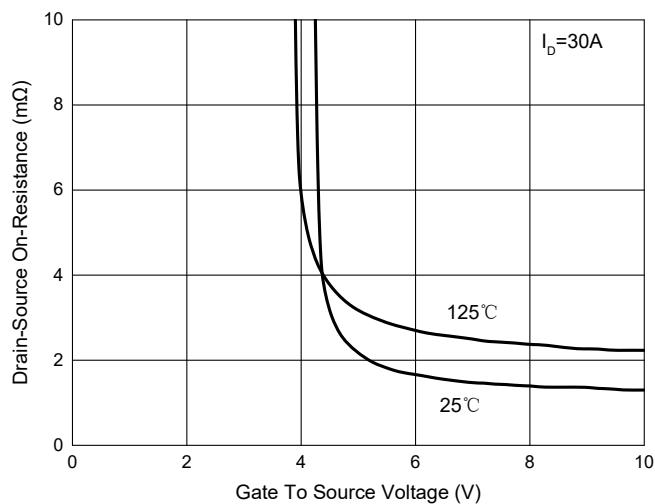


Fig. 4 - Normalized On Resistance Characteristics

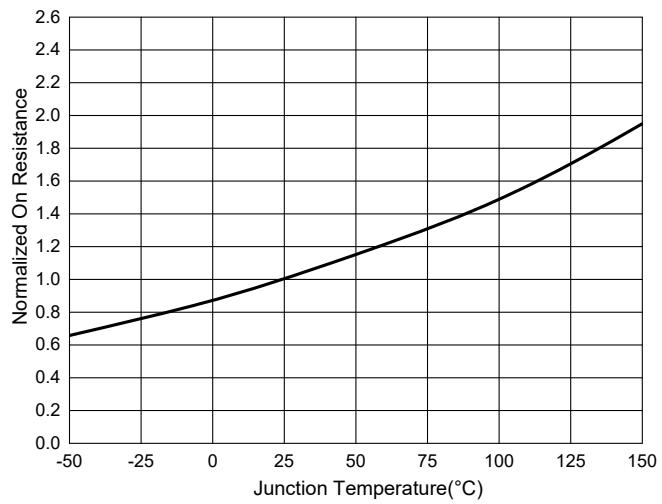


Fig. 5 - Capacitance Characteristics

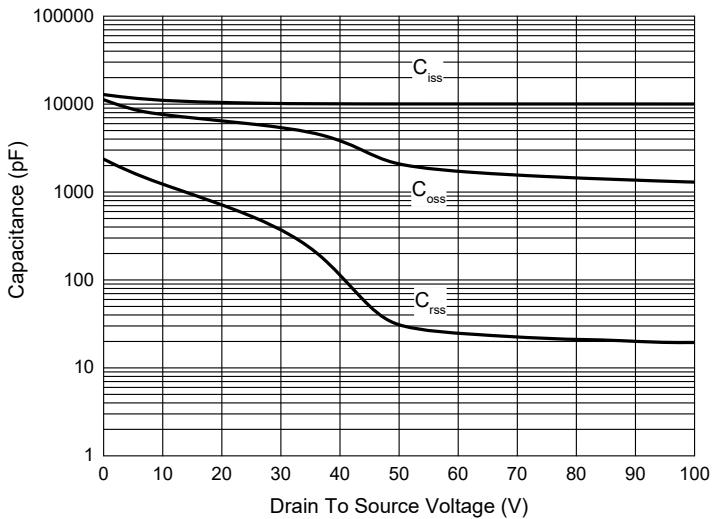
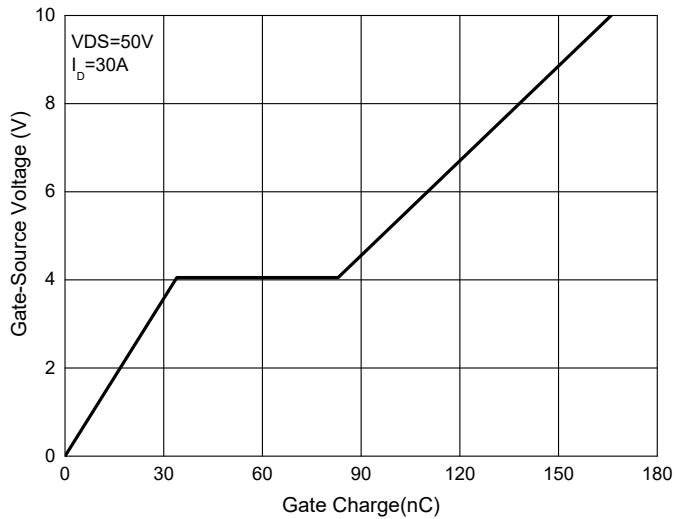
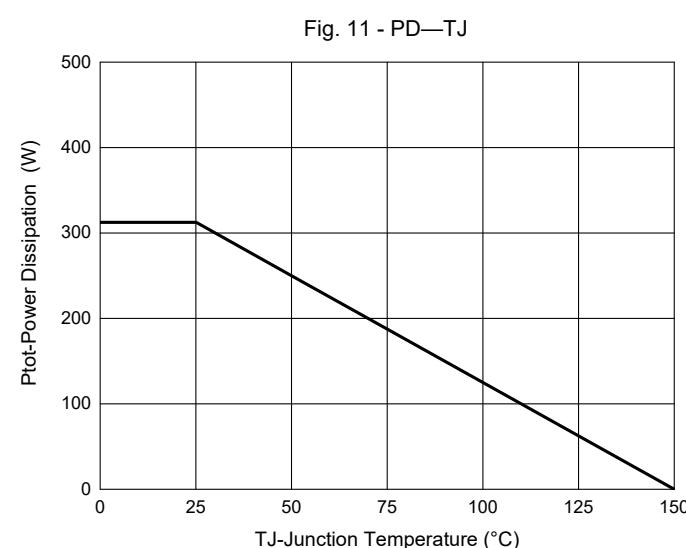
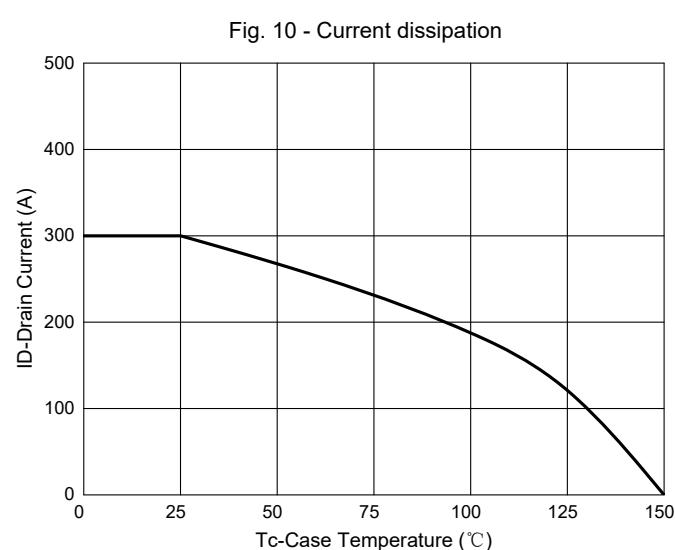
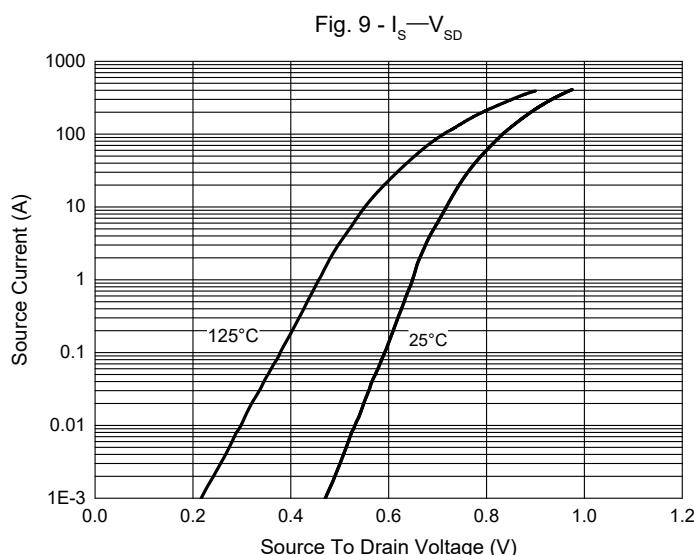
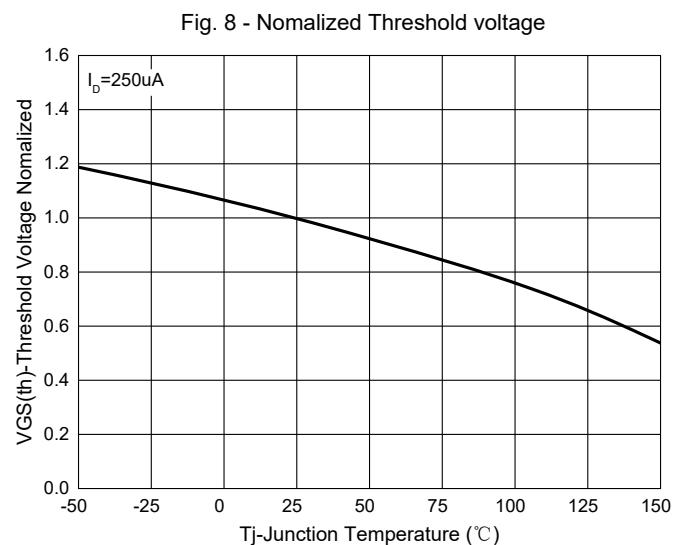
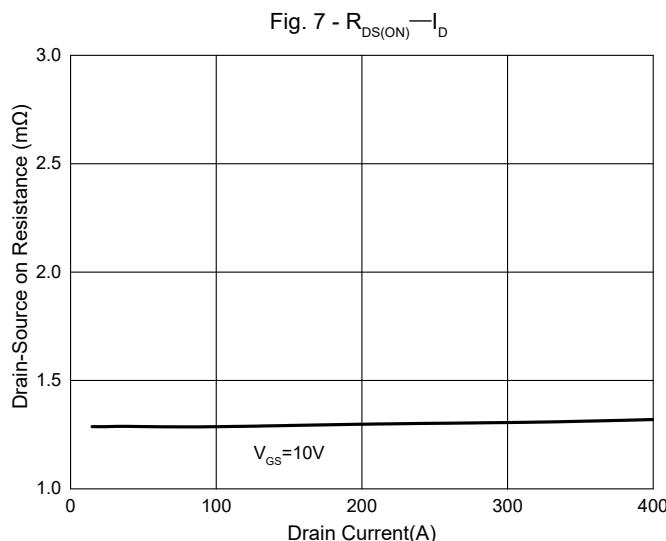


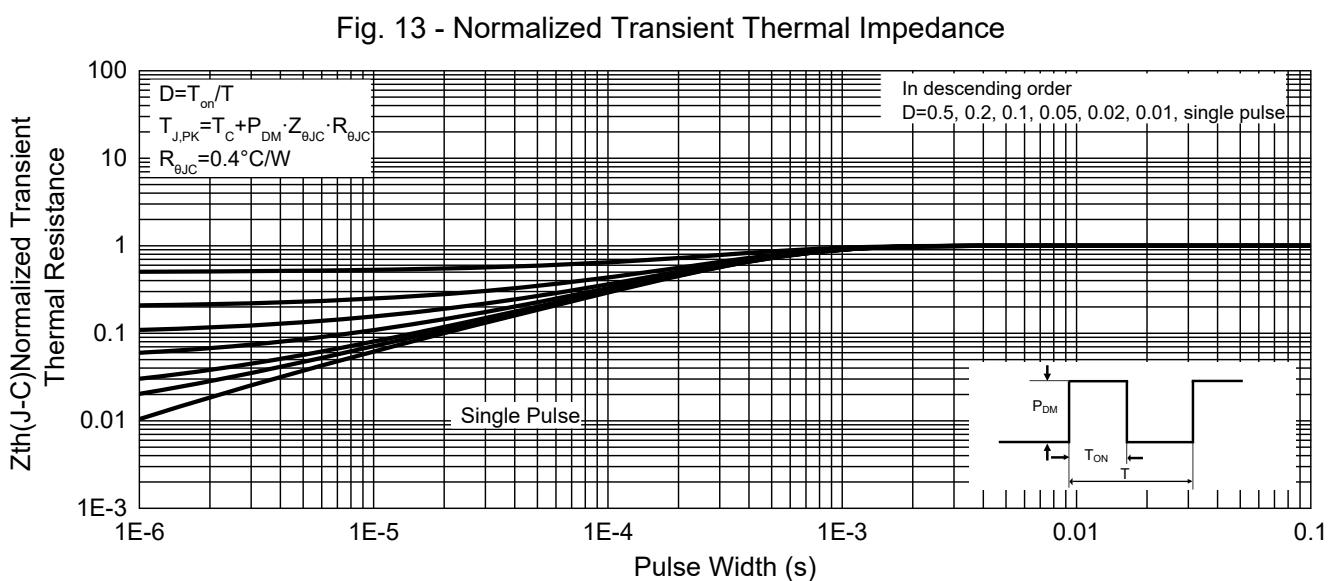
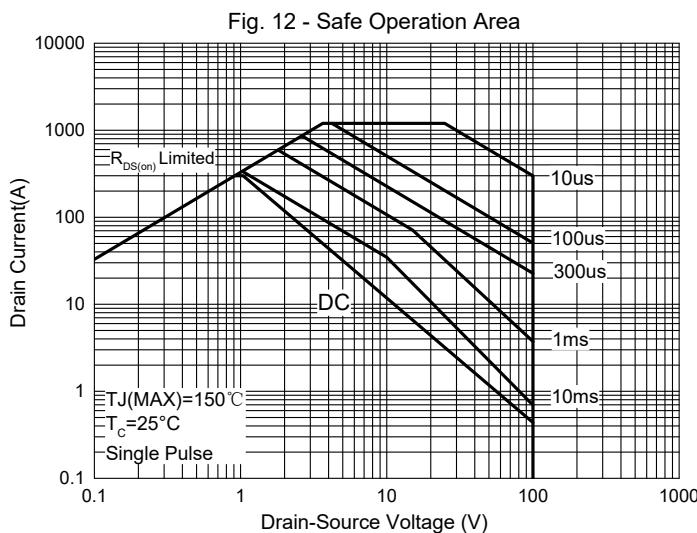
Fig. 6 - Gate Charge



## Curve Characteristics



## Curve Characteristics



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2Kpcs/Reel

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