

PESD3V3Z1BZF

Extremely low capacitance bidirectional ESD protection diode

Product data sheet

1. General description

Extremely low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode, part of the TrEOS protection family, designed to protect one signal line from the damage caused by ESD and other transients. The device is housed in a leadless ultra small and full encapsulated DFN0603-2 (SOD972) Surface-Mounted Device (SMD) package.

2. Features and benefits

- Bidirectional ESD protection of one line
- Extremely low diode capacitance $C_d = 0.28 pF$
- · Extremely low clamping to protect sensitive I/Os
- ESD protection up to ±20 kV according to IEC 61000-4-2
- Leadless ultra small and full encapsulated SMD package

3. Applications

ESD and surge protection for:

- ultra high-speed datalines
- very sensitive interface lines
- generic interface lines

in portable electronics, communication, consumer and computing devices.

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	3.3	V
C _d	diode capacitance	$f = 1 \text{ MHz}; V_R = 0 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$	-	0.28	0.4	pF
		$f = 1 \text{ MHz}; V_R = 1.5 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$	-	0.25	-	pF



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode		1 [1] 2
2	K2	cathode	1 2	sym045
			Transparent top view DFN0603-2 (SOD972)	

6. Ordering information

Table 3. Ordering information

	<u> </u>				
Type number		Package			
		Name	Description	Version	
	PESD3V3Z1BZF	DFN0603-2	Ultra small and leadless full encapsulated package; 2 terminals; body $0.63 \times 0.33 \times 0.2 \text{ mm}$	SOD972	

7. Marking

Table 4. Marking codes

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Type number	Marking code
PESD3V3Z1BZF	Н

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

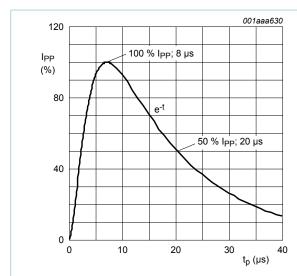
Symbol	Parameter	Conditions		Min	Max	Unit	
I _{PPM}	rated peak pulse current	t _p = 8/20 μs	[1]	-	8	Α	
Tj	junction temperature			-	150	°C	
T _{amb}	ambient temperature			-40	125	°C	
T _{stg}	storage temperature			-65	150	°C	
ESD maximum	ESD maximum ratings						
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[2]	-	20	kV	
		IEC 61000-4-2; air discharge	[2]	-	20	kV	

^[1] According to IEC 61000-4-5

^[2] Device stressed with ten non-repetitive ESD pulses.

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Extremely low capacitance bidirectional ESD protection diode



8/20 µs pulse waveform according to IEC 61000-4-5

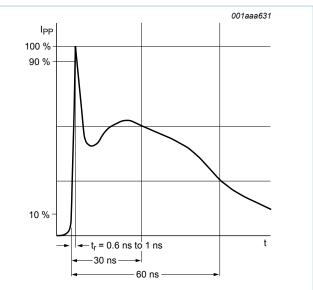


Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
V_{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		-	6.9	8	V
I _{RM}	reverse leakage current	V _{RWM} = 3.3 V; T _{amb} = 25 °C		-	1	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	0.28	0.4	pF
		$f = 1 \text{ MHz}; V_R = 1.5 \text{ V}; T_{amb} = 25 ^{\circ}\text{C}$		-	0.25	-	pF
V _{CL}	clamping voltage	I _{PPM} = 8 A; T _{amb} = 25 °C	[1]	-	4.6	6	V
		I_{PP} = 1 A; T_{amb} = 25 °C; t_p = TLP	[2]	-	3	-	V
		I_{PP} = 16 A; T_{amb} = 25 °C; t_p = TLP	[2]	-	6	-	V
R _{dyn}	dynamic resistance	4 A ≤ I _R ≤ 16 A; T _{amb} = 25 °C	[2]	-	0.19	-	Ω

Non-repetitive current pulse 8/20 μ s exponential decay waveform according to IEC 61000-4-5. Non-repetitive current pulse, Transmission Line Pulse (TLP) t_p = 100 ns; square pulse; ANSI / ESD STM5.5.1-2008

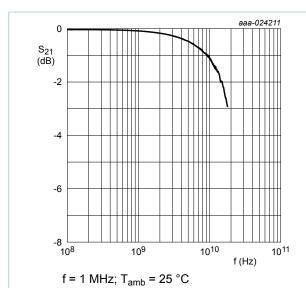


Fig. 3. Insertion loss; typical values

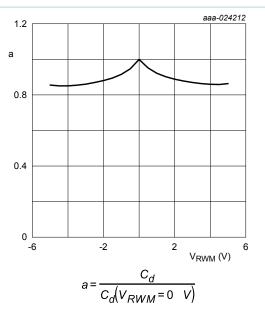


Fig. 4. Relative capacitance as a function of reverse standoff voltage; typical values

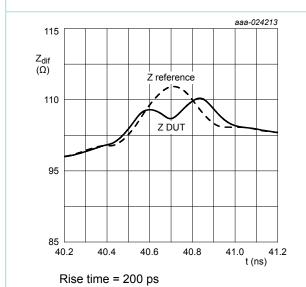
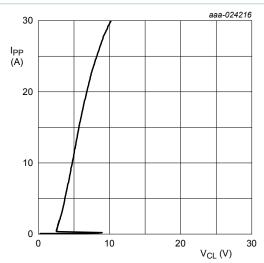


Fig. 5. Differential Time Domain Reflectometer (TDR) plot; typical values

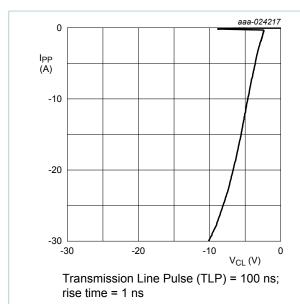


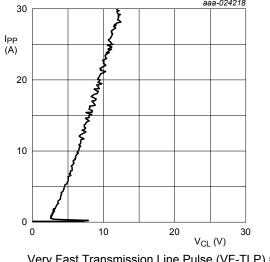
Transmission Line Pulse (TLP) = 100 ns; rise time = 1 ns

Fig. 6. Positive clamping voltage (TLP); typical values

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Extremely low capacitance bidirectional ESD protection diode

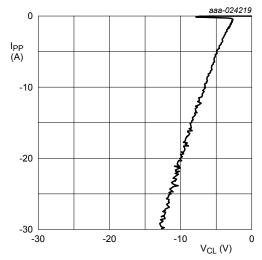




Very Fast Transmission Line Pulse (VF-TLP) = 5

Fig. 7. Negative clamping voltage (TLP); typical values | Fig. 8.



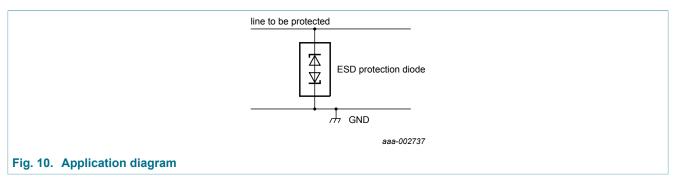


Very Fast Transmission Line Pulse (VF-TLP) = 5 ns

Negative clamping voltage (VF-TLP); typical values Fig. 9.

10. Application information

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground. The device is not designed to be used on lines connected to a DC supply.

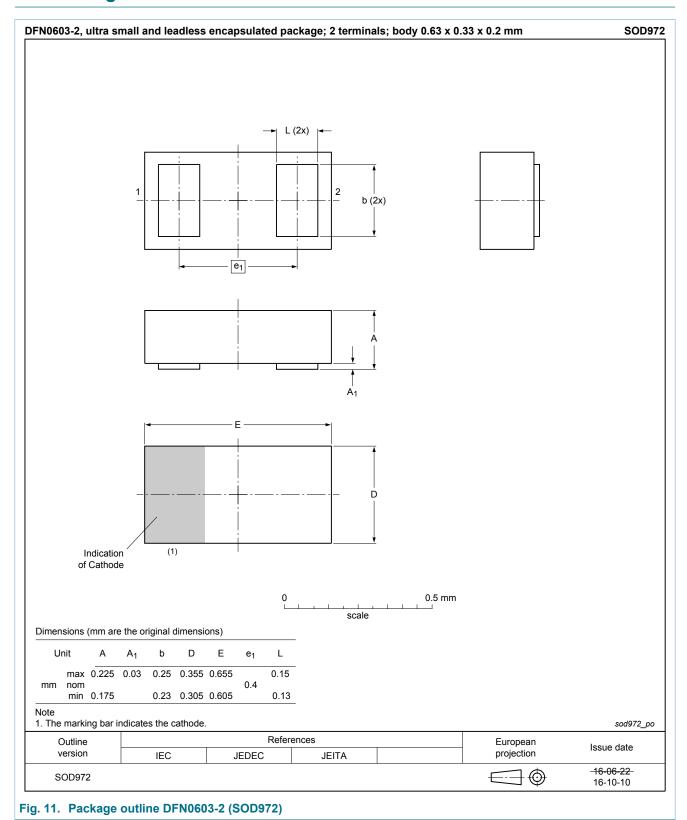


Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline



12. Soldering

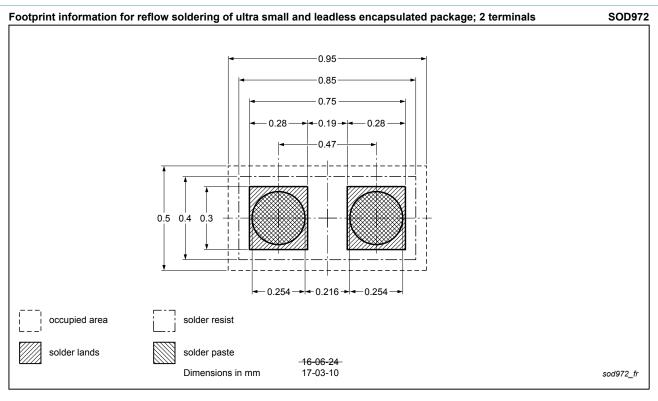


Fig. 12. Reflow soldering footprint for DFN0603-2 (SOD972)

13. Revision history

Table 7. Revision history

rable 1. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD3V3Z1BZF v.5	20170323	Product data sheet	-	PESD3V3Z1BZF v.4
Modifications:	Product status	changed		
PESD3V3Z1BZF v.4	20161006	Preliminary data sheet	-	PESD3V3Z1BZF v.3
PESD3V3Z1BZF v.3	20160719	Preliminary data sheet	-	PESD3V3Z1BZF v.2
PESD3V3Z1BZF v.2	20160701	Preliminary data sheet	-	PESD3V3Z1BZF v.1
PESD3V3Z1BZF v.1	20160624	Preliminary data sheet	-	-

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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