Radial Leaded Ceramic Disc Capacitors

Safety Standard Recognized, C900, Encapsulated, AC Type, X1 440 VAC/Y2 300 VAC (Industrial Grade)



Overview

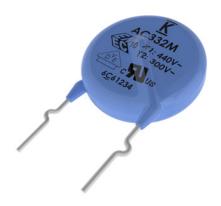
KEMET's 900 encapsulated radial leaded ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution when needing to suppress line disturbances at the source.

Safety Certified Capacitors are classified as either X and/ or Y capacitors. Class X capacitors are primarily used in line-to-line (across-the-line) applications. In this application, there is no danger of electric shock to humans should the capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock.

With a working voltage of 440 VAC in line-to-line (Class X) and 300 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y2 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 5 KV (Y2) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94 V-0.

Benefits

- Safety standard recognized (IEC 60384–14)
- Reliable operation up to 125°C
- Class X1/Y2
- 7.5 and 10 mm lead spacing
- · Lead (Pb)-free and RoHS Compliant
- Halogen-free
- Capacitance offerings ranging from 10 pF up to 10 nF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- · High reliability
- Preformed (crimped) or straight lead configurations
- Non-polar device, minimizing installation concerns
- Encapsulation meets flammability standard UL 94 V-0



Applications

Typical applications include:

- · Line-to-line (Class X) filtering
- · Line-to-ground (Class Y) filtering
- Antenna coupling
- Primary and secondary coupling (switching power supplies)
- Line disturbances suppression (motors and motor controls, relays, switching power supplies, and invertors)

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Ordering Information

C9	7	1	U	472	M	Z	W	D	A	Α	7317
Ceramic Series	Body Diameter	Lead Spacing ^{1,3}	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/ Temp. Char.	Design	Lead Configuration ^{2,3}	Failure Rate	Packaging (C-Spec)
C9 = Ceramic 900 Series	0 = 7.0 mm 1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 4 = 11.0 mm 6 = 13.0 mm 8 = 15.0 mm	7 = 7.5 mm 1 = 10.0 mm	U = Safety	Two significant digits and number of zeroes	J = ±5% K = ±10% M = ±20%	Z = X1 440 VAC /Y2 300 VAC	S = SL Y = Y5P W = Y5U V = Y5V	D = Disc	A = Straight B = Vertical Kink C = Outside Kink	A = N/A	See "Packaging C-Spec Ordering Options Table"

¹ Capacitor body diameter will limit available lead spacing and packaging options. See "Dimensions" and "Product Ordering Codes and Ratings" sections of this document to determine availability.

Packaging C-Spec Ordering Options Table

Packaging Type	Lead Length (mm) ^{2,3}	Packaging Ordering Code (C-Spec)
Ammo Pack	See Note 5	7317
	3.0±1.0	WL30
	3.5±1.0	WL35
Dulle Dan	4.0±1.0	WL40
Bulk Bag	4.5±1.0	WL45
	5.0±1.0	WL50
	20.0 minimum⁴	WL20

¹ Preformed (crimped) lead configurations include "Vertical Kink" and "Outside Kink." See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

³ Bulk packaging lead length availability is dependent upon "Lead Configuration" and "Lead Spacing." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.

² "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

³ For nonstandard lead length inquiries, please contact KEMET.

⁴ Lead length of 20.0 mm minimum only available for straight leads.

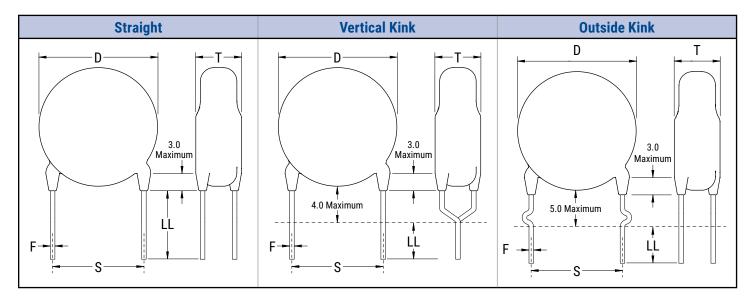
 $^{^{5}}$ Lead length for ammo pack packaging is defined by the H and H $_{0}$ dimensions in Table 3.

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Lead Configurations



Dimensions - Millimeters

	Lead	S	Lead	D	D T Body Body Thickness		ØF
Lead Configuration	Configuration	Lead Spacing ²	Spacing Tolerance				Lead Diameter
Ctroight	Δ.	7.5	±1.0	,			
Straight	Α	10.0	±1.0				
Vertical Kink	В	7.5	±1.0	See Table 1 - "Product Ordering Codes and Ratings"		3.0 maximum	0.5510.1
(Preformed)	В	10.0	±1.0				0.55±0.1
Outside Kink	0	7.5	±1.0				
(Preformed)	C	10.0	±1.0				

¹Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

² Body diameter of capacitor will limit available lead spacing and packaging options. See "Product Ordering Codes and Ratings" sections of this document for further details.

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Approval Standard and Certification No.

Safety Standard	Standard No.	Subclass	Working Voltage	Certificate No.	
VDE	IEC 60384-14	X1	440 VAC	40036415	
(ENEC)	IEG 00304-14	Y2	300 VAC	40030413	
UL	UL 60384-14 and	X1	440 VAC	F256200	
CAN/CSA	E60384-14	Y2	300 VAC	<u>E356389</u>	

These devices are VDE/ENEC and UL recognized for antenna coupling and AC line-to-line (Class X) and line-to-ground (Class Y) applications per IEC60384–14 and UL 60384–14.

Environmental Compliance

These devices are Halogen-free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.





General Specifications/Performance Characteristics

Dielectric/Temperature Characteristic	SL	Y5P	Y5U	Y5V
Operating Temperature Range:		-40°C to	o +125°C	
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	-1,000 ~ +350 ppm/ °C	· '		
Dielectric Withstanding Voltage (7.5 and 10 mm Lead Spacing)	2,600 VAC (60 ±5 seconds at 25°C)			
Quality Factor (Q)	30 pF and above: ≥ 1,000 Below 30 pF: ≥ 400 +(20xC)*	See "Dissipation Factor"		
Dissipation Factor (tanδ) at +25°C1	See "Quality Factor" 2.50% 2.50%			5.0%
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60 ±5 seconds at 25°C)			

^{*}C = Nominal capacitance

SL: 1 MHz ±100 kHz and 1.0 ±0.2 Vrms

X5P, Y5U and Y5V: 1 kHz ±50 Hz and 1.0 ±0.2 Vrms

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

¹ Capacitance and Dissipation Factor (DF) measured under the following conditions:

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Table 1 - Product Ordering Codes and Ratings

D: 1 /				Dimensions (mm)				pacing	
Dielectric/ Temp. Char.	KEMET Part Number	Capacitance Tolerance		Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging	
	C90(1)U100JZSD(2)A(3)	10 pF							
	C90(1)U120JZSD(2)A(3)	12 pF							
	C90(1)U150JZSD(2)A(3)	15 pF							
	C90(1)U180JZSD(2)A(3)	18 pF							
	C90(1)U200JZSD(2)A(3)	20 pF							
	C90(1)U220JZSD(2)A(3)	22 pF							
	C90(1)U240JZSD(2)A(3) C90(1)U270JZSD(2)A(3)	24 pF 27 pF		7.0					
	C90(1)U300JZSD(2)A(3)	30 pF		7.0					
	C90(1)U330JZSD(2)A(3)	33 pF					7.5	mm	
SL	C90(1)U360JZSD(2)A(3)	36 pF	±5%		5.0	0.55±0.1	0		
	C90(1)U390JZSD(2)A(3)	39 pF	İ				10 ו	mm	
	C90(1)U470JZSD(2)A(3)	47 pF					İ		
	C90(1)U500JZSD(2)A(3)	50 pF							
	C90(1)U510JZSD(2)A(3)	51 pF							
	C91(1)U560JZSD(2)A(3)	56 pF							
	C91(1)U620JZSD(2)A(3)	62 pF		8.0					
	C91(1)U680JZSD(2)A(3)	68 pF 75 pF							
	C91(1)U750JZSD(2)A(3) C92(1)U820JZSD(2)A(3)	82 pF		9.0					
	C93(1)U101JZSD(2)A(3)	100 pF		10.0					
	1 030(1)01010200(2)11(0) 1	100 pi		10.0					
	C90(1)U101KZYD(2)A(3)	100 pF							
	C90(1)U151KZYD(2)A(3)	150 pF							
	C90(1)U221KZYD(2)A(3)	220 pF		7.0				7.5 mm or	
VED	C90(1)U331KZYD(2)A(3)	330 pF	1100		F 0	0.55.01			
Y5P	C90(1)U471KZYD(2)A(3) C91(1)U561KZYD(2)A(3)	470 pF 560 pF	±10%		5.0	0.55±0.1			
	C91(1)U681KZYD(2)A(3)	680 pF		8.0		10 mm	10 mm		
	C92(1)U821KZYD(2)A(3)	820 pF							
	C92(1)U102KZYD(2)A(3)	1,000 pF		9.0					
		, , , , ,							
	C90(1)U102MZWD(2)A(3)	1,000 pF		7.0					
	C92(1)U152MZWD(2)A(3)	1,500 pF		9.0			7.5	mm	
Y5U	C92(1)U222MZWD(2)A(3)	2,200 pF	±20%		5.0	0.55±0.1		r	
	C94(1)U332MZWD(2)A(3) C96(1)U392MZWD(2)A(3)	3,300 pF		11.0			10 :		
	C96(1)U472MZWD(2)A(3)	3,900 pF 4,700 pF		13.0					
	1070(1)0472WIZWD(Z)A(3)	4,700 μι							
	C90(1)U102MZVD(2)A(3)	1,000 pF							
	C90(1)U152MZVD(2)A(3)	1,500 pF		7.0					
	C90(1)U222MZVD(2)A(3)	2,200 pF					7 5	mm	
Y5V	C92(1)U332MZVD(2)A(3)	3,300 pF	±20%	9.0	5.0	0.55±0.1		ии Г	
'''	C94(1)U392MZVD(2)A(3)	3,900 pF	120.0	11.0	0.0	0.0020.1	10 ו		
	C94(1)U472MZVD(2)A(3)	4,700 pF							
	C96(1)U682MZVD(2)A(3) C98(1)U103MZVD(2)A(3)	6,800 pF		13.0 15.0					
	1 020(1)U1U3MZVD(Z)A(3)	10,000 pF		13.0					
	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Lead S	pacing	

⁽¹⁾ To properly complete ordering code, insert the one-digit numeric code to reflect required lead spacing: (Note that select capacitance values and packaging options may limit lead spacing availability. See table above to verify availability.)

^{7 = 7.5} mm

 $^{1 = 10.0 \, \}text{mm}$

⁽²⁾ To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

A = Straight

B = Vertical Kink

C = Outside Kink

⁽³⁾ To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code." See "Dimensions" section of this document, page 2, for available options.

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Table 2 - Performance & Reliability: Test Methods and Conditions

It	em	Speci	fication	Test Method			
Operating Tem	perature Range			-40°C to +125°C			
	Between lead wires	No failures		The capacitor shall not be damaged when 2,600 VAC (rms) is applied between the lead wires for 60 seconds.			
Dielectric Strength Body Insulation		No failures		into a container inied with inetai bans		is tightly acitor m each ted ls 500 VAC veen the	
Insulation R	esistance (IR)	10,000 M	Ω minimum	The insulation res		easured with 500 ±50 VDC	
Сара	citance	Within spec	ified tolerance				
		Temperature Characteristics	Specification				
		Y5P, Y5U	DF ≤ 2.5%	Y5P, Y5U, and Y5V: Capacitance is measured at 1 kHz ±20% and 5 V _{rms} or less. (20±2°C) SL: Capacitance is measured at 1 MHz ±20% and			
	(5.5)	Y5V	DF ≤ 5.0%				
Dissipation F	actor (DF) or Q	SL	\geq 30 pF: Q \geq 1,000 < 30 pF: Q \geq 400 +(20 x C) C = Nominal capacitance	1.0 ±0.2 V _{rms} (25°C)			
				A capacitance me	easurement is mad	e at each step specified:	
		T	Capacitance Change	Step	Temperature		
		Temperature Characteristics		1	+20±2°C		
			J	2	-25±2°C		
Temperature	Characteristics	Y5P	Within ±10%	3	+20±2°C		
•		Y5U	Within +20%/-55%	4	+85±2°C		
		Y5V	Within ~+30%/-80%	5	+20±2°C		
		SL	-1,000 ~+350 ppm°C (+20°C ~+85°C)	Pre-treatment: Capacitor is stored at 85±2°C for 1 hour and then placed at room			
	Tensile		citor body shall not eak.	condition¹ for 24±2 hours before measurement. With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen. With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.			
Terminal Strength	Bending		citor body shall not eak.				

 $^{^1}$ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Table 2 - Performance & Reliability: Test Methods and Conditions cont.

Item		Speci	ification	Test M	lethod			
Solder	ability	of solder in the ax	ave a uniform coating ial direction and over ircumference.	The lead wire of the capacitor is dipped into molten solder for 5±0.5 seconds. The depth of immersion is up to 1.5 mm (+5/-0 mm) from the root of lead wires. Solder Temperature: Lead free solder (Sn-3Ag - 0.5 Cu) 245°C ±5°C.				
	Appearance	No vis	ual defect	As shown in the figure below, the lead wires are immersed in molten solder up to 1.5 mm (+5/-0 mm) from the end of the				
	IR	1,0	00 ΜΩ	epoxy meniscus (root of lead wire).				
	Dielectric Strength	Per	item 1	Duration/Solder Temperature: 3.5±0.5 seconds/350°C ±10°C or 10±1 seconds/260°C ±5°C				
Soldering Effect (Non-Preheat)	Capacitance	SL: Within ±2.5% o	Y5V: Within ±10% or ±0.25 pF, whichever larger	Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition¹ for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition¹.				
	Appearance	No visi	ual defect	Capacitor is stored at 120°C +0/-				
	IR	1,0	00 ΜΩ	Then, as shown in the figure belo in molten solder up to 1.5 mm (+)	5/-0mm) from the end of the			
	Dielectric Strength	Per item 1		epoxy meniscus (root of lead wire). Duration/Solder Temperature: 7.5 +0/-1 seconds/260°C ±5°C				
Soldering Effect (Preheat)	Capacitance	SL: Within ±2.5% o	Y5V: Within ±10% or ±0.25 pF, whichever larger	Thermal Capacitor Screen 1.5 to 1.5 to Molten Solder Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition¹ for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition¹.				
	Appearance	No visual defect		Steady State Humidity:	Load Humidity:			
		Temperature Characteristics	Capacitance Change					
		Y5P	Within ±10%					
	Capacitance	Y5U	Within ±20%					
	, - p	Y5V	Within ±30%					
Biased Humidity		SL	Within ±2.5% or ±0.25 pF, whichever is larger	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours.	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours with full rated voltage applied.			
	DF		: 5.0% maximum % maximum	Post Treatment: Capacitor is stored for 1 to 2 hours at room condition ¹ .	Post Treatment: Capacitor is stored for 1 to 2 hours at room condition ¹ .			
	Q	Q ≥ 100 More than C = Nomina	s than 30 pF: + 10 × C/3 30 pF: Q ≥ 200 al capacitance		nouis at 100111 Condition'.			
	IR		: 3,000 MΩ minimum MΩ minimum					
	Dielectric Strength		failures					

[&]quot;Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Table 2 - Performance & Reliability: Test Methods and Conditions cont.

Ite	em	Specification	Test Method
High Temperature Life	Appearance Capacitance Change IR Dielectric Strength	No visual defect Y5P, Y5V, and Y5U: Within ±20% SL: Within ±3 or ±0.3 pF, whichever is larger. 3,000 MΩ minimum SL: 1,000 MΩ minimum	Impulse Voltage: Each individual capacitor is subjected to three 5 kv impulses prior to life testing. Cx tr td (uF) (uS) (uS) (uS) (uS) (0.01 1.2 46 0.1 1.5 47) Capacitors are placed in a circulating air oven for a period of
Flame Test		The capacitor flame extinguishes as follows:	1,000 hours. The air in the oven is maintained at a temperature of 125°C ±2 throughout the test. The capacitors are subjected to AC 510 V _{rms} . Each hour the voltage is increased to 1,000 V _{rms} for 0.1 seconds. The capacitor is exposed to a flame for 15 seconds and then removed for 15 seconds. This test is repeated for 5 cycles.
		CycleTime1 ~ 430 seconds maximum560 seconds maximum	Gas Burner (Unit:mm)
			The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge. Story C1 C2 C3 C3 C4 C1 Vt
Active Flammability		The cheesecloth should not ignite.	$ \begin{array}{ c c c c c c } \hline C_{1,2} & 1 \ \mu F \pm 10\% & C_{3} & 0.033 \ \mu F \pm 5\% \ 10 \ kV \\ \hline L_{I\cdot4} & 1.5 \ Mh \pm 20\% \ 16A \ Rod \ core \ choke & Cx & Test \ capacitor \\ \hline R & 100\pm 2\% & V_{_{AC}} & VR \pm 5\% \\ \hline Ct & 3 \ \mu F \pm 5\% \ 10 \ kV & V_{_{R}} & Rated \ Voltage \\ \hline F & Fuse, Rated \ 10A & Vt & Voltage \ applied \ to \ Ct \\ \hline Vx & \\ \hline \hline Vx & \\ \hline \hline $

[&]quot;Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Table 2 - Performance & Reliability: Test Methods and Conditions cont.

Ite	em	Specif	ication	Test Method				
Passive Flammability		seco	nould not exceed 30 onds. should not ignite.	which best flame one to the state of the sta	Test Speciment 2000 ±5mm Tissue About 10mm Thick Board f exposure to flame: Length of flame: Gas burner length: Inside diameter: Outside diameter:	specimen is e	xposed to the	
	Appearance	No visua	al defect					
	_	Temperature Characteristics SL	Capacitance Change Within ±5%	The capacitor is subjected to 5 temperature cycles. Temperature Cycle				
	Capacitance	Y5P	Within ±10% Within ±20%	Step	Temperature (°C)	Dwell Time	Transition Time	
		Y5U, Y5V	WILIIII ±20%			(minutes)	(minutes)	
Temperature		SL	≥ 30 pF: Q ≥ 350	1	-40+0/-3	30		
Ċycle			< 30 pF: Q ≥ 275	2	Room temperature	3	3	
			+5/2C C = Nominal	3	125+3/-0	30	3	
	DF/Q		capacitance	4	Room temperature	3		
		Y5P	DF ≤ 5%			<u> </u>		
		Y5U, Y5V	DF ≤ 7.5%	Pre-treatment: Capacitor shall be stored at 85 ±2 for 1 hour th placed at room condition for 24 ±2 hours. Post-treatment: Capacitor is stored for 1 to 2 hours at room				
	IR	3,000 MΩ	minimum	condition ¹ .		J 101 1 10 ∠ NOU	is at toom	
	Dielectric Strength	No fa	ilures					

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Soldering and Mounting Information

Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

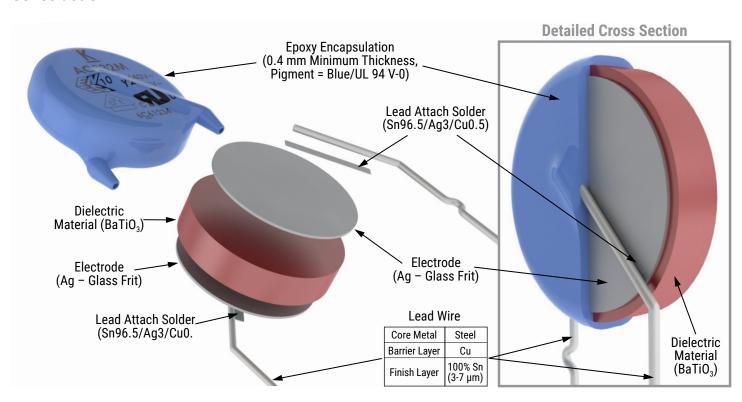
- Temperature of iron-tip: 400°C maximum
- · Soldering iron wattage: 50 W maximum
- · Soldering time: 3.5 seconds maximum

Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions:

- · Rinse bath capacity: Output of 20 watts per liter or less
- · Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

Construction



Radial Leaded Ceramic Disc Capacitors

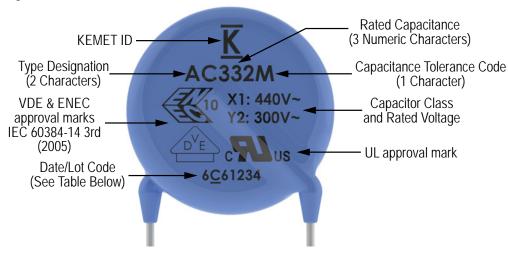
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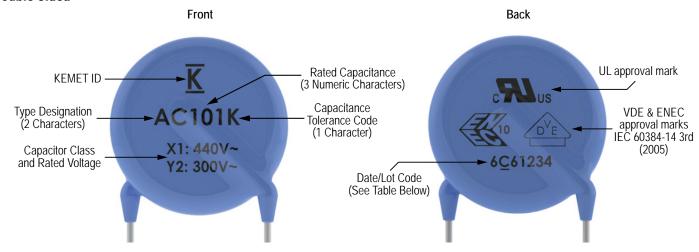
Marking

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance, and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two-sided marking is limited to capacitors with body diameters ≤ 8.0 mm)

Single Sided



Double Sided



Date/Lot Code Explanation

6	<u>C</u>	6	1234
Last digit of year, e.g., 6 = 2016	Manufacturing Location Code	Manufacturing Month: 1-9 = Jan - Sept A = October N = November D = December	Last 4 digits of lot number

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Safety Standard Recognized, C900, Encapsulated, AC Type, X1 440 VAC/Y2 300 VAC (Industrial Grade)



Packaging Quantities

Capacitor		5 !! 5	Ammo Pack (Carrier Tape Component pitch on carrier 12.7 mm 15 mm		ape)
Body Diameter	Body Diameter Code ¹	Bulk Bag (Loose)			er tape ²
(mm)	oouc	(20000)			25.4 mm
7.0	0		·		
8.0	1				
9.0	2				1 000 pieces /hey
10.0	3				1,000 pieces/box
11.0	4	500 pieces/bag	1,000 pi	eces/box	
12.0	5				
13.0	6				
14.0	7				500 pieces/box
15.0	8				

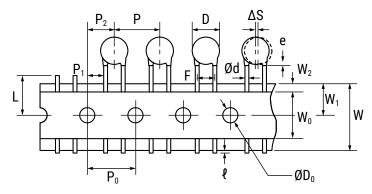
¹ The "Body Diameter Code" is located in the third character position of the ordering code. This code identifies the maximum diameter of the capacitor body in millimeters. For more information regarding the ordering code, see "Ordering Information" section of this document.

² For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.

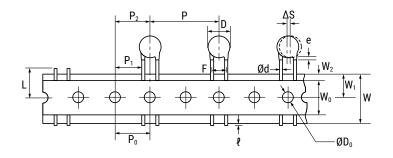


Figure 1 - Ammo Pack Taping Format

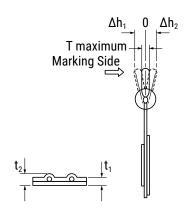
5 mm and 7.5 mm Lead Spacing:



10 mm Lead Spacing:



For All Lead Spacing:



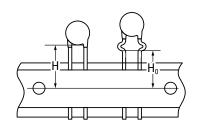


Table 3 – Ammo Pack Taping Specifications

Lead Spacing		5 mm				7.5 mm				10 mm	
Lead Style		Straight		Preformed ¹		Stra	aight Prefo		rmed¹	Straight	Preformed ¹
Item	Symbol	Dimensions (mm)									
Lead Spacing	F		5.0	±1.0		7.5 ±1.0			10.0	±1.0	
Component Pitch	Р	12.7 ±1.0	25.4 ±2.0	12.7 ±1.0	25.4 ±2.0	12.7 ±1.0	25.4 ±2.0	12.7 ±1.0	25.4 ±2.0	25.4	±2.0
Sprocket Hole Pitch	P ₀	12.7 ±0.3 12.7 ±0.3			12.7 ±0.3						
Sprocket Hole Center to Component Center	P ₂	6.35 ±1.5	12.7 ±1.5	6.35 ±1.5	12.7 ±1.5	7.5±1.5	12.7 ±1.5	7.5±1.5	12.7 ±1.5	12.7 ±1.5	
Sprocket Hole Center to Lead Center	P ₁	3.75±1.0	10.2 ±1.5	3.75±1.0	10.2 ±1.5	3.75±1.0	8.95 ±1.5	3.75±1.0	8.95 ±1.5	7.7 ±1.5	
Body Diameter	D	See "Product Ordering Codes and Ratings" section of this document.									
Component Alignment (side/side)	ΔS	0 ±2.0									
Carrier Tape Width	w	18.0 +1.0/-0.5									
Sprocket Hole Position	W ₁	9.0 ±0.5									

¹ Prefromed (crimped) lead configurations include vertical kink and outside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² Also referred to as "lead length" in this document.

Radial Leaded Ceramic Disc Capacitors

Safety Standard Recognized, C900, Encapsulated, AC Type, X1 440 VAC/Y2 300 VAC (Industrial Grade)



Table 3 – Ammo Pack Taping Specifications cont.

Lead Spacing		5 1	nm	7.5	mm	10 mm			
Lead Style		Straight	Preformed ¹	Straight Preformed ¹		Straight	Preformed ¹		
Item	Symbol	Dimensions (mm)							
Height to Seating Plane ² (preformed leads ¹)	H ₀	N/A	18.0 +2.0/-0	N/A	18.0 +2.0/-0	N/A	18.0 +2.0/-0		
Height to Seating Plane ² (straight leads)	Н	20.0 +1.5/-1.0	N/A	20.0 +1.5/-1.0	N/A	20.0 +1.5/-1.0	N/A		
Lead Protrusion	ę	2.0 maximum							
Diameter of Sprocket Hole	D ₀	4.0 ±0.2							
Lead Diameter	φd	0.55 ±0.1							
Carrier Tape Thickness	t ₁	0.6 ±0.3							
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	t ₂	1.5 maximum							
Component Alignment	Δh ₁	2.0 maximum							
(front/back)	Δh ₂	Z.U IIIAXIMUM							
Cut Out Length	L	11.0 maximum							
Hold-Down Tape Width	W _o	11.0 m	inimum	11.5 minimum					
Hold-Down Tape Position	W ₂	3.0 ma	iximum	1.5 ±1.5					
Coating Extension on Leads (meniscus)	e	3.0 maximum for straight lead; not to exceed the bend for preformed1 lead configurations.							
Body Thickness	Т	See "Product Ordering Codes and Ratings" section of this document.							

¹ Prefromed (crimped) lead configurations include vertical kink and outside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

Application Notes

Storage and Operating Conditions:

The insulating coating of these devices does not form an air and moisture-tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees Centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

Working Voltage:

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.

² Also referred to as "lead length" in this document.

Radial Leaded Ceramic Disc Capacitors

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Application Notes cont.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

Operating Temperature and Self-Generating Heat:

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

Handling - Vibration and Impact:

Do not expose these devices or their leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

Radial Leaded Ceramic Disc Capacitors

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