

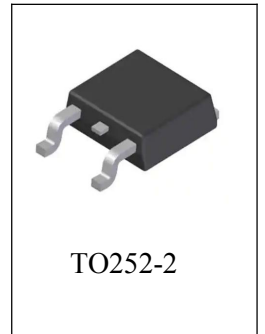
78DXX

3-Terminal Positive Voltage Regulator

General Description

The 78DXX family is monolithic fixed voltage regulator integrated circuit.

They are suitable for applications that required supply current up to 1A.



Features

- ◆ Output Current Up To 1 A
- ◆ Fixed Output Voltage Of 5V, 6V, 8V, 9V, 10V, 12V, 15V, 18V and 24V Available
- ◆ Thermal Overload Shutdown Protection
- ◆ Short Circuit Current Limiting Output
- ◆ Transistor SOA Protection

Package Information

Part NO.	Package Description	Package Marking	Package Option
78DXX	TO252-2	CHMC 78DXX SXXXX	70/Tube 2500/Reel

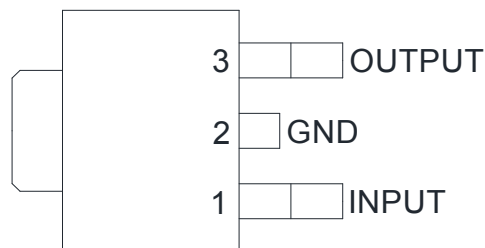
CHMC: Trademark

SXXXX: Lot NO.

78DXX: Part NO. (XX: Voltage)

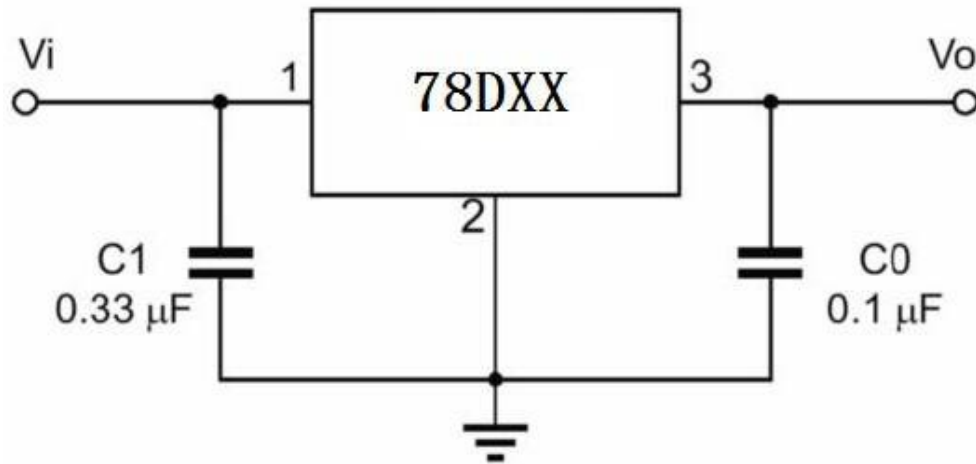
XX(Output Voltage): 05(5.0V)/06(6.0V)/08(8.0V)/09(9.0V)/10(10V)/12(12V)/15(15V)/18(18V)/24(24V)

Pin Connection



78DXX(TO252-2)

Typical Application Circuits



Absolute Maximum Ratings ($T_J=25^{\circ}C$, unless otherwise specified)

Characteristic	Value	Value	Unit
Input Voltage	V_i	35	V
Thermal Resistance Junction-Case	$R_{\theta JC}$	10	$^{\circ}C/W$
Thermal Resistance Junction-Air	$R_{\theta JA}$	93	$^{\circ}C/W$
Operating Temperature Range	T_{OPR}	-40~+125	$^{\circ}C$
Storage Temperature Range	T_{STG}	-55~+150	$^{\circ}C$

Electrical Characteristics(Unless otherwise specified, $I_o=500\text{mA}$, $V_i=10\text{V}$, $C_i=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$)**For 78D05 ($V_{IN}=10\text{V}$, $I_{OUT}=0.5\text{A}$)**

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=+25^\circ\text{C}$, $I_o=5\text{mA}\sim 1\text{A}$	4.8	5.0	5.2	V
		$7.5\text{V}\leq V_i\leq 20\text{V}$, $I_o=5\text{mA}\sim 1\text{A}$	4.75	5.0	5.25	
Line Regulation*	ΔV_o	$7\text{V}\leq V_i\leq 25\text{V}$, $T_j=25^\circ\text{C}$			50	mV
		$7.5\text{V}\leq V_i\leq 20\text{V}$, $I_o=1\text{A}$, $T_j=25^\circ\text{C}$			50	
Load Regulation*	ΔV_o	$T_j=25^\circ\text{C}$, $I_o=5\text{mA}\sim 1\text{A}$		25	50	mV
		$T_j=25^\circ\text{C}$, $I_o=0.25\text{A}\sim 0.75\text{A}$		10	25	
Quiescent Current	I_Q	$T_j=+25^\circ\text{C}$, $I_o\leq 1.0\text{A}$		5.0	8.0	mA
Quiescent Current Change	ΔI_Q	$7.5\text{V}\leq V_i\leq 20\text{V}$			1.0	mA
		$5\text{mA}\leq I_o\leq 1\text{A}$			0.5	
Output Noise Voltage	V_N	$10\text{Hz}\leq f\leq 100\text{kHz}$, $T_j=25^\circ\text{C}$		40	200	μV
Ripple Rejection	RR	$8\text{V}\leq V_i\leq 18\text{V}$, $f=120\text{Hz}$	59	80		dB
Dropout Voltage	V_d	$T_j=+25^\circ\text{C}$		2		V
Short Circuit Current	I_{SC}	$V_i=30\text{V}$, $V_o=0\text{V}$, $T_j=+25^\circ\text{C}$		0.25		A
Peak Current	I_{PK}	$T_j=+25^\circ\text{C}$		1.8		A

For 78D06 ($V_{IN}=11\text{V}$, $I_{OUT}=0.5\text{A}$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=+25^\circ\text{C}$, $I_o=5\text{mA}\sim 1\text{A}$	5.76	6.0	6.24	V
		$8.5\text{V}\leq V_i\leq 21\text{V}$, $I_o=5\text{mA}\sim 1\text{A}$	5.7	6.0	6.3	
Line Regulation*	ΔV_o	$8\text{V}\leq V_i\leq 25\text{V}$, $T_j=25^\circ\text{C}$			60	mV
		$8.5\text{V}\leq V_i\leq 21\text{V}$, $I_o=1\text{A}$, $T_j=25^\circ\text{C}$			60	
Load Regulation*	ΔV_o	$T_j=25^\circ\text{C}$, $I_o=5\text{mA}\sim 1\text{A}$			60	mV
		$T_j=25^\circ\text{C}$, $I_o=0.25\text{A}\sim 0.75\text{A}$			30	
Quiescent Current	I_Q	$I_o\leq 1.0\text{A}$			8.0	mA
Quiescent Current Change	ΔI_Q	$8.5\text{V}\leq V_i\leq 21\text{V}$			1.0	mA
		$5\text{mA}\leq I_o\leq 1\text{A}$			0.5	
Output Noise Voltage	V_N	$10\text{Hz}\leq f\leq 100\text{kHz}$, $T_j=25^\circ\text{C}$		45		μV
Ripple Rejection	RR	$9\text{V}\leq V_i\leq 19\text{V}$, $f=120\text{Hz}$, $I_o=500\text{mA}$, $T_j=25^\circ\text{C}$	56	75		dB
Dropout Voltage	V_d	$T_j=+25^\circ\text{C}$		2		V
Short Circuit Current	I_{SC}	$V_i=35\text{V}$, $V_o=0\text{V}$, $T_j=+25^\circ\text{C}$		0.25		A
Peak Current	I_{PK}	$V_i=10\text{V}$, $V_o=0\text{V}$, $T_j=+25^\circ\text{C}$		1.8		A

For 78D08 ($V_{IN}=14V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	7.68	8.0	8.32	V
		$10.5V \leq V_i \leq 23V$, $I_O=5mA \sim 1A$	7.6	8.0	8.4	
Line Regulation*	ΔV_O	$10.5V \leq V_i \leq 25V$, $T_J=25^{\circ}C$			80	mV
		$10.5V \leq V_i \leq 23V$, $I_O=1A$, $T_J=25^{\circ}C$			80	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			80	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			40	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$10.5V \leq V_i \leq 23V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		58	200	μV
Ripple Rejection	RR	$11.5V \leq V_i \leq 21.5V$, $f=120Hz$, $I_O=500mA$, $T_J=25^{\circ}C$	53	72		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

For 78D09 ($V_{IN}=15V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	8.64	9	9.36	V
		$11.5V \leq V_i \leq 24V$, $I_O=5mA \sim 1A$	8.55		9.45	
Line Regulation*	ΔV_O	$11.5V \leq V_i \leq 25V$, $T_J=25^{\circ}C$			90	mV
		$11.5V \leq V_i \leq 24V$, $I_O=1A$, $T_J=25^{\circ}C$			90	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			90	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			45	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$11.5V \leq V_i \leq 24V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		58		μV
Ripple Rejection	RR	$12.5V \leq V_i \leq 22.5V$, $f=120Hz$, $I_O=500mA$, $T_J=25^{\circ}C$	53	72		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

For 78D10 ($V_{IN}=16V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	9.6	10	10.40	V
		$12.5V \leq V_i \leq 25V$, $I_O=5mA \sim 1A$	9.5		10.5	
Line Regulation*	ΔV_O	$12.5V \leq V_i \leq 25V$, $T_J=25^{\circ}C$			100	mV
		$12.5V \leq V_i \leq 25V$, $I_O=1A$, $T_J=25^{\circ}C$			100	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			100	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			50	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$12.5V \leq V_i \leq 25V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		58		μV
Ripple Rejection	RR	$12.5V \leq V_i \leq 22.5V$, $f=120Hz$, $I_O=500mA$, $T_J=25^{\circ}C$	53	72		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

For 78D12 ($V_{IN}=19V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	11.52	12	12.48	V
		$14.5V \leq V_i \leq 27V$, $I_O=5mA \sim 1A$	11.4		12.6	
Line Regulation*	ΔV_O	$14.5V \leq V_i \leq 30V$, $T_J=25^{\circ}C$			120	mV
		$14.6V \leq V_i \leq 27V$, $I_O=1A$, $T_J=25^{\circ}C$			120	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			120	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			60	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$14.5V \leq V_i \leq 30V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		75		μV
Ripple Rejection	RR	$15V \leq V_i \leq 25V$, $f=120Hz$, $T_J=25^{\circ}C$	52	72		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

For 78D15 ($V_{IN}=23V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	14.4	15	15.6	V
		$17.5V \leq V_i \leq 30V$, $I_O=5mA \sim 1A$	14.25		15.75	
Line Regulation*	ΔV_O	$18.5V \leq V_i \leq 30V$, $T_J=25^{\circ}C$			150	mV
		$17.5V \leq V_i \leq 30V$, $I_O=1A$, $T_J=25^{\circ}C$			150	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			150	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			75	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$17.5V \leq V_i \leq 30V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		90		μV
Ripple Rejection	RR	$18.5V \leq V_i \leq 28.5V$, $f=120Hz$, $T_J=25^{\circ}C$	51	72		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

For 78D18 ($V_{IN}=27V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	17.28	18	18.72	V
		$21V \leq V_i \leq 33V$, $I_O=5mA \sim 1A$	17.1		18.9	
Line Regulation*	ΔV_O	$21V \leq V_i \leq 33V$, $T_J=25^{\circ}C$			180	mV
		$21V \leq V_i \leq 33V$, $I_O=1A$, $T_J=25^{\circ}C$			180	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			180	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			90	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$21.5V \leq V_i \leq 33V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		110		μV
Ripple Rejection	RR	$22V \leq V_i \leq 32V$, $f=120Hz$, $I_O=500mA$, $T_J=25^{\circ}C$	50	67		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

For 78D24 ($V_{IN}=33V$, $I_{OUT}=0.5A$)

Characteristics	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_O	$T_J=+25^{\circ}C$, $I_O=5mA \sim 1A$	23.04	24	24.96	V
		$27V \leq V_i \leq 38V$, $I_O=5mA \sim 1A$	22.8		25.2	
Line Regulation*	ΔV_O	$27V \leq V_i \leq 38V$, $T_J=25^{\circ}C$			240	mV
		$27V \leq V_i \leq 38V$, $I_O=1A$, $T_J=25^{\circ}C$			240	
Load Regulation*	ΔV_O	$T_J=25^{\circ}C$, $I_O=5mA \sim 1A$			240	mV
		$T_J=25^{\circ}C$, $I_O=0.25A \sim 0.75A$			120	
Quiescent Current	I_Q	$T_J=+25^{\circ}C$, $I_O \leq 1.0A$			8.0	mA
Quiescent Current Change	ΔI_Q	$28V \leq V_i \leq 38V$			1.0	mA
		$5mA \leq I_O \leq 1A$			0.5	
Output Noise Voltage	V_N	$10Hz \leq f \leq 100kHz$, $T_J=25^{\circ}C$		170		μV
Ripple Rejection	RR	$28V \leq V_i \leq 38V$, $f=120Hz$, $I_O=500mA$, $T_J=25^{\circ}C$	47	66		dB
Dropout Voltage	V_d	$T_J=+25^{\circ}C$		2		V
Short Circuit Current	I_{SC}	$V_i=35V$, $V_O=0V$, $T_J=+25^{\circ}C$		0.25		A
Peak Current	I_{PK}	$T_J=+25^{\circ}C$		1.8		A

Outline Dimensions

TO252-2

Unit: mm

The image displays three technical views of the TO252-2 package:

- Side View (Top):** Shows the profile of the package with dimensions A (lead width), A1 (lead height), B2 (lead thickness), and A2 (lead angle).
- Front View (Bottom Left):** Shows the front of the package with dimensions D1 (lead height), B1 (lead width), B (lead thickness), and D (package width).
- Top View (Bottom Right):** Shows the top of the package with dimensions C (package length), E1 (package width), E (lead width), and e (lead pitch).

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.470	0.570	0.018	0.023
A1	2.220	2.380	0.087	0.094
A2	0.470	0.570	0.018	0.023
B	0.820	0.840	0.032	0.033
B1	2.380	2.480	0.093	0.098
B2	0.500	0.520	0.019	0.021
C	4.250	4.450	0.167	0.176
D	6.000	6.200	0.236	0.245
D1	1.150	1.250	0.045	0.050
E	0.650	0.850	0.025	0.034
E1	6.450	6.750	0.253	0.266
e	2.285 (BSC)		0.090 (BSC)	

Statements

- Silicore Technology reserves the right to make changes without further notice to any products or specifications herein. Before customers place an order, customers need to confirm whether datasheet obtained is the latest version, and to verify the integrity of the relevant information.
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