

Features

- General Purpose, Low Cost
- Gain Bandwidth Product: 1MHz
- Low Input Bias Current: 10pA (Typ.)
- Low Offset Voltage: 5mV (Max.)
- Quiescent Current: 50µA per Amplifier (Typ.)
- Unity Gain Stable
- Rail-to-Rail Input and Output
- Single or Dual Supply Operation
- Supply Voltage Range: 2.1V to 5.5V
- Operating Temperature: -40°C ~ +125°C
- Type Package:SOP-8

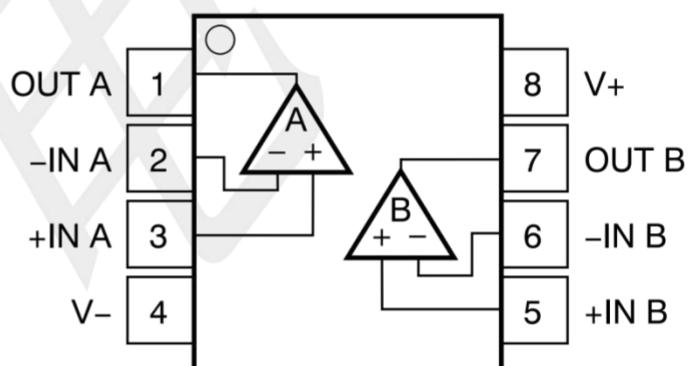
Applications

- Temperature Sensors
- Battery-Powered Instruments
- Smoke/Gas/Environment Sensors
- Medical Equipment
- Portable Instruments and Mobile Device
- Active Filters
- Piezo Electrical Transducer Amplifier
- Sensor Interface
- Handheld Test Equipment

General Description

The TP358 is a single supply, low power CMOS dual operational amplifier; these amplifiers offer bandwidth of 1MHz, rail-to-rail inputs and outputs, and single-supply operation from 2.1V to 5.5V. The embedded anti-RF filter can significantly increase the RF immunity without extra components. Typical low quiescent supply current of 50µA in dual operational amplifiers within one chip and very low input bias current of 10pA make the devices an ideal choice for low offset, low power consumption and high impedance applications such as smoke detectors, photodiode amplifiers, and other sensors.

Pinout (top view)

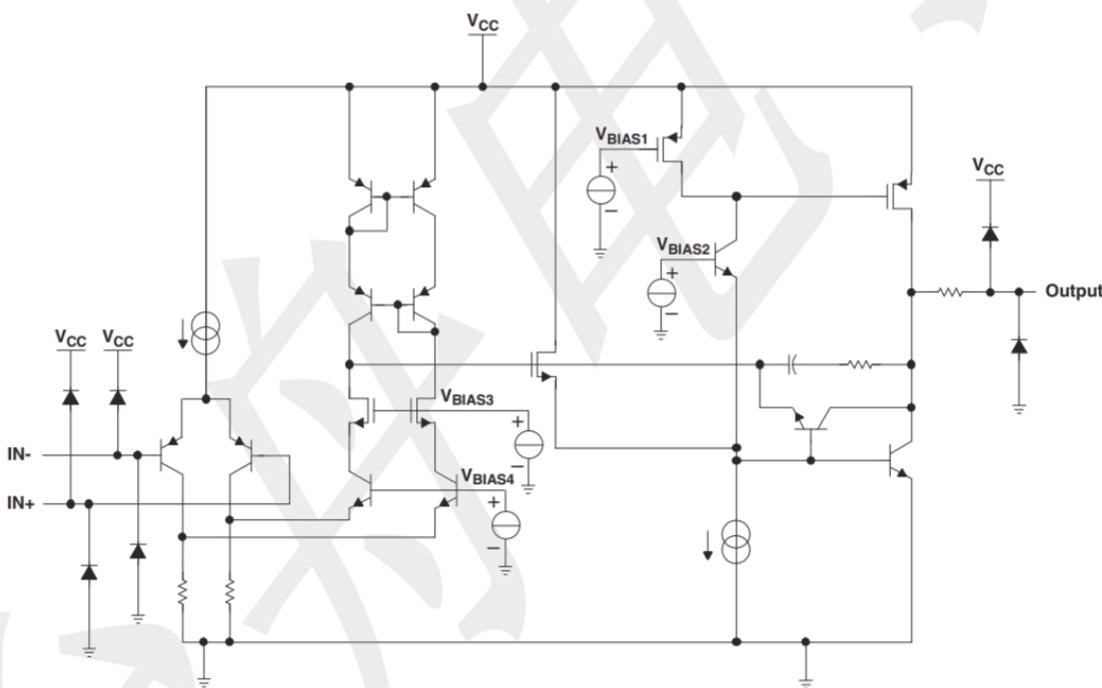
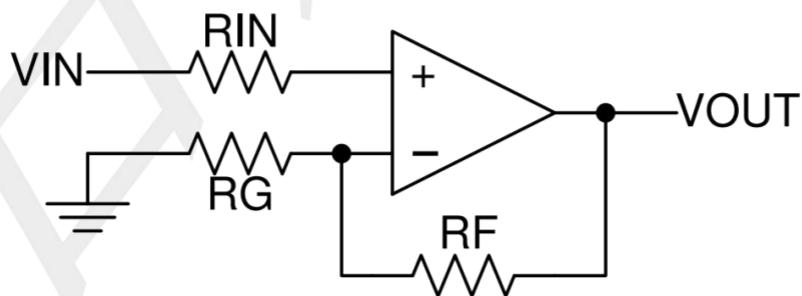


Pin Configurations

| Pin Number | Pin Name | Pin Function |
|------------|----------|-----------------------------------|
| 1 | OUT A | Output A |
| 2 | -IN A | Reverse input A |
| 3 | +IN A | In-phase input A |
| 4 | -V | Chip Supply Voltage(Negative)/GND |
| 5 | +IN B | In-phase input B |
| 6 | -IN B | Reverse input B |
| 7 | OUT B | Output B |
| 8 | +V | Chip Supply Voltage(Positive)/VCC |

Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

| Condition | | Rating | UNIT |
|------------|---------------------------------------|-------------------|------|
| VDD to GND | Power Supply Voltage | 7V | V |
| IN+ or IN- | Signal Input Terminals Voltage | GND-0.3V~VDD+0.3V | V |
| IN+ or IN- | Signal Input Terminals Current | -1mA ~ +1mA | mA |
| OUT to GND | Output Short-Circuit | Continuous | mA |
| TJ | Junction Temperature | 150 | °C |
| LT | Lead Temperature (Soldering, 10 sec.) | 260 | °C |
| TA | Operating Temperature Range | -50 150 | °C |
| Tstg | Storage Temperature Range | -65 150 | °C |

BLOCK DIAGRAM

Power Supply Bypassing


Electrical Characteristics

(At TA = +25°C, VS = +5V, VIN = 0V, unless otherwise noted.)

| PARAMETER | SYMBOL | TEST Conditions | MIN | TYP | MAX | UNIT |
|---------------------------------|--------------------------|---|---------|------|----------------------|-------|
| Supply-Voltage Range | V _{CC} | | 2.1 | -- | 5.5 | V |
| Quiescent Current/Amplifier | I _Q | V _{CC} = 5V | -- | 50 | 80 | uA |
| Input Offset Voltage | V _{OS} | V _{CC} =±2.5 | -2 | 0.5 | 5 | mV |
| Input Offset Voltage Tempco | dV _{OS} /dT | T _A = -55°C to 125°C | -- | 0.65 | -- | µV/°C |
| Input Bias Current | I _B | (2) | -- | 10 | -- | PA |
| Input Offset Current | I _{OS} | (2) | -- | 10 | -- | PA |
| Common-Mode Voltage Range | V _{CM} | | GND-0.1 | -- | V _{CC} +0.1 | V |
| Common-Mode Rejection Ratio | CMRR | V _{DD+0.1} >V _{CM} > V _{SS-0.1} V T _A = -55°C to 125°C | 80 | 95 | -- | dB |
| Power-Supply Rejection Ratio | PSRR | V _{CC} = +2.5V to +5.5V T _A = -55°C to 125°C | 75 | 94 | -- | dB |
| Open-Loop Voltage Gain | A _V | ΔV _{OUT} =1V | 80 | 100 | -- | dB |
| Output Swing from Positive Rail | V _{OUT} - SWING | R _L =5kΩ | -- | 60 | -- | mV |
| Output Swing from Negative Rail | | R _L =100kΩ | -- | 6 | -- | mV |
| Output Short-Circuit Current | I _{SC} | Sinking or Sourcing | -- | 20 | -- | mA |
| Gain Bandwidth Product | GBW | CL=120pF | -- | 1 | -- | MHz |
| Specified temperature | | | -40 | -- | 125 | °C |

Notes:

- 1: All devices are 100% production tested at TA = +25°C; range is guaranteed by design, not production tested.
- 2: Parameter is guaranteed by design.
- 3: Capacitive load drive means that above a given maximum value, the output waveform will oscillate under the step response.

Typical Application Circuit

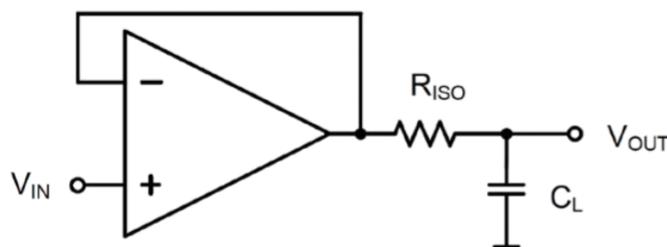


Figure 1. Indirectly Driving Heavy Capacitive Load

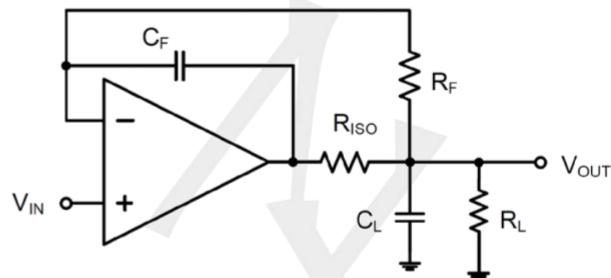


Figure 2. Indirectly Driving Heavy Capacitive Load with DC Accuracy

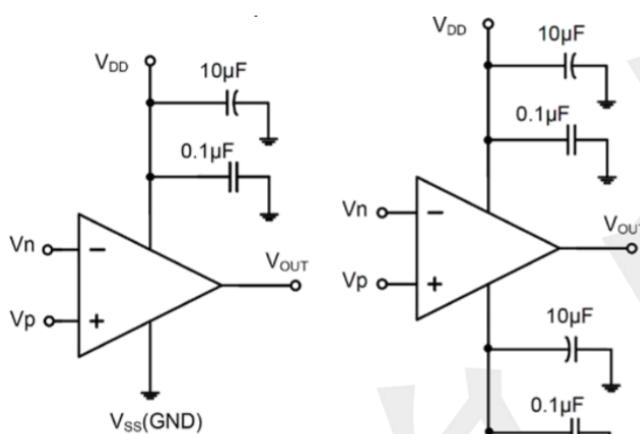


Figure 3. Amplifier with Bypass Capacitors

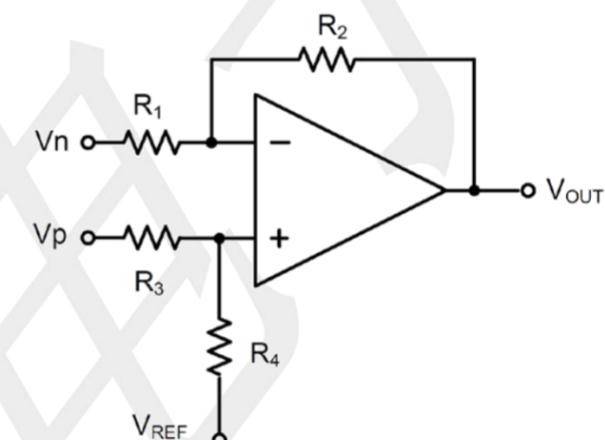


Figure 4. Differential Amplifier

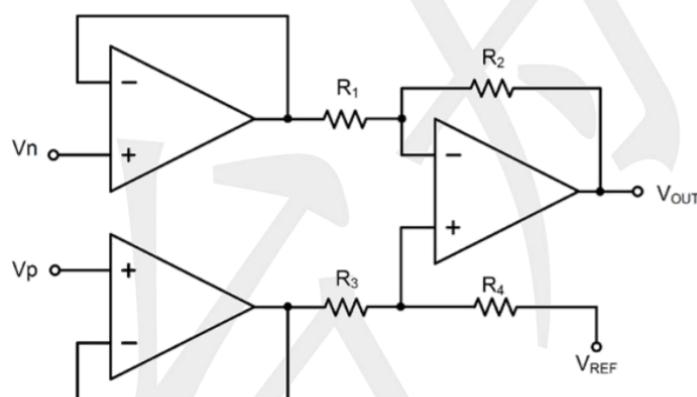


Figure 5. Instrumentation Amplifier

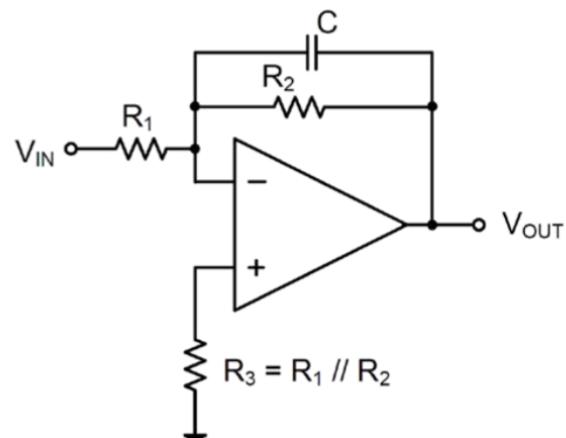
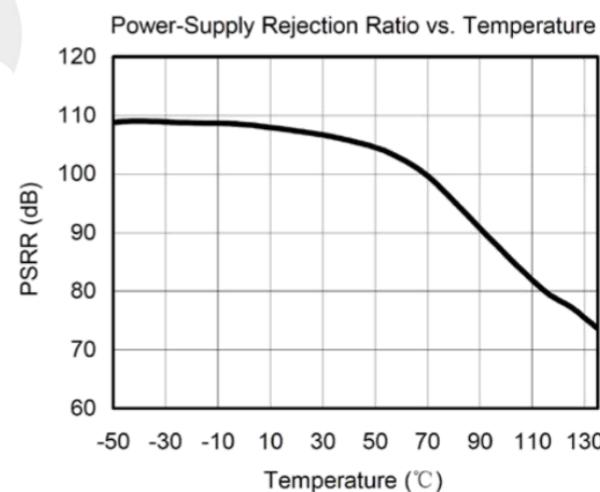
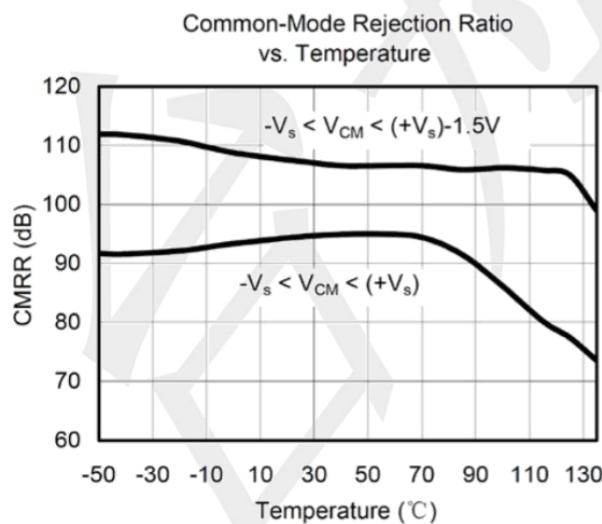
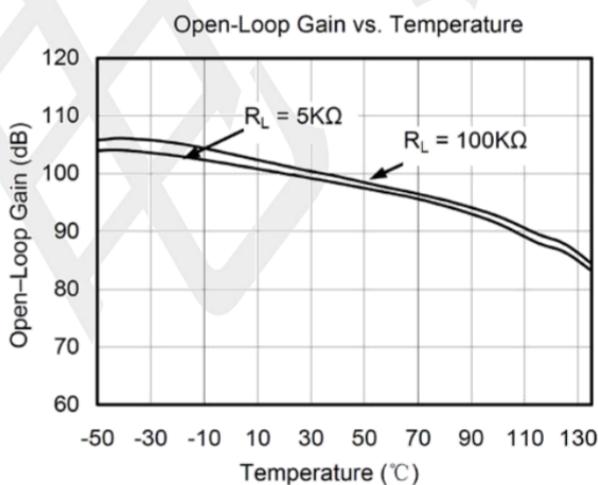
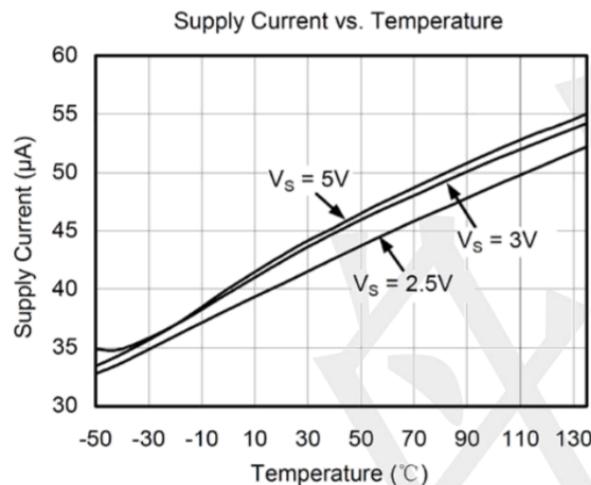
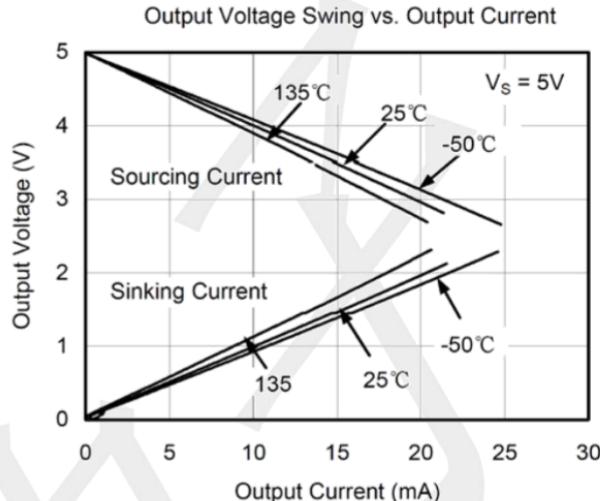
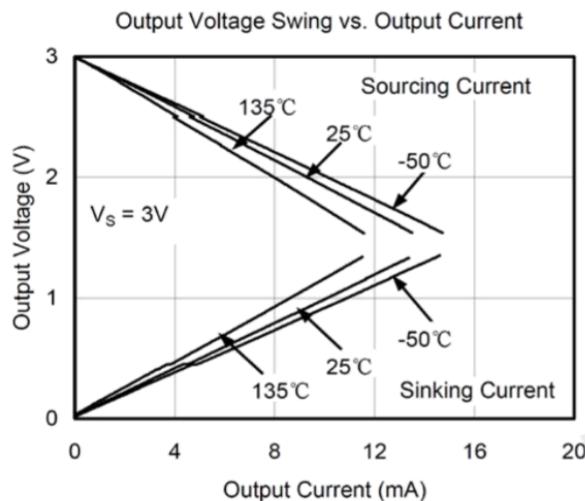


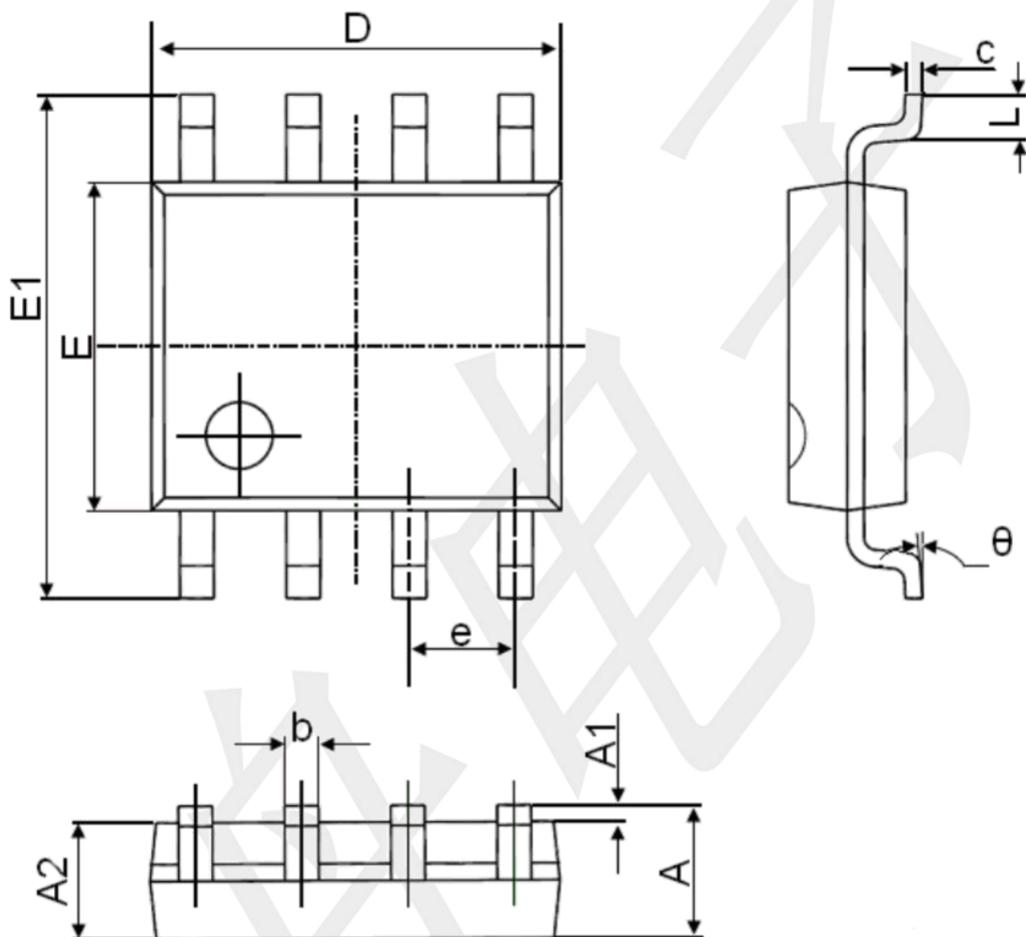
Figure 6. Low Pass Active Filter

Typical Performance Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)



Package information

SOP-8



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 1.350 | 1.750 | 0.053 | 0.069 |
| A1 | 0.100 | 0.250 | 0.004 | 0.010 |
| A2 | 1.350 | 1.550 | 0.053 | 0.061 |
| b | 0.330 | 0.510 | 0.013 | 0.020 |
| c | 0.170 | 0.250 | 0.006 | 0.010 |
| D | 4.700 | 5.100 | 0.185 | 0.200 |
| E | 3.800 | 4.000 | 0.150 | 0.157 |
| E1 | 5.800 | 6.200 | 0.228 | 0.244 |
| e | 1.270(BSC) | | 0.050(BSC) | |
| L | 0.400 | 1.270 | 0.016 | 0.050 |
| θ | 0° | 8° | 0° | 8° |