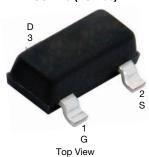


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Vishay Siliconix

Automotive P-Channel 40 V (D-S) MOSFET

SOT-23 (TO-236)



Marking Code: 9F

PRODUCT SUMMARY				
V _{DS} (V)	-40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.075			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.145			
I _D (A)	-4.6			
Configuration	Single			

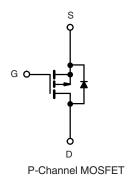
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified c
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2319ADS (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	-40	V	
Gate-source voltage		V_{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	1	-4.6		
Continuous drain current	T _C = 125 °C	- I _D	-2		
Continuous source current (diode conduction)		I _S	-2.2	Α	
Pulsed drain current ^a		I _{DM}	-18		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-13		
Single pulse avalanche energy	L = U.T IIII	E _{AS}	8.4	mJ	
Maximum power dissipation ^a	T _C = 25 °C	В	2.5	W	
	T _C = 125 °C	- P _D	0.5	VV	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB mount b	R _{thJA}	166	°C/W
Junction-to-Foot (Drain)	·	R _{thJF}	50	C/VV

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	ise noted) TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static Static								
Drain-source breakdown voltage	V _{DS}	V _{GS}	V _{GS} = 0, I _D = -250 μA		-	-	.,	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		-2.0	-2.5	V	
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA	
		$V_{GS} = 0 V$	V _{DS} = -40 V	-	-	-1		
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = -40 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	-50	μΑ	
		V _{GS} = 0 V	V _{DS} = -40 V, T _J = 150 °C	-	-	-150]	
On-state drain current a	I _{D(on)}	V _{GS} = -10 V	$V_{DS} \le -5 \text{ V}$	-10	-	-	Α	
		V _{GS} = -10 V	$I_D = -3 A$	-	0.068	0.075	Ω	
Drain agures on state registance 3	Б	V _{GS} = -10 V	I _D = -3 A, T _J = 125 °C	-	0.105	-		
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -10 V	I _D = -3 A, T _J = 150 °C	-	0.115	-		
		V _{GS} = -4.5 V	I _D = -2.4 A	-	0.105	0.145		
Forward transconductance b	9 _{fs}	V _{DS}	$V_{DS} = -5 \text{ V}, I_{D} = -3 \text{ A}$		8		S	
Dynamic ^b								
Input capacitance	C _{iss}			-	464	620		
Output capacitance	C _{oss}	$V_{GS} = 0 V$	$V_{DS} = -20 \text{ V, f} = 1 \text{ MHz}$	-	90	120	pF	
Reverse transfer capacitance	C _{rss}			-	53	70		
Total gate charge ^c	Q_g			-	10.5	16		
Gate-source charge ^c	Q _{gs}	$V_{GS} = -10 \text{ V}$	$V_{DS} = -20 \text{ V}, I_{D} = -3 \text{ A}$	-	1.4	-	nC	
Gate-drain charge ^c	Q _{gd}			-	2.7	-		
Gate resistance	R_g	f = 1 MHz		2.1	4.3	6.4	Ω	
Turn-on delay time ^c	t _{d(on)}	$V_{DD} = -20 \text{ V, } R_L = 6.7 \Omega$ $I_D \cong -3 \text{ A, } V_{GEN} = -10 \text{ V, } R_g = 1 \Omega$		-	4	8		
Rise time ^c	t _r			-	18	28	ns	
Turn-off delay time ^c	t _{d(off)}			-	17	27		
Fall time ^c	t _f			-	17	27		
Source-Drain Diode Ratings and Characteristics ^b								
Pulsed current ^a	I _{SM}			-	-	-18	Α	
		$I_F = -1.5 \text{ A}, V_{GS} = 0$						

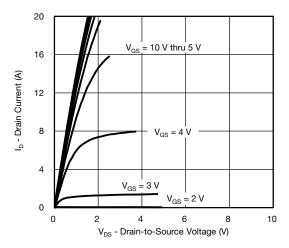
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

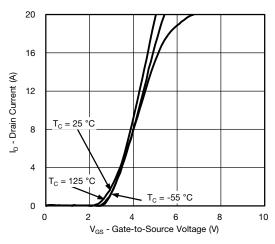
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



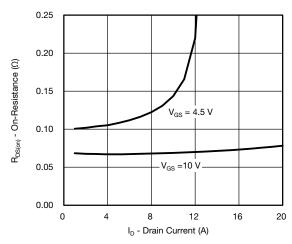
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



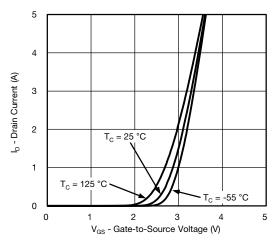
Output Characteristics



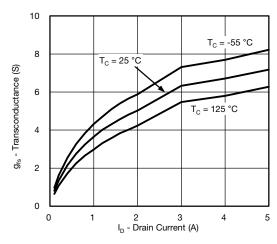
Transfer Characteristics



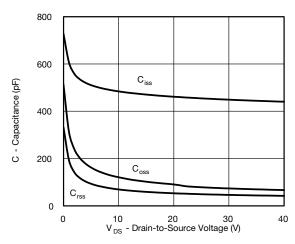
On-Resistance vs. Drain Current



Transfer Characteristics



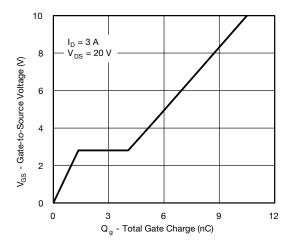
Transconductance



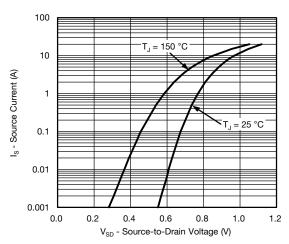
Capacitance



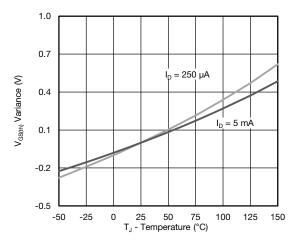
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



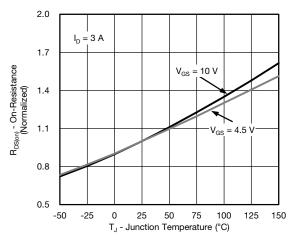
Gate Charge



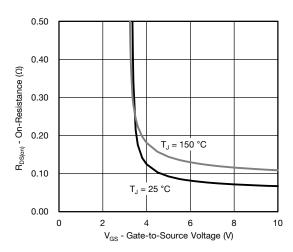
Source Drain Diode Forward Voltage



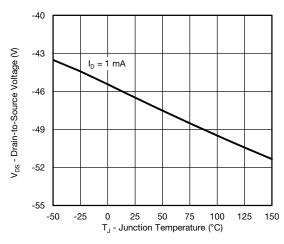
Threshold Voltage



On-Resistance vs. Junction Temperature



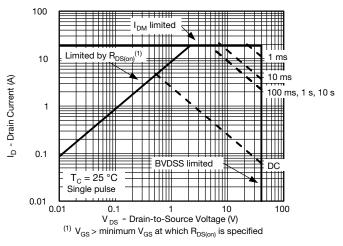
On-Resistance vs. Gate-to-Source Voltage



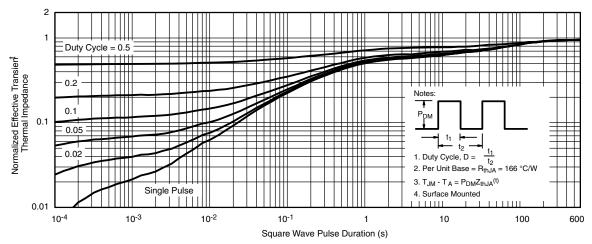
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



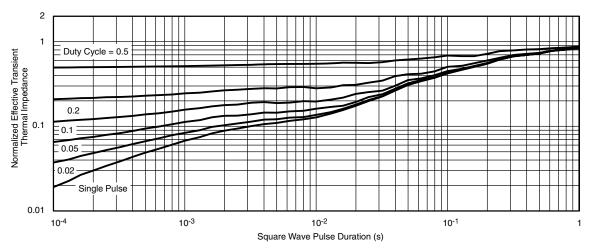
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

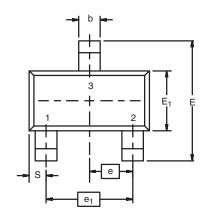
Note

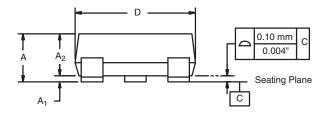
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

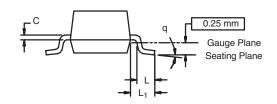
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SOT-23 (TO-236): 3-LEAD







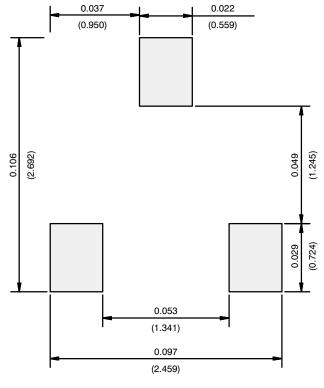
Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01				

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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