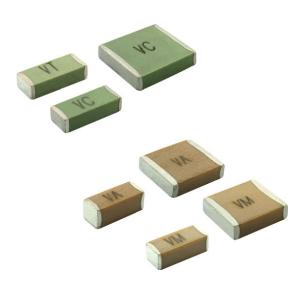


www.vishay.com

Vishay Vitramon

## **Surface Mount Multilayer Ceramic Chip Capacitors** for Safety Certified Applications



#### **LINKS TO ADDITIONAL RESOURCES**







#### **ELECTRICAL SPECIFICATIONS**

• Electrical characteristics at +25 °C unless otherwise specified

Operating Temperature: -55 °C to +125 °C

Capacitance Range X1 / Y2 (1):

C0G (NP0): 10 pF to 1.0 nF X7R: 100 pF to 4.7 nF

Capacitance Range X2 (1): C0G (NP0): 10 pF to 390 pF X7R: 100 pF to 12 nF Voltage Range: 250 V<sub>AC</sub>

#### Temperature Coefficient of Capacitance (TCC):

COG (NP0): 0 ppm/°C  $\pm$  30 ppm/°C from -55 °C to +125 °C X7R:  $\pm$  15 % from -55 °C to +125 °C, with 0  $V_{DC}$  applied

#### Dissipation Factor (DF) (1):

C0G (NP0): 0.1 % maximum X7R: 2.5 % maximum

#### **Insulating Resistance:**

at +25 °C 100 000 M $\Omega$  min. or 1000  $\Omega$ F whichever is less at +125 °C 10 000 M $\Omega$  min. or 100  $\Omega$ F whichever is less

#### Note

Test conditions per IEC 60384-14: C0G (NP0): 1.0 V<sub>RMS</sub> at 1 MHz X7R: 1.0 V<sub>RMS</sub> at 1 kHz

#### **FEATURES**

- Approved IEC 60384-14
- · Specialty: safety certified capacitors
- AEC-Q200 qualified available with PPAP
- Wet build process
- Reliable Noble Metal Electrode (NME) system
- Flexible termination "W" for improved bending capability performance available for selected
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## AUTOMOTIV





#### **APPLICATIONS**

- Power supplies
- EMI and AC line filtering
- · EV charging systems
- AC equipment and appliances
- · Lighting strike and voltage surge protection
- Isolators
- Facsimile and telephone

#### Aging Rate:

C0G (NP0): 0 % maximum per decade X7R: 1 % maximum per decade

#### **Voltage Proof Test:**

X1 / Y2: min. 1500 V<sub>AC</sub> X2: min. 1075 V<sub>DC</sub> Peak Impulse Voltage:

#### X1 / Y2: 5000 V

X2: 2500 V

#### **Voltage Rating DC:**

X1 / Y2: 2000 V<sub>DC</sub> X2: 1500 V<sub>DC</sub>

#### Climatic Category According to EN 60068-1:

55/125/21

Vishay Vitramon

QUICK REFERENCE DATA					
DIELECTRIC	CASE	MAXIMUM VOLTAGE	CAPACITANCE		
DIELECTRIC	DIELECTRIC	(V <sub>AC</sub> )	MINIMUM	MAXIMUM	
C0G (NP0) (X1 / Y2)	2008	250	10 pF	220 pF	
COG (NFO) (X17 12)	2220	250	47 pF	1.0 nF	
C0G (NP0) (X2)	2008	250	10 pF	390 pF	
X7R (X1 / Y2)	2008	250	100 pF	1.0 nF	
A/H (A1 / 12)	2220	250	270 pF	4.7 nF	
X7R (X2)	2008	250	100 pF	2.7 nF	
Λ/Π (ΛΖ)	2220	250	270 pF	12 nF	

#### **Notes**

- Detail ratings see "Selection Chart"
- Size 2008 is compatible with 1808 solderlands and full conform with the IEC-60384-14 requirements for creepage distance

ORD	ERING INFO	RMATION						
VJ2008	Υ	102	K	Х	U	s	Т	### (1)
CASE CODE	DIELECTRIC	CAPACITANCE NOMINAL CODE	CAPACITANCE TOLERANCE	TERMINATION	AC VOLTAGE RATING	MARKING	PACKAGING	PROCESS CODE
2008 2220	A = C0G (NP0) Y = X7R	Expressed in picofarads (pF). The first two digits are significant, the third is a multiplier. <b>Examples:</b> 101 = 100 pF 102 = 1000 pF 103 = 10 000 pF	COG (NP0): $J = \pm 5 \%$ $K = \pm 10 \%$ X7R: $K = \pm 10 \%$ $M = \pm 20 \%$	X = Ni barrier 100 % matte tin plate finish W = Ni barrier with flexible layer, 100 % matte tin plate finish	U = 250 V <sub>AC</sub>	S = marked (see Part Marking table below)	plastic tape	X1 = X1 / Y2 X2 = X2 Vishay automotive grade per customer request, add "A": X1A = X1 / Y2 X2A = X2

#### Notes

- Detail ratings see "Selection Chart"
- (1) Process code must be added to control products and requirements

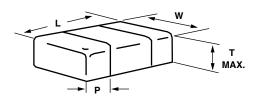
PART MARKING	PART MARKING				
MARKING	1 <sup>ST</sup> DIGIT MANUFACTURER	2 <sup>ND</sup> DIGIT DIELECTRIC AND RATING			
VC		C = C0G (NP0), X1 / Y2 - "X" termination option			
VT		T = C0G (NP0), X2 - "X" termination option			
VD		D = C0G (NP0), X1 / Y2 - "W" termination option			
VU	V. Viahov	U = C0G (NP0), X2 - "W" termination option			
VA	V = Vishay	A = X7R, X1 / Y2 - "X" termination option			
VM		M = X7R, X2 - "X" termination option			
VB		B = X7R, X1 / Y2 - "W" termination option			
VN		N = X7R, X2 - "W" termination option			



www.vishay.com

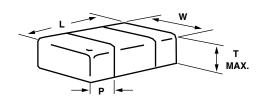
Vishay Vitramon

#### **DIMENSIONS FOR "X" TERMINATION OPTION** in inches (millimeters)



CASE CODE	PART ORDERING NUMBER	LENGTH	WIDTH (W)	MAXIMUM THICKNESS		NATION P)
	NUMBER (L)		(L) (W)		MINIMUM	MAXIMUM
2008	VJ2008	0.200 ± 0.010 (5.08 ± 0.25)	0.080 ± 0.010 (2.03 ± 0.25)	0.086 (2.18)	0.010 (0.25)	0.030 (0.76)
2220	VJ2220	0.220 ± 0.008 (5.59 ± 0.20)	$0.200 \pm 0.010$ (5.08 ± 0.25)	0.086 (2.18)	0.010 (0.25)	0.030 (0.76)

#### **DIMENSIONS FOR "W" TERMINATION OPTION** in inches (millimeters)



CASE CODE	PART ORDERING NUMBER	LENGTH WIDTH		MAXIMUM THICKNESS	TERMII (F	NATION P)
	NOWIBER	(L)	(W)	(T)	MINIMUM	MAXIMUM
2008	VJ2008	0.200 - 0.010 / + 0.020 (5.08 - 0.25 / + 0.50)	0.080 ± 0.010 (2.03 ± 0.25)	0.086 (2.18)	0.010 (0.25)	0.030 (0.76)
2220 C0G (NP0)	VJ2220A	0.220 - 0.008 / + 0.018 (5.59 - 0.20 / + 0.45)	$0.200 \pm 0.010$ (5.08 ± 0.25)	0.086 (2.18)	0.010 (0.25)	0.030 (0.76)
2220 X7R	VJ2220Y	0.220 - 0.008 / + 0.018 (5.59 - 0.20 / + 0.45)	$0.200 \pm 0.010$ (5.08 ± 0.25)	0.105 (2.65)	0.010 (0.25)	0.030 (0.76)

RECOMMENDED S	RECOMMENDED SOLDERING PAD DIMENSIONS in millimeters				
A C					
CASE CODE	Α	В	С	r <sup>(1)</sup>	
2008	2.70	1.50	4.00	0.5	
2220	5.80	1.50	4.20	0.5	

#### Note

(1) Radius optional

Vishay Vitramon

SELECTION C	HART			
DIELECTRIC		COG (NPC	0) (X1 / Y2)	C0G (NP0) (X2)
STYLE		VJ2008 <sup>(1)</sup>	VJ2220 <sup>(1)</sup>	VJ2008 <sup>(1)</sup>
CASE CODE		2008	2220	2008
VOLTAGE (V <sub>AC</sub> )		250	250	250
VOLTAGE CODE		U	U	U
CAP. CODE	CAP.			
100	10 pF	•		•
120	12 pF	•		•
150	15 pF	•		•
180	18 pF	•		•
220	22 pF	•		•
270	27 pF	•		•
330	33 pF	•		•
390	39 pF	•		•
470	47 pF	•	•	•
560	56 pF	•	•	•
680	68 pF	•	•	•
820	82 pF	•	•	•
101	100 pF	•	•	•
121	120 pF	•	•	•
151	150 pF	•	•	•
181	180 pF	•	•	•
221	220 pF	•	•	•
271	270 pF		•	•
331	330 pF		•	•
391	390 pF		•	•
471	470 pF		•	
561	560 pF		•	
681	680 pF		•	
821	820 pF		•	
102	1.0 nF		•	
122	1.2 nF			
152	1.5 nF			
182	1.8 nF			

#### Note

<sup>(1)</sup> See soldering recommendations within this data book, or visit <a href="www.vishay.com/doc?45034">www.vishay.com/doc?45034</a>

Vishay Vitramon

SELECTION CH	IART				
DIELECTRIC			(1 / Y2)	X7R	
STYLE		VJ2008 <sup>(1)</sup>	VJ2220 <sup>(1)</sup>	VJ2008 <sup>(1)</sup>	VJ2220 <sup>(1)</sup>
CASE CODE		2008	2220	2008	2220
VOLTAGE (V <sub>AC</sub> )		250	250	250	250
VOLTAGE CODE		U	U	U	U
CAP. CODE	CAP.				
100	10 pF				
220	22 pF				
330	33 pF				
470	47 pF				
560	56 pF				
680	68 pF				
820	82 pF				
101	100 pF	•		•	
121	120 pF	•		•	
151	150 pF	•		•	
181	180 pF	•		•	
221	220 pF	•		•	
271	270 pF	•	•	•	•
331	330 pF	•	•	•	•
391	390 pF	•	•	•	•
471	470 pF	•	•	•	•
561	560 pF	•	•	•	•
681	680 pF	•	•	•	•
821	820 pF	•	•	•	•
102	1.0 nF	•	•	•	•
122	1.2 nF		•	•	•
152	1.5 nF		•	•	•
182	1.8 nF		•	•	•
222	2.2 nF		•	•	•
272	2.7 nF		•	•	•
332	3.3 nF		•		•
392	3.9 nF		•		•
472	4.7 nF		•		•
562	5.6 nF				•
682	6.8 nF				•
822	8.2 nF				•
103	10 nF				•
123	12 nF				•
153	15 nF				

#### Notes

Values available with "W" termination

<sup>(1)</sup> See soldering recommendations within this data book, or visit <a href="www.vishay.com/doc?45034">www.vishay.com/doc?45034</a>

Vishay Vitramon

PACKAGING QUANTITIES (1)				
		7" REEL QUANTITIES		
CASE CODE	TAPE SIZE	PACKAGING CODE "T"		
2008	12 mm	2000		
2220	12 mm	1000		

#### Note

<sup>(1)</sup> Reference: EIA standard RS481 - "Taping of Surface Mount Components for Automatic Placement"

APPROVALS FOR	COG (NPO)			
VDE approval mark (update	e 2020-02-20):			
X1 / Y2-capacitor:	40036706	10 pF to 1000 pF	250 V <sub>AC</sub>	$\wedge$
X2-capacitor:	40036706	10 pF to 470 pF	250 V <sub>AC</sub>	DVE
DIN EN 60384-14 (VDE 056	65-1-1):2014-04; EN 6038	4-14:2013-08; IEC 60384-14 (ed	.4)	
CAN / cCSAus approval m	ark (update 2020-05-05):			
X1 / Y2-capacitor:	70001064	10 pF to 1000 pF	250 V~	
X2-capacitor:	70001064	10 pF to 470 pF	250 V~	(S P®
CAN / CSA-E60384-14:14	and ANSI / UL 60384-14-2	2017		c Us

APPROVALS FOR X7R					
VDE approval mark (*u	update 2020-02-20), **	update 2021-01-14:			
X1 / Y2-capacitor:	X termination	40037440*	82 pF to 4700 pF	250 V <sub>AC</sub>	
AT / 12-Gapacitor.	W termiation	40052169**	100 pF to 4700 pF		$\wedge$
VO sanasitari	X termination	40037440*	82 pF to 12 000 pF	050.\/	DVE
X2-capacitor:	W termiation	40052169**	100 pF to 12 000 pF	250 V <sub>AC</sub>	
DIN EN 60384-14 (VD	E 0565-1-1):2014-04;	EN 60384-14:2013-0	08; IEC 60384-14 (ed.4)		
CSA / cCSAus approv	al mark (update 2020-	05-05):			
X1 / Y2-capacitor:		70001064	82 pF to 4700 pF	250 V~	
X2-capacitor:		70001064	82 pF to 12 000 pF	250 V~	(SP®
CAN / CSA-E60384-1	4:14 and ANSI / UL 60	384-14-2017			cus

GENERAL CERTIFICATES		
# Quality management system according to ISO/IATF 16949	Yes	
# Quality management system according to ISO 9001	Yes	
# Environmental certification according to ISO 14001	Yes	
# Health and safety system according to ISO 45001	Yes	

#### STORAGE AND HANDLING CONDITIONS

- (1) Store the components at 5 °C to 40 °C ambient temperature and ≤ 70 % relative humidity conditions.
- (2) The product is recommended to be used within a time-frame of 2 years after shipment. Check solderability in case extended shelf life beyond the expiry date is needed.

#### Precautions:

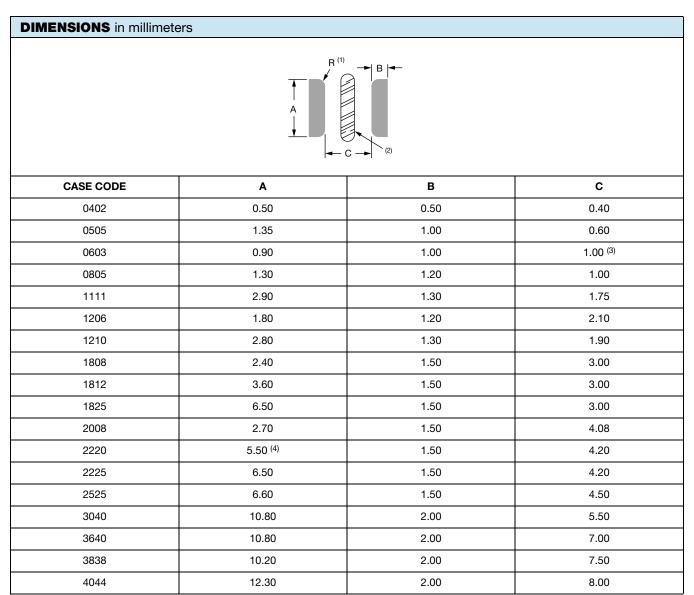
- a. Do not store products in an environment containing corrosive elements, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. This may cause corrosion or oxidization of the terminations, which can easily lead to poor soldering.
- b. Store products on the shelf and avoid exposure to moisture or dust.
- c. Do not expose products to excessive shock, vibration, direct sunlight and so on.



www.vishay.com

Vishay Vitramon

# Solder Pad Dimensions for Vishay Surface-Mount Multilayer Ceramic Chip Capacitors



#### Notes

<sup>(1)</sup> For safety capacitors and voltages above 3000 V, corner rounding (R) of 0.5 mm is recommended to suppress arcing

<sup>(2)</sup> Add a 1 mm slot in PCB between pads to allow cleaning and coating under MLCC

<sup>(3)</sup> For VJ HiFREQ Series, this dimension is 0.6 mm

<sup>(4)</sup> For safety capacitors, the A dimension should be 5.80 mm

# VISHAY.

#### **Guidelines for MLCC Solder Pads and PCBs**

www.vishay.com

Vishay Vitramon

## PRINTED CIRCUIT BOARD PCB DESIGN CONSIDERATIONS FOR HIGH VOLTAGE SURFACE-MOUNT MLCCS

Special assembly process and design considerations should be employed for today's high voltage rating MLCCs. As case sizes remain the same and voltage ratings increase, MLCC manufacturers must design, evaluate, and qualify their capacitors using methods that reduce the occurrence of corona discharge and arcover events. To meet similar capability in high voltage applications, users should employ similar cautionary design and assembly methods.

#### **MLCC PAD LAYOUT**

A capacitor's arcover inception point can degrade due to factors such as the MLCC termination, PCB pad design, PCB cleanliness, solder flux residue, surface contamination / deposits and environmental conditions. PCB pads and their design affect the air gap distance between the opposing polarities of the MLCC termination. For voltage rating greater than 1500  $V_{DC}$  add a corner radius to the inward facing edge of the MLCC pads and as large a gap as possible between the pads. Too small of a pad gap distance will reduce the capacitor's own arcover inception voltage level. Refer to the Figure and Table Figure 1.0, MLCC Pad Layout and Table 1.0, Vishay MLCC Solder Pad Dimensions for the recommended MLCC solder pad dimensions.

#### **SLOT OR TRENCH BETWEEN PADS**

PCB assembly can deposit dust, trap solder balls, or flux residue underneath the capacitors. These contaminants will reduce conductive clearances and the arcover inception level. Assembly methods must include a final PCB cleaning process. A slot or trench can be cut into the PCB in between the pads to allow cleaners to penetrate underneath the MLCC. The slot will also allow conformal or epoxy coatings to flow underneath the MLCC and build an insulative barrier between pads. Refer to Figure 1.0 MLCC Pad Layout for slot reference location.

#### **COATING PRINTED CIRCUIT BOARD**

Coating a printed circuit board with materials such as acrylic, silicone and urethane resins provide a protective dielectric barrier that is non-conductive and will enhance the resistance to arcing. Various processes exist which include dipping, brushing, and spaying. Optimal performance will come from coating the MLCC on all sides, top and bottom. The PCB slot in between the pads should extend slightly beyond the width of the MLCC. Refer to Figure 1.0 MLCC Pad Layout for slot reference location.



### **Legal Disclaimer Notice**

Vishay

#### **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.