

SANYO Semiconductors DATA SHEET

2SC4520 — NPN Epitaxial Planar Silicon Transistor High-Speed Switching Applications

Features

- · Adoption of FBET, MBIT processes.
- Large current capacity.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.
- · Small-sized package.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		60	V
Collector-to-Emitter Voltage	VCEO		45	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		1.5	А
Collector Current (Pulse)	ICP		3	А
Collector Dissipation	PC	Mounted on a ceramic board (250mm ² ×0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Unit
Collector Cutoff Current	ІСВО	V _{CB} =45V, I _E =0A			1	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =3V, I _C =0A			1	μΑ

Marking: CK Continued on next page.

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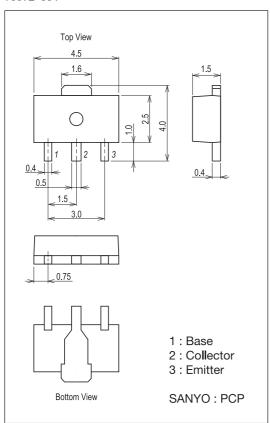
Parameter	Symbol Conditions	Conditions	Ratings			Unit
		min	typ	max	Onit	
DC Current Gain	hFE1	V _{CE} =2V, I _C =100mA	100*		400*	
	hFE2	V _{CE} =2V, I _C =1.5A	40			
Gain-Bandwidth Product	fT	V _{CE} =2V, I _C =100mA		300		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		13		pF
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C =800mA, I _B =40mA		0.25	0.7	V
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	IC=800mA, IB=40mA		0.9	1.3	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0A	60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	45			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0A	5			V
Turn-ON Time	ton	See specified Test Circuit.		50	100	ns
Storage Time	tstg	See specified Test Circuit.		150	270	ns
Turn-OFF Time	t _{off}	See specified Test Circuit.		180	350	ns

^{*:} The 2SC4520 is classified by 100mA hFE as follows:

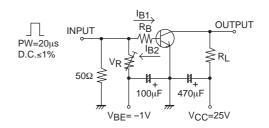
Rank	R	S	T	
hFE	100 to 200	140 to 280	200 to 400	

Package Dimensions

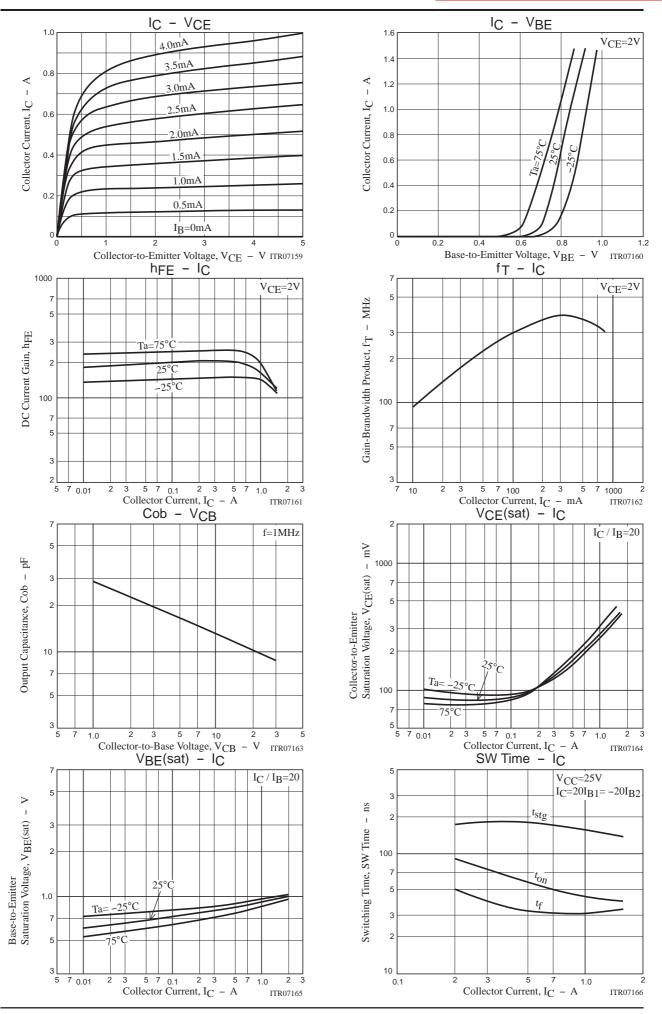
unit : mm (typ) 7007B-004

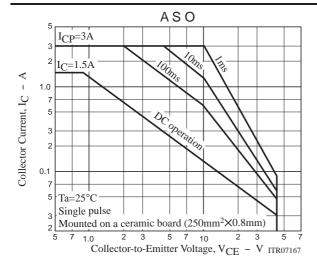


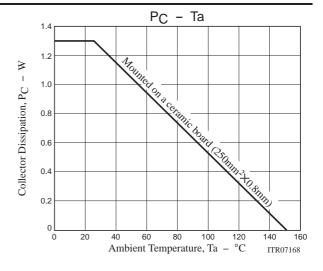
Switching Time Test Circuit



 $I_{C}=20I_{B1}=-20I_{B2}=800mA$







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