

RJK1211DNS

Silicon N Channel Power MOS FET Power Switching

R07DS0090EJ0300 Rev.3.00 Feb 01, 2012

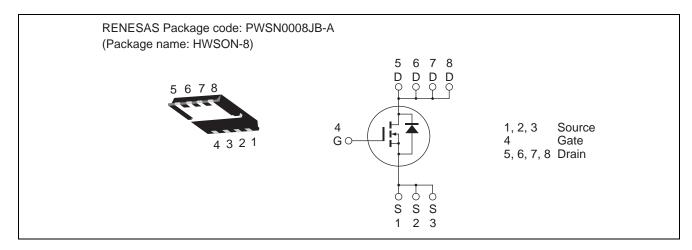
Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)} \! = 100 \; m\Omega$ typ. (at $V_{GS} \! = 10 \; V)$

- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	120	V
Gate to source voltage	V _{GSS}	+12, -5	V
Drain current	I _D	5	A
Drain peak current	I _{D(pulse)} Note1	15	A
Body-drain diode reverse drain current	I _{DR}	5	А
Avalanche current	I _{AP} Note 2	3	A
Avalanche energy	E _{AR} Note 2	0.77	mJ
Channel dissipation	Pch Note3	10	W
Channel to case thermal impedance	θch-c Note3	12.5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. Tc = 25°C

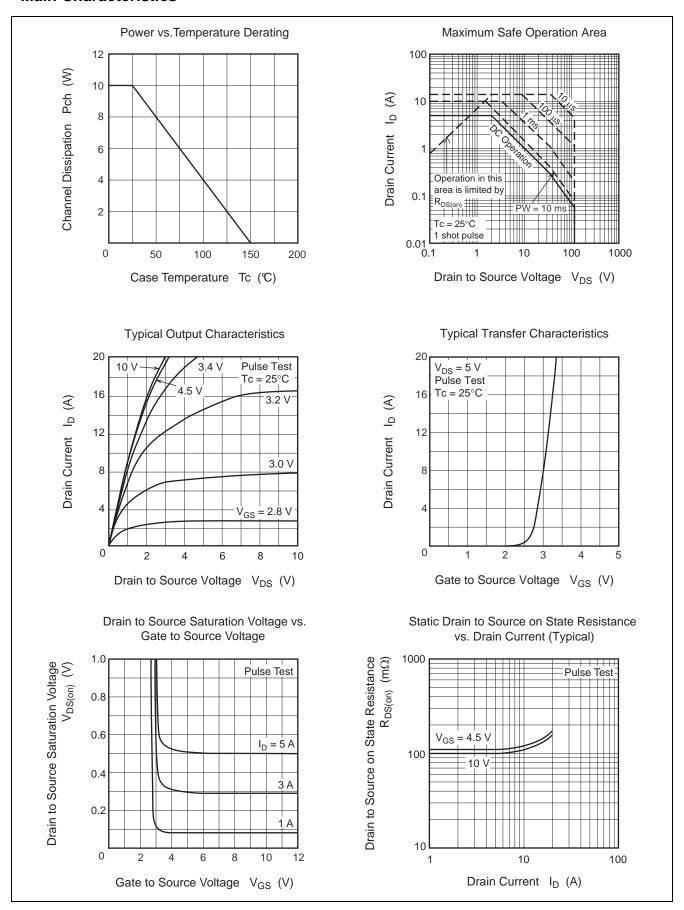
Electrical Characteristics

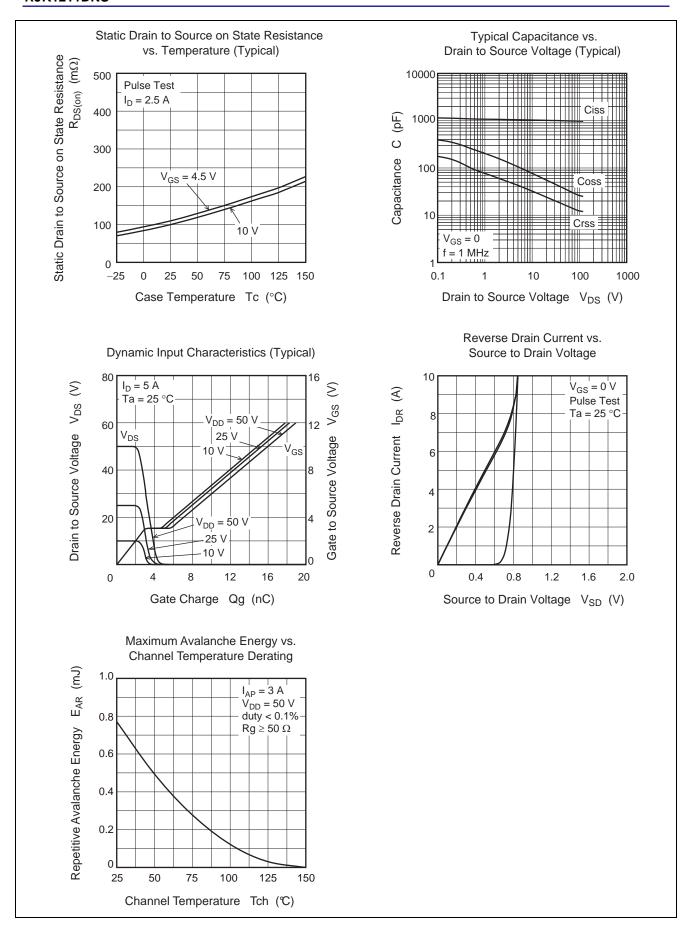
 $(Ta = 25^{\circ}C)$

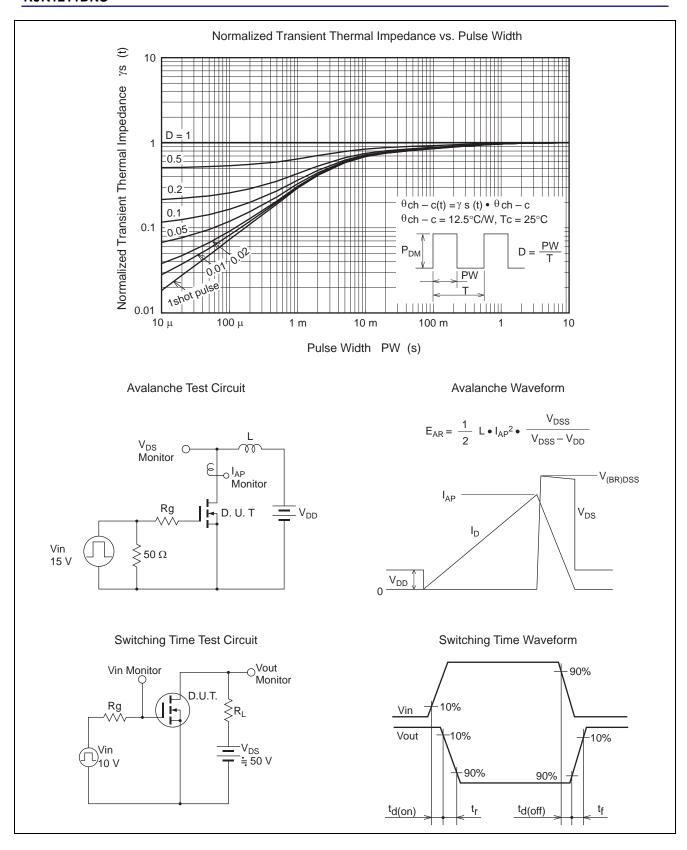
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	120	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	1	_	± 0.1	μΑ	$V_{GS} = +12, -5 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	1	_	10	μΑ	$V_{DS} = 120 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	100	130	mΩ	$I_D = 2.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	110	150	mΩ	$I_D = 2.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}		9.0	_	S	$I_D = 2.5 \text{ A}, V_{DS} = 5 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	1070	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	80	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss		35	_	pF	
Gate Resistance	Rg		1.7	_	Ω	
Total gate charge	Qg		8.0	_	nC	V _{DD} = 50 V V _{GS} = 4.5 V I _D = 5 A
Gate to source charge	Qgs	_	3.0	_	nC	
Gate to drain charge	Qgd		2.0	_	nC	
Turn-on delay time	t _{d(on)}		7.8	_	ns	$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$
Rise time	t _r		2.8	_	ns	$V_{DD} \cong 30 \text{ V}$
Turn-off delay time	$t_{d(off)}$		38	_	ns	$R_L = 12 \Omega$
Fall time	t _f		2.7	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}		0.83	1.1	V	$I_F = 5 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body–drain diode reverse recovery time	t _{rr}	_	40	_	ns	$I_F = 5 \text{ A}, V_{GS} = 0$ $di_F / dt = 100 \text{ A} / \mu \text{s}$

Notes: 4. Pulse test

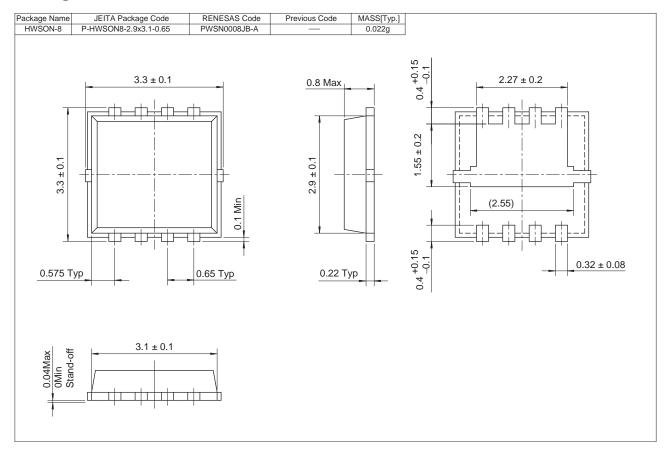
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK1211DNS-00-J5	5000 pcs	Taping

Note: The symbol of 2nd "-" is occasionally presented as "#".

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +444-1628-585-100, Fax: +444-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-2353-1155, Fax: +86-10-8235-7679

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 161F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852-2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiv Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632 Tel: +65-6213-0200, Fax: +65-6278-8001

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 11F., Samik Lavied' or Bidg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: 482-2-558-3737, Fax: 482-2-558-5141

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