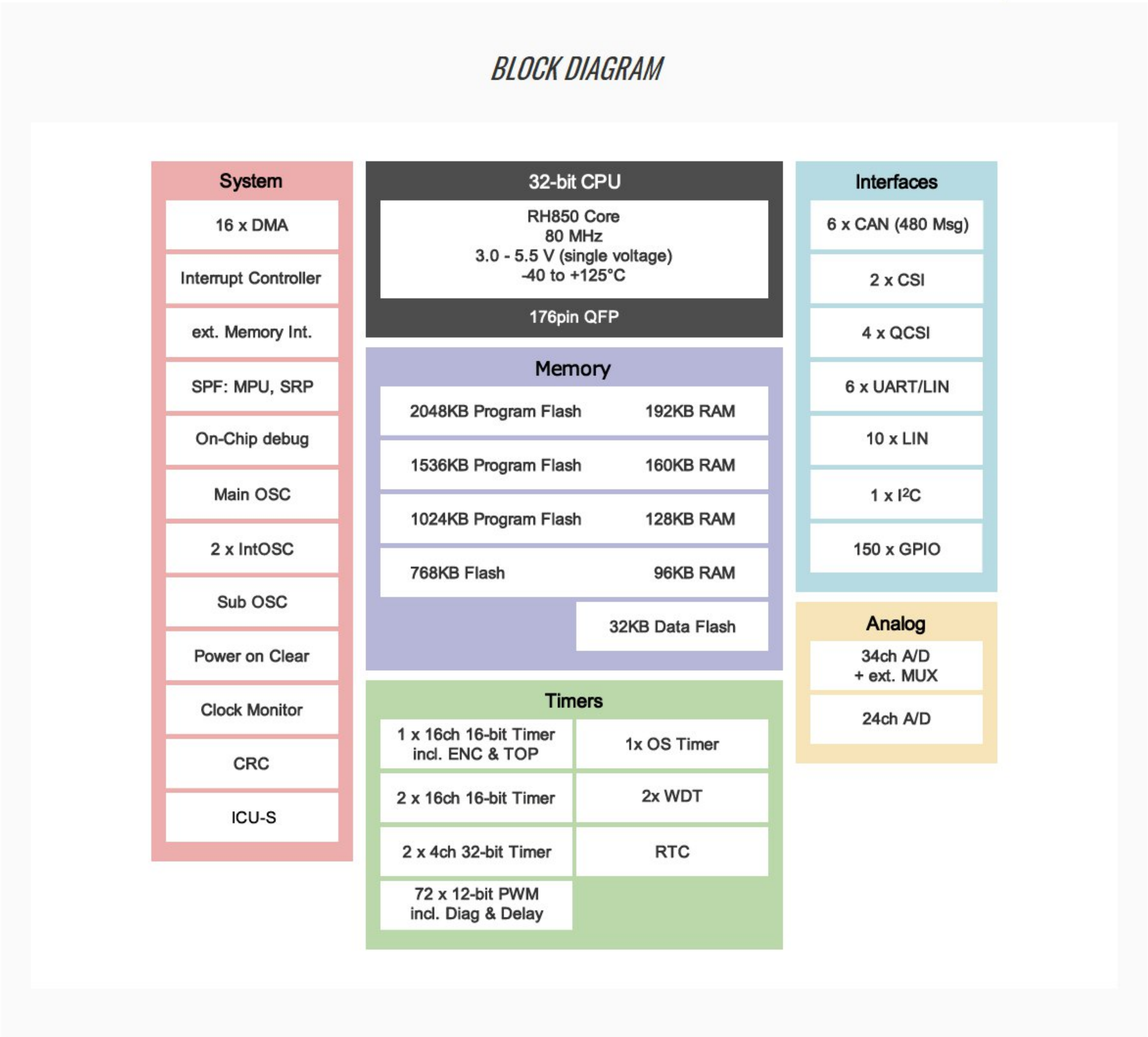


R7F7010123AFP



PARAMETRICS

Parameters	R7F7010123AFP
Package	
Pin Count	64
Package Type	LFQFP
Memory	
Program memory	384 KB Flash memory
RAM	48 KB
Data Flash	32 KB
CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RH850G3K (32-bit)
Max. Frequency	80 MHz
On-chip Oscillator Freq.	8 MHz, Low Speed: 240 kHz
PLL	YES
Power-On Reset	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 16ch
External Interrupt Pins	8
I/O Ports	49
Timers	
16-bit Timers	16 ch
32-bit Timers	4 ch
Watchdog Timers	2 ch
Other Timers	Timer for OS x 1ch
PWM Output	24
Analog	
A/D Converters	10-bit x 11 ch, 12-bit x 10 ch
Interfaces	
CAN	3 ch
CSIs	1 ch
SPIs	1 ch
UARTs	2 ch
I ² Cs	1 ch
LIN	4 ch
Serial Interface Remarks	UART: RLIN3, LIN: RLIN3+RLIN2
Operating Conditions	
Operating Voltage	to 5.5 V
Operating Temperature	-40 to 105 °C (Ambient Temperature)
Remarks	
Remarks	64-pin 32-bit microcontroller with low power RH850G3K core suitable for car body control (R850/F1L)

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