



SANYO Semiconductors

DATA SHEET

N-Channel Silicon MOSFET

SCH2401 — General-Purpose Switching Device Applications

Features

- The SCH2401 incorporates two elements in the same package which are N-channel MOSFETs, thereby enabling high-density mounting.
- Low ON-resistance.
- High-speed switching.
- 2.5V drive.
- High ESD voltage (TYP 300V) [Built-in one side diode for protection between Gate-to-Source].

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage (*1)	V _{GSS}		10	V
Drain Current (DC)	I _D		0.7	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	2.8	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (900mm ² ×0.8mm) 1unit	0.65	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

(*1) : Since the diode between Gate-to-Source for gate prevention serves as one side direction, this product should be careful in circuitry.

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR) _{DSS}	I _D =1mA, V _{GS} =0V	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =8V, V _{DS} =0V			1	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =100μA	0.4		1.3	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =350mA	0.48	0.8		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =350mA, V _{GS} =4V		0.7	0.9	Ω
	R _{DS(on)2}	I _D =200mA, V _{GS} =2.5V		0.8	1.15	Ω
	R _{DS(on)3}	I _D =10mA, V _{GS} =1.5V		1.6	2.4	Ω
Input Capacitance	C _{iss}	V _{DS} =10V, f=1MHz		30		pF
Output Capacitance	C _{oss}	V _{DS} =10V, f=1MHz		7		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =10V, f=1MHz		3.5		pF

Marking : LA

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SANYO Semiconductor Co., Ltd.

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

SCH2401

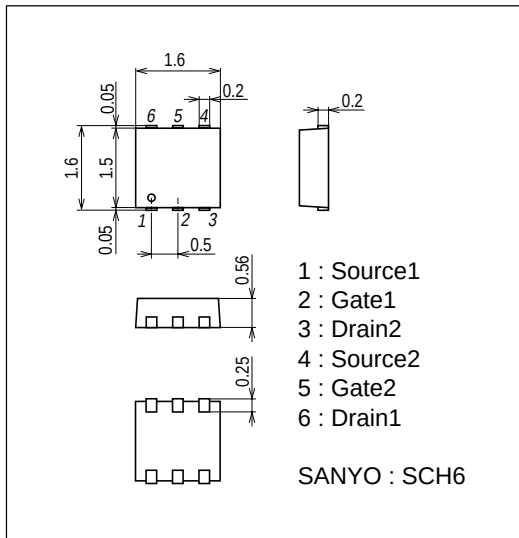
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		8		ns
Rise Time	t_r	See specified Test Circuit.		6		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		10		ns
Fall Time	t_f	See specified Test Circuit.		8		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=10V, I_D=700mA$		1		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS}=10V, V_{GS}=10V, I_D=700mA$		0.4		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS}=10V, V_{GS}=10V, I_D=700mA$		0.2		nC
Diode Forward Voltage	V_{SD}	$I_S=700mA, V_{GS}=0V$		0.93	1.2	V

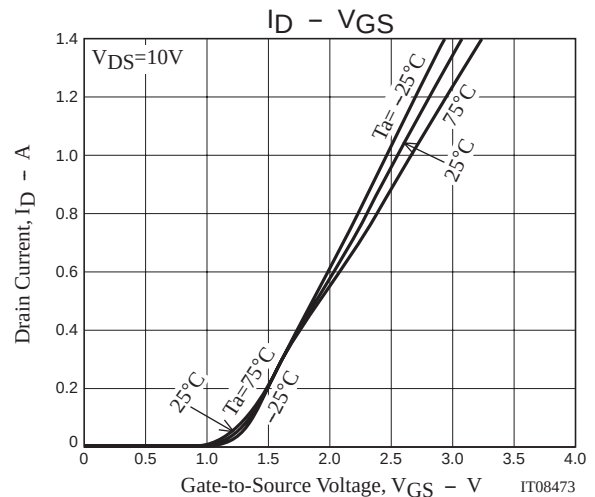
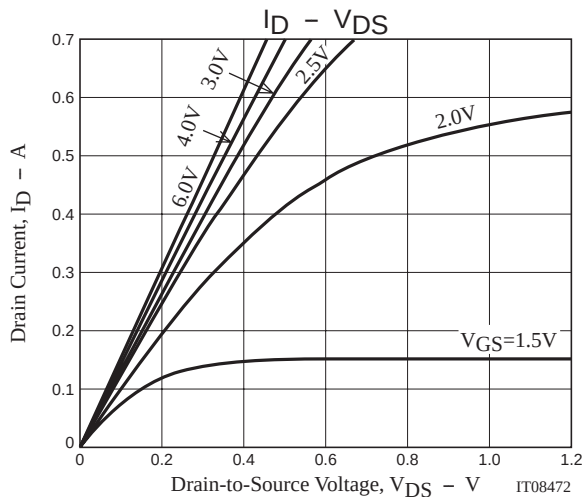
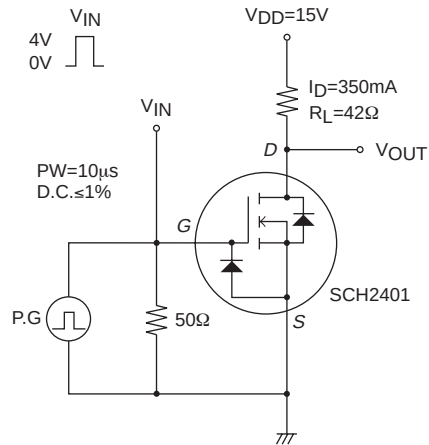
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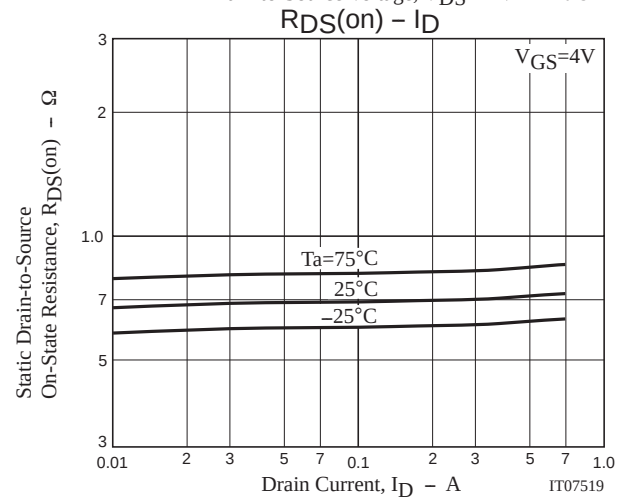
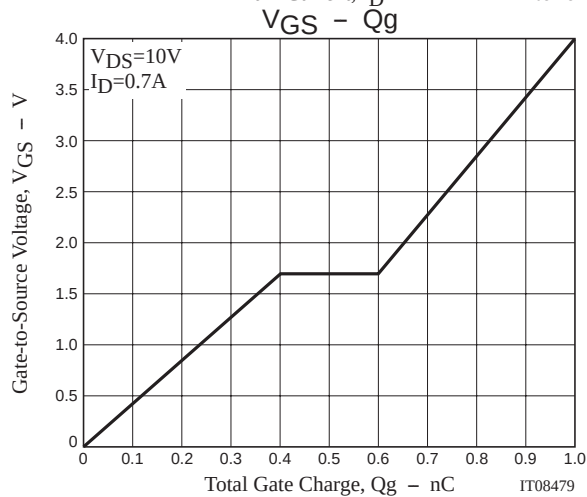
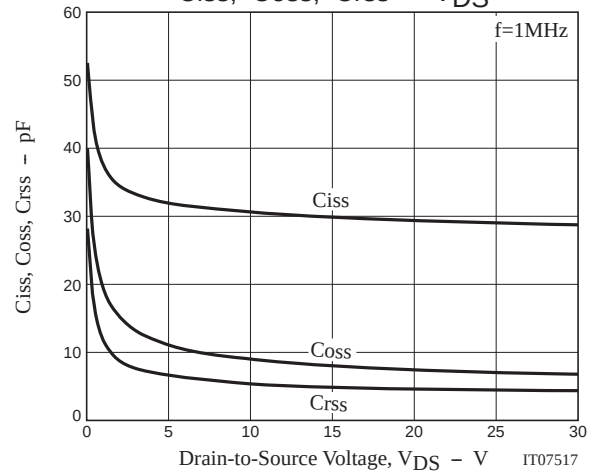
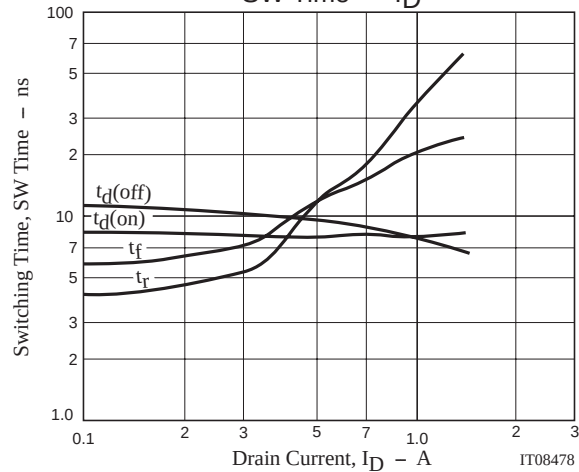
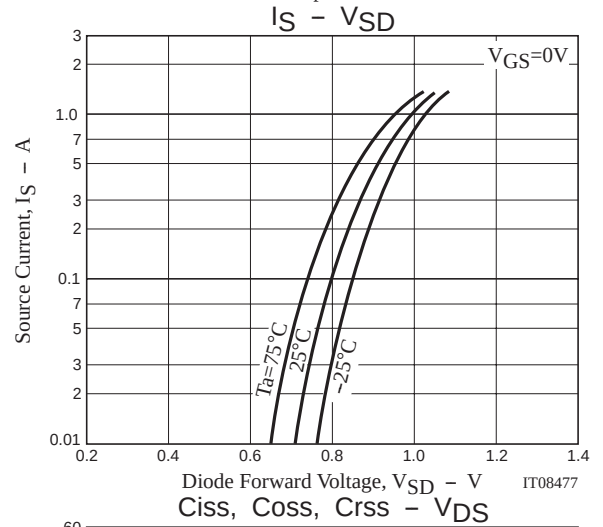
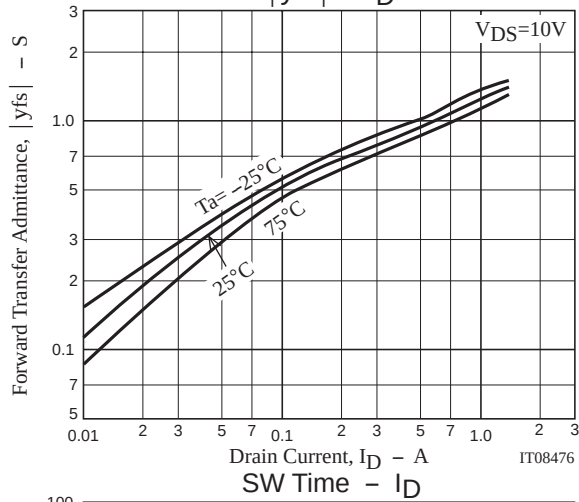
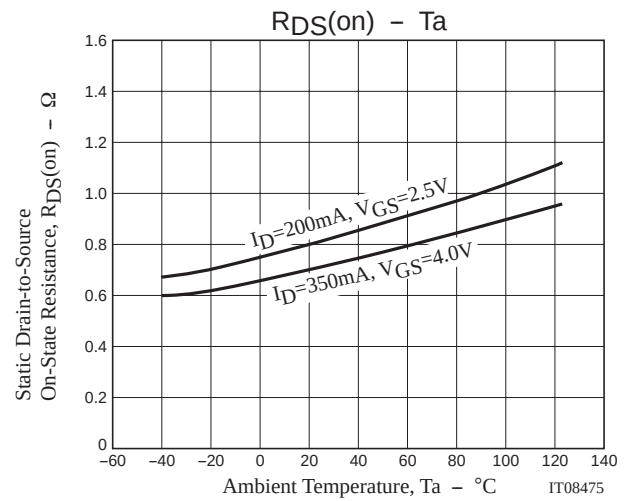
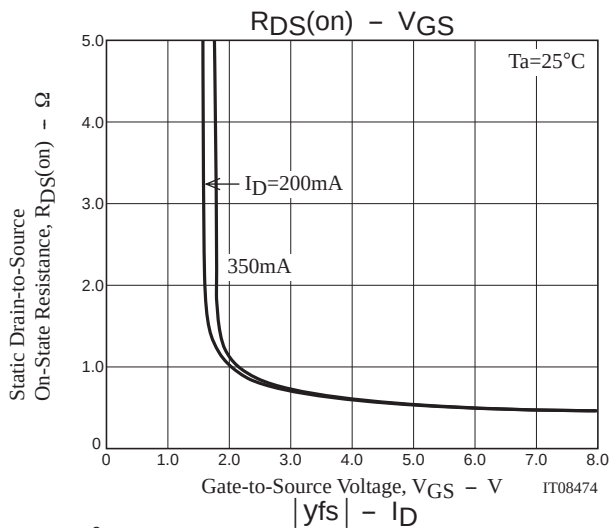
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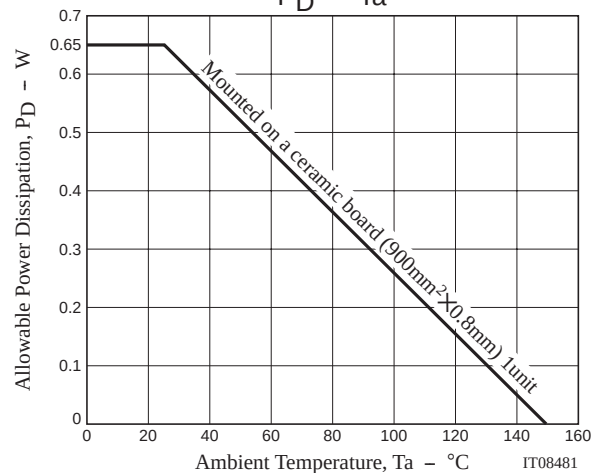
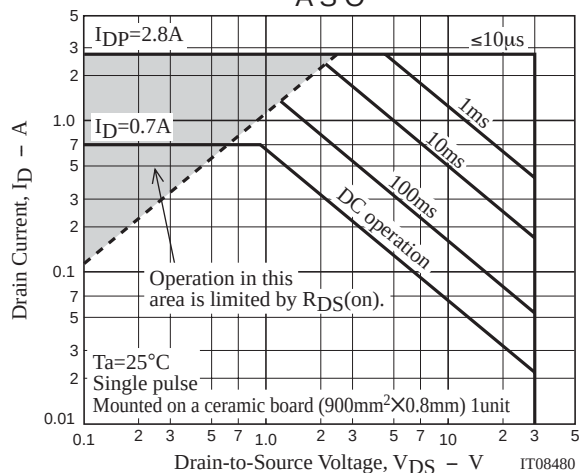
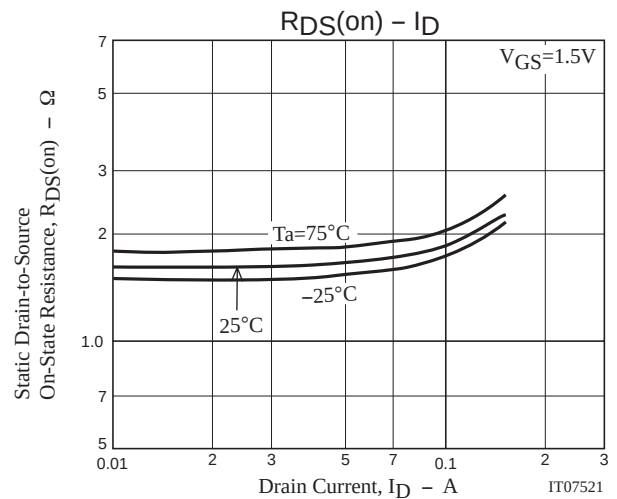
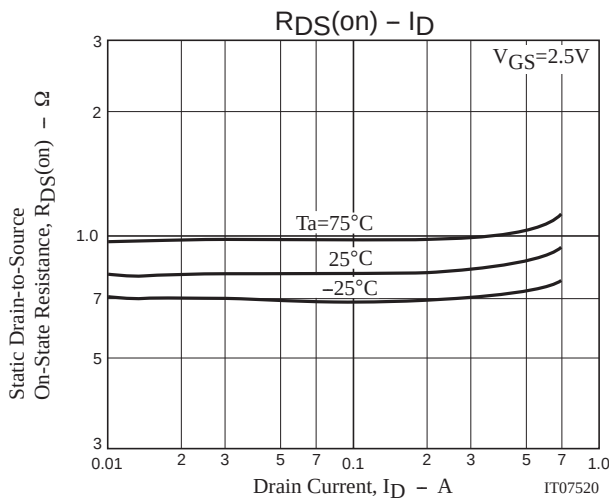
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Switching Time Test Circuit







Note on usage : Since the SCH2401 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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