

PART NUMBER 5447BEA-ROCV

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

 Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

INCH-POUND

MIL-M-38510/10D 16 February 2005 SUPERSEDING MIL-M-38510/10C 3 March 1986

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, BIPOLAR, TTL, DECODERS MONOLITHIC SILICON

Inactive for new design after 7 September 1995.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product herein shall consist of this specification sheet and MIL-PRF 38535

- 1. SCOPE
- 1.1 <u>Scope.</u> This specification covers the detail requirements for monolithic silicon, TTL, microcircuit decoders. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number. For this product, the requirements of MIL-M-38510 have been superseded by MIL-PRF-38535, (see 6.4).
 - 1.2 Part or Identifying Number (PIN). The PIN is in accordance with MIL-PRF-38535.
 - 1.2.1 Device types. The device types are as follows:

Device type	<u>Circuit</u>
01	BCD-to-decimal decoder
02	Excess-3-to-decimal decoder
03	Excess-3-gray-to-decimal decoder
04	BCD-to-decimal decoder/driver (30 volt, open collector output)
05	BCD-to-decimal decoder/driver (15 volt, open collector output)
06	BCD-to-seven segment decoder/driver (30 volt, open collector output)
07	BCD-to-seven segment decoder/driver (15 volt, open collector output)
08	BCD-to-seven segment decoder/driver
09	BCD-to-seven segment decoder/driver (5.5 volt, open collector output)

- 1.2.2 Device class. The device class is the product assurance level as defined in MIL-PRF-38535.
- 1.2.3 <u>Case outlines</u>. The case outlines are as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
Α	GDFP5-F14 or CDFP6-F14	14	Flat pack
В	GDFP4-F14	14	Flat pack
С	GDIP1-T14 or CDIP2-T14	14	Dual-in-line
D	GDFP1-F14 or CDFP2-F14	14	Flat pack
E	GDIP1-T16 or CDIP2-T16	16	Dual-in-line
F	GDFP2-F16 or CDFP3-F16	16	Flat pack

Comments, suggestions, or questions on this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAS, P. O. Box 3990, Columbus, OH 43218-3990, or emailed to bipolar@dscc.dla.mil. Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at http://assist.daps.dla.mil.

AMSC N/A FSC 5962

1.3 Absolute maximum ratings.

In _l St	upply voltage range	-1.5 V dc at -12 mA to +5.5 V dc
IVI	aximum power dissipation (P _D): 1/	226 mW
	Device types 01, 02 and 03	
	Device types 04 and 05	
TL	Lead temperature (soldering, 10 seconds)	+300°C
11	nermal resistance, junction to case (θ_{JC}):	(Coo MIL CTD 1925)
1	Cases A, B, C, D, E and F	
	Inction temperature (T _J) <u>2</u> /	175°C
IVI	aximum current into any output (output off):	4 4
	Device types 04, 05, 06, 07 and 09	1 mA
1.4 <u>R</u>	ecommended operating conditions.	
Su	upply voltage (V _{CC})	4.5 V dc minimum to 5.5 V dc maximum
Mi	inimum high level input voltage (V _{IH})	2.0 V dc
Ma	aximum low level input voltage (V _{IL})	0.8 V dc
	nk current capability by device type:	
	Device types 01, 02, 03	16 mA
	Device types 04, 05	
	Device types 06, 07	

2. APPLICABLE DOCUMENTS

2.1 <u>General.</u> The documents listed in this section are specified in sections 3, 4, or 5 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements of documents cited in sections 3, 4, or 5 of this specification, whether or not they are listed.

 Outputs A - G
 6.4 mA

 BI/RBO node
 8 mA

 Device type 09
 10 mA

 Case operating temperature range (Tc)
 -55° to +125°C

 $[\]underline{1}$ / Must withstand the added P_D due to short-circuit test (e.g., I_{OS}).

^{2/} Maximum junction temperature should not be exceeded except in accordance with allowable short duration burn-in screening condition in accordance with MIL-PRF-38535.

2.2 Government documents.

2.2.1 <u>Specifications and standards.</u> The following specifications and standards form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATIONS

MIL-PRF-38535 - Integrated Circuits (Microcircuits) Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard for Microelectronics.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines

(Copies of these documents are available online at http://assist.daps.dla.mil/quicksearch/ or http://assist.daps.dla.mil or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.3 <u>Order of precedence.</u> In the event of a conflict between the text of this specification and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

- 3.1 <u>Qualification</u>. Microcircuits furnished under this specification shall be products that are manufactured by a manufacturer authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.3 and 6.3).
- 3.2 <u>Item requirements</u>. The individual item requirements shall be in accordance with MIL-PRF-38535 and as specified herein or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not affect the form, fit, or function as described herein. This slash sheet has been modified to allow the manufacturer to use the alternate die/fabrication requirements of paragraph A.3.2.2 of MIL-PRF-38535 or other alternative approved by the qualifying activity.
- 3.3 <u>Design, construction, and physical dimensions.</u> The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535 and herein.
 - 3.3.1 Case outlines. The case outlines shall be as specified in 1.2.3.
- 3.3.2 <u>Logic diagrams and terminal connections.</u> The logic diagrams and terminal connections shall be as specified on figures 1 and 2.
 - 3.3.3 <u>Truth tables.</u> The truth tables shall be as specified on figure 3.
- 3.3.4 <u>Schematic circuits</u>. The schematic circuits shall be maintained by the manufacturer and made available to the qualifying activity and the preparing activity upon request.
 - 3.4 <u>Lead material and finish</u>. The lead material and finish shall be in accordance with MIL-PRF-38535 (see 6.6).
- 3.5 <u>Electrical performance characteristics</u>. The electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range, unless otherwise specified.
- 3.6 <u>Electrical test requirements</u>. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

TABLE I. Electrical performance characteristics, device types 01, 02 and 03.

Test	Symbol	Conditions	Lin	nits	Unit
		-55°C ≤ T _C ≤ $+125$ °C	Min	Max	
		unless otherwise specified			
High level output voltage	V _{OH}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.8 \text{ mA}$	2.4		V
Low level output voltage	V _{OL}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 16 \text{ mA},$		0.4	V
		V _{IN} = 0.8 V and 2.0 V			
Input clamp voltage	V _{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$		-1.5	V
Low level input current	I _{IL}	V _{CC} = 5.5 V, V _{IN} = 0.4 V <u>1</u> /	-0.7	-1.6	mA
High level input current	I _{IH1}	V _{CC} = 5.5 V, V _{IN} = 2.4 V <u>2</u> /		40	μА
	I _{IH2}	V _{CC} = 5.5 V, V _{IN} = 5.5 V <u>2</u> /		100	μА
Short circuit output current	Ios	Vcc = 5.5 V <u>3</u> /	-20	-55	mA
Supply current	I _{CC}	V _{CC} = 5.5 V, V _{IN} = 0 V		41	mA
Propagation delay time through	t _{PHL}	C _L = 50 pF minimum,	5	39	ns
two logic levels Propagation delay time through	t _{PLH}	$R_L = 390 \Omega \pm 5\%$ (Figure 4)	5	39	ns
two logic levels					
Propagation delay time through three logic levels	t _{PHL}		5	46	ns
Propagation delay time through three logic levels	t _{PLH}		5	46	ns

 ^{1/} All unspecified inputs at 5.5 volts.
 2/ All unspecified inputs grounded.
 3/ Not more than one output should be shorted at one time.

TABLE I. Electrical performance characteristics, device types 04 and 05.

Test	Symbol	Conditions	Lin	nits	Unit
		$-55^{\circ}C \le T_C \le +125^{\circ}C$ unless otherwise specified	Min	Max	
Low level output voltage	V _{OL1}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 80 \text{ mA}$		0.9	٧
Low level output voltage	V _{OL2}	V_{CC} = 4.5 V, I_{OL} = 20 mA		0.4	V
Input clamp voltage	V _{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$		-1.5	V
Maximum collector cut-off current	I _{CEX}	V _{CC} = 4.5 V, V _{OH} = max <u>1</u> /		250	μΑ
Low level input current	I _{IL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V} \underline{2}/$	-0.7	-1.6	mA
High level input current	I _{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V} \underline{3}/$		40	μΑ
	I _{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V} \underline{3}/$		100	μΑ
Supply current	I _{CC}	V _{CC} = 5.5 V, V _{IN} = 0 V		62	mA
Propagation delay time to a high logic level	t _{PLH}	C_L = 50 pF minimum, R_L = 390 Ω ±5%	5	73	ns
Propagation delay time to a low logic level	t _{PHL}	(Figure 5)	5	73	ns

 $[\]begin{array}{l} \underline{1}/ \ \ \, \text{Device type 04 maximum V}_{\text{OH}} = 30 \text{ V}. \\ \ \ \, \text{Device type 05 maximum V}_{\text{OH}} = 15 \text{ V}. \\ \underline{2}/ \ \ \, \text{All unspecified inputs at 5.5 volts}. \\ \underline{3}/ \ \ \, \text{All unspecified inputs grounded}. \end{array}$

TABLE I. Electrical performance characteristics, device types 06 and 07.

Test	Symbol	Conditions	Lin	nits	Unit
		-55°C ≤ T _C ≤ +125°C	Min	Max	
		unless otherwise specified			
Low level output voltage 1/	V _{OL1}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 40 \text{ mA}$		0.4	V
Low level output voltage 2/	V _{OL2}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 8 \text{ mA}$		0.4	V
Input clamp voltage	Vic	V _{CC} = 4.5 V, I _{IN} = -12 mA		-1.5	V
High level output voltage 2/	V _{OH}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.2 \text{ mA}$	2.4		V
Maximum collector cut-off current 3/	I _{CEX}	$V_{CC} = 4.5 \text{ V}, V_{OH} = \text{max } 3/$		250	μΑ
Low level input current 4/	I _{IL1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V} \underline{5}/$	-0.4	-1.6	mA
Low level input current 2/	I _{IL2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V} \underline{5}/$	-1.7	-4.2	mA
High level input current 4/	I _{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V} \underline{6}/$		40	μΑ
	I _{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V} \underline{6}/$		100	μА
Short circuit output current 1/	I _{OS}	Vcc = 5.5 V <u>6</u> /		-4	mA
Supply current	I _{CC}	V _{CC} = 5.5 V, V _{IN} = 5.5 V		85	mA
Propagation delay time from any	t _{PLH}	C _L = 50 pF minimum,	8	144	ns
input except RBI to any output		$R_L = 120 \Omega \pm 5\% \frac{1}{4}$			
Propagation delay time from	t _{PLH}	$R_L = 560 \Omega \pm 5\% \frac{2}{2}$	8	144	ns
RBI to any output		(Figure 6)			
Propagation delay time from any	t _{PHL}		8	144	ns
input except RBI to any output					
Propagation delay time from	t _{PHL}		8	144	ns
RBI to any output					

 ^{1/} Outputs A through G only.
 2/ BI/RBO node only.
 3/ Device type 06 maximum V_{OH} = 30 V. Device type 07 maximum V_{OH} = 15 V.
 4/ Any input except BI/RBO node.
 5/ All unspecified inputs at 5.5 volts.
 6/ All unspecified inputs grounded.

TABLE I. Electrical performance characteristics, device types 08.

Test	Symbol	Conditions	Lin	Unit	
		$-55^{\circ}C \le T_C \le +125^{\circ}C$ unless otherwise specified	Min	Max	
High level output voltage 1/	V _{OH1}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.4 \text{ mA}$	2.4		V
High level output voltage 2/	V _{OH2}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.2 \text{ mA}$	2.4		V
Low level output voltage 1/	V _{OL1}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 6.4 \text{ mA}$		0.4	V
Low level output voltage 2/	V _{OL2}	V _{CC} = 4.5 V, I _{OL} = 8 mA		0.4	V
Input clamp voltage	Vic	V _{CC} = 4.5 V, I _{IN} = -12 mA		-1.5	V
Low level input current 3/	I _{IL1}	V _{CC} = 5.5 V, V _{IN} = 0.4 V <u>4</u> /	-0.4	-1.6	mA
Low level input current 3/	I _{IL2}	V _{CC} = 5.5 V, V _{IN} = 0.4 V <u>4</u> /	-1.7	-4.2	mA
High level input current 3/	I _{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V} \underline{5}/$		40	μΑ
	I _{IH2}	V _{CC} = 5.5 V, V _{IN} = 5.5 V <u>5</u> /		100	μА
Short circuit output current	I _{OS}	V _{CC} = 5.5 V		-4	mA
Supply current	Icc	V _{CC} = 5.5 V, V _{IN} = 0 V		76	mA
Propagation delay time from any input except RBI to any output	t _{PLH}	$C_L = 50 \text{ pF minimum},$ $R_L = 750 \Omega \pm 5\% \frac{1}{2}$	8	144	ns
Propagation delay time from RBI to any output	t _{PLH}	$R_L = 560 \Omega \pm 5\% \frac{2}{4}$ (Figure 7)	8	144	ns
Propagation delay time from any input except RBI to any output	t _{PHL}	, ,	8	144	ns
Propagation delay time from RBI to any output	t _{PHL}		8	144	ns

^{1/} Outputs A through G only.
2/ Bl/RBO node only.
3/ Any input except Bl/RBO node.
4/ All unspecified inputs at 5.5 volts.
5/ All unspecified inputs grounded.

TABLE I. Electrical performance characteristics, device types 09.

Test	Symbol	Conditions	Lin	nits	Unit
		-55°C ≤ T _C ≤ +125°C	Min	Max	
		unless otherwise specified			
Low level output voltage	V _{OL}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 10 \text{ mA}$		0.4	V
Input clamp voltage	V _{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$		-1.5	V
Maximum collector cut-off current	I _{CEX}	V _{CC} = 4.5 V, V _{OH} = 5.5 V		250	μΑ
Low level input current	I _{IL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V} \text{ 1/}$	-0.4	-1.6	mA
High level input current	I _{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V} \underline{2}/$		40	μΑ
	I _{IH2}	V _{CC} = 5.5 V, V _{IN} = 5.5 V <u>2</u> /		100	μΑ
Supply current	I _{CC}	V _{CC} = 5.5 V, V _{IN} = 0 V		47	mA
Propagation delay time from any t _{PL} input to any output		C_L = 50 pF minimum, R_L = 470 Ω ±5%	8	144	ns
Propagation delay time from any input to any output	t _{PHL}	(Figure 8)	8	144	ns

^{1/} All unspecified inputs at 5.5 volts. 2/ All unspecified inputs grounded.

TABLE II. Electrical test requirements.

	Subgroups (see table III)				
MIL-PRF-38535	Class S	Class B			
test requirements	devices	devices			
Interim electrical parameters	1	1			
Final electrical test parameters	1*, 2, 3, 7 9, 10, 11	1*, 2, 3, 7, 9			
Group A test requirements	1, 2, 3, 7, 8 9, 10, 11	1, 2, 3, 7, 8, 9, 10, 11			
Group B electrical test parameters when using the method 5005 QCI option	1, 2, 3, 9, 10, 11	N/A			
Group C end-point electrical parameters	1, 2, 3, 9, 10, 11	1, 2, 3			
Group D end-point electrical parameters	1, 2, 3	1, 2, 3			

^{*}PDA applies to subgroup 1.

- 3.7 Marking. Marking shall be in accordance with MIL-PRF-38535.
- 3.7.1 <u>Certification/compliance mark.</u> The certification mark for device classes Q and V shall be a "QML" or "Q" as required in MIL-PRF-38535. For class Q product built in accordance with A.3.2.2 of MIL-PRF-38535 or other alternative approved by the qualifying activity, the "QD" certification mark shall be used in place of the "QML" or "Q" certification mark.
- 3.8 <u>Microcircuit group assignment.</u> The devices covered by this specification shall be in microcircuit group number 4 (see MIL-PRF-38535, appendix A).

4. VERIFICATION

- 4.1 <u>Sampling and inspection.</u> Sampling and inspection procedures shall be in accordance with MIL-PRF-38535 or as modified in the device manufacturer's Quality Management (QM) plan. The modification in the QM plan shall not effect the form, fit, or function as described herein.
- 4.2 <u>Screening.</u> Screening shall be in accordance with MIL-PRF-38535 and shall be conducted on all devices prior to qualification and conformance inspection. The following additional criteria shall apply:
 - a. The burn-in test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
 - c. Additional screening for space level product shall be as specified in MIL-PRF-38535.

- 4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-38535.
- 4.4 <u>Technology Conformance Inspection (TCI)</u>. Technology conformance inspection shall be in accordance with MIL-PRF-38535 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).
- 4.4.1 <u>Group A inspection.</u> Group A inspection shall be in accordance with table III of MIL-PRF-38535 and as follows:
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, and 6 shall be omitted.
 - 4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of MIL-PRF-38535.
- 4.4.3 Group C inspection. Group C inspection shall be in accordance with table IV of MIL-PRF-38535 and as follows:
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. The steady-state life test duration, test condition, and test temperature, or approved alternatives shall be as specified in the device manufacturer's QM plan in accordance with MIL-PRF-38535. The burn-in test circuit shall be maintained under document control by the device manufacturer's Technology Review Board (TRB) in accordance with MIL-PRF-38535 and shall be made available to the acquiring or preparing activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
- 4.4.4 <u>Group D inspection.</u> Group D inspection shall be in accordance with table V of MIL-PRF-38535. End-point electrical parameters shall be as specified in table II herein.
 - 4.5 Methods of inspection. Methods of inspection shall be specified as follows:
- 4.5.1 <u>Voltage and current.</u> All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

	Device type 01, 02, 03, 04, and 05	Device type 06 and 07	Device type 08	Device type 09
Terminal	Cases	Cases	Cases	Cases
number	E and F	E and F	E and F	A, B, C, and D
1	OUT 0	IN B	IN B	IN B
2	OUT 1	IN C	IN C	IN C
3	OUT 2	LT	LT	BI
4	OUT 3	RBO	RBO/B1	IN D
5	OUT 4	RBI	RBI	IN A
6	OUT 5	IN D	IN D	OUT E
7	OUT 6	IN A	IN A	GND
8	GND	GND	GND	OUT D
9	OUT 7	OUT E	OUT E	OUT C
10	OUT 8	OUT D	OUT D	OUT B
11	OUT 9	OUT C	OUT C	OUT A
12	IN D	OUT B	OUT B	OUT G
13	IN C	OUT A	OUT A	OUT F
14	IN B	OUT G	OUT G	Vcc
15	IN A	OUT F	OUT F	
16	V _{CC}	Vcc	Vcc	

LT = Lamp Test BI = Blanking Input RBO = Ripple-blanking Output RBI = Ripple-blanking Input

FIGURE 1. <u>Terminal connections</u>.

DEVICE TYPE 01,04 AND 05

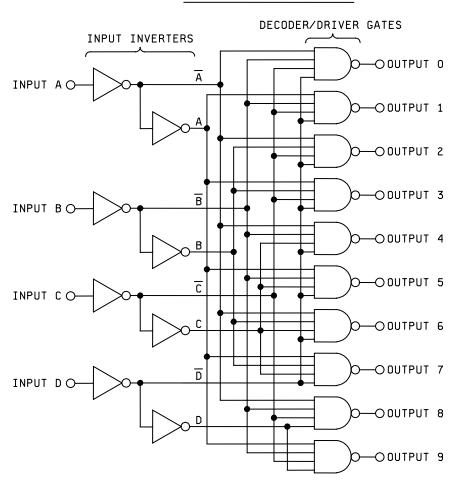


FIGURE 2. Logic diagrams.

DEVICE TYPE 02

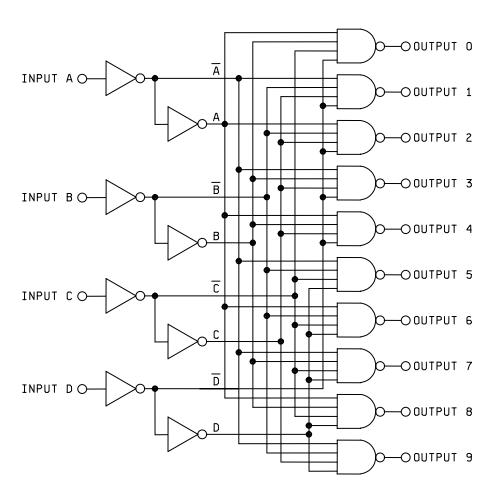


FIGURE 2. Logic diagrams - Continued.

DEVICE TYPE 03

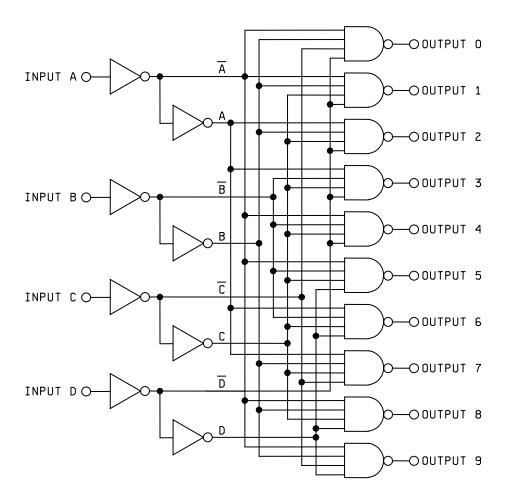


FIGURE 2. Logic diagrams - Continued.

DEVICE TYPES 06 AND 07

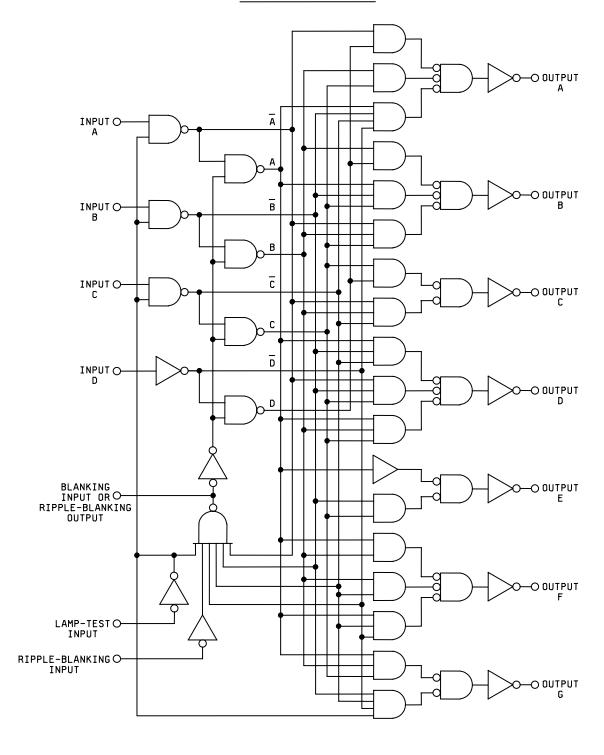


FIGURE 2. Logic diagrams - Continued.

DEVICE TYPE 08

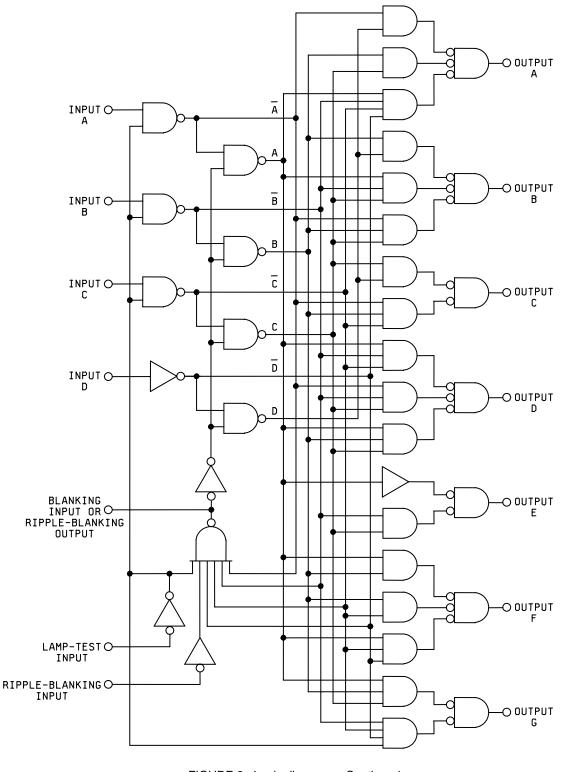


FIGURE 2. Logic diagrams - Continued.

DEVICE TYPE 09

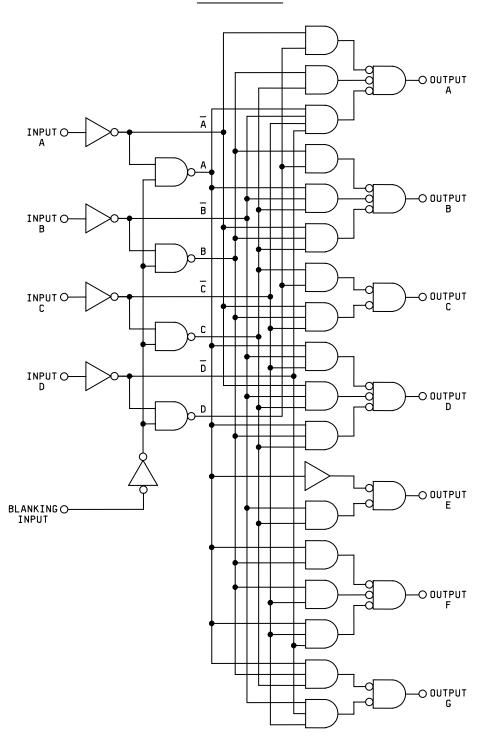


FIGURE 2. Logic diagrams - Continued.

Device types 01, 04, and 05

	INPL	JTS			OUTPUTS								
D	С	В	Α	0	1	2	3	4	5	6	7	8	9
L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

FIGURE 3. Truth tables.

Device type 02

	INPL	JTS						OUTI	PUTS				
D	С	В	Α	0	1	2	3	4	5	6	7	8	9
L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
L	Н	L	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
L	Н	Н	L	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
Н	L	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
Н	L	Η	Н	Н	Н	Н	Н	Η	Н	Н	Н	L	Н
Н	Н	L	L	Н	Н	Н	Н	Η	Н	Н	Н	Н	L
Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	Η	L	Н	Н	Н	Н	Η	Н	Н	Н	Н	Н

Device type 03

	INPL	JTS						OUTI	PUTS				
D	С	В	Α	0	1	2	3	4	5	6	7	8	9
L	L	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	Н	Н	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
L	Н	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
Н	Н	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
Н	L	Η	L	Η	Η	Η	Η	Η	Н	Η	Η	Н	L
Н	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	L	Ι	Η	Η	Η	Η	Н	Ι	Η	Н	Н
L	L	L	Ι	Ι	Η	Η	Η	Η	Н	Ι	Η	Н	Н
L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

FIGURE 3. <u>Truth tables</u> - Continued.

Device types 06 and 07

DECIMAL				INPU	TS					0	UTPU	ΓS			
OR FUNCTION	LT	RBI	D	С	В	Α	BI/RBO	Α	В	С	D	Е	F	G	NOTE
0	Н	Н	L	L	L	L	Н	L	L	L	L	L	L	Н	1
1	Ι	Х	L	L	L	Ι	Н	Ι	L	L	Н	Н	Ι	Ι	1
2	Н	Х	L	L	Н	L	Н	L	L	Н	L	L	Н	L	
3	Н	Х	L	L	Н	Н	Н	L	L	L	L	Н	Н	L	
4	Н	Х	L	Н	L	L	Н	Н	L	L	Н	Н	L	L	
5	Н	Х	L	Н	L	Н	Н	L	Н	L	L	Н	L	L	
6	Н	Х	L	Н	Н	L	Н	Н	Н	L	L	L	L	L	
7	Н	Х	L	Н	Н	Н	Н	L	L	L	Н	Н	Н	Н	
8	Н	Х	Н	L	L	L	Н	L	L	L	L	L	L	L	
9	Н	Х	Н	L	L	Н	Н	L	L	L	Н	Н	L	L	
10	Н	Х	Н	L	Н	L	Н	Н	Н	Н	L	L	Н	L	
11	Н	Х	Н	L	Н	Н	Н	Н	Н	L	L	Н	Н	L	
12	Н	Х	Н	Н	L	L	Н	Н	L	Н	Н	Н	L	L	
13	Н	Х	Н	Н	L	Н	Н	L	Н	Н	L	Н	L	L	
14	Н	Х	Н	Н	Н	L	Н	Н	Н	Н	L	L	L	L	
15	Н	Х	Н	Н	Н	Н	Н	Η	Н	Н	Н	Н	Н	Н	
BI	Χ	Х	Х	Х	Х	Χ	L	Η	Н	Н	Н	Н	Н	Н	2
RBI	Н	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	Н	3
LT	L	Х	Χ	Χ	Χ	Χ	Н	L	L	L	L	L	L	L	4

NOTES:

- 1. BI/RBO is wire-OR logic serving as blanking input (BI) and/or ripple-blanking output (RBO). The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired, and ripple-blanking input (RBI) must be open or at a high logic level during the decimal 0 output. X = input may be high or low.
- 2. When a low logic level is applied to the blanking input (forced condition) all segment outputs go to a low logic level regardless of the state of any other input condition.
- 3. When ripple-blanking input (RBI) is at a low logic level, lamp test input is at high logic level and A = B = C = D = low logic level, all segment outputs go to a low logic level and the ripple-blanking output goes to a low logic level (response condition).
- 4. When blanking input/ripple-blanking output is open or held at a high logic level, and a low logic level is applied to lamp test input, all segment outputs go to a high logic level.

FIGURE 3. Truth tables - Continued.

Device type 08

DECIMAL				INPU [*]	ΓS					0	UTPUT	ΓS			
OR FUNCTION	LT	RBI	D	С	В	Α	BI/RBO	Α	В	O	D	ш	F	G	NOTE
0	Н	Н	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	L	1
1	Н	Х	L	L	L	Η	Н	L	Η	Η	L	L	L	L	1
2	Н	Х	L	L	Η	L	Н	Η	Н	L	Н	Н	L	Н	
3	Н	Х	L	L	Н	Н	Н	Н	Н	Н	Н	L	L	Н	
4	Н	Х	L	Н	L	L	Н	L	Н	Н	L	L	Н	Н	
5	Н	Х	L	Н	L	Н	Н	Н	L	Н	Н	L	Н	Н	
6	Н	Х	L	Н	Н	L	Н	L	L	Н	Н	Н	Н	Н	
7	Н	Х	L	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	
8	Н	Х	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	
9	Н	Х	Н	L	L	Н	Н	Н	Н	Н	L	L	Н	Н	
10	Н	Х	Н	L	Н	L	Н	L	L	L	Н	Н	L	Н	
11	Н	Х	Н	L	Н	Н	Н	L	L	Н	Н	L	L	Н	
12	Н	Х	Н	Н	L	L	Н	L	Н	L	L	L	Н	Н	
13	Н	Х	Н	Н	L	Н	Н	Н	L	L	Н	L	Н	Н	
14	Н	Х	Н	Н	Н	L	Н	L	L	L	Н	Н	Н	Н	
15	Н	Х	Н	Н	Η	Н	Н	L	L	L	L	L	L	L	
BI	Χ	Х	Χ	Х	Χ	Χ	L	L	L	L	L	L	L	L	2
RBI	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	3
LT	L	Х	Χ	Χ	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н	4

NOTES:

- 1. Bl/RBO is wire-OR logic serving as blanking input (BI) and/or ripple-blanking output (RBO). The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired, and ripple-blanking input (RBI) must be open or at a high logic level during the decimal 0 output. X = input may be high or low.
- 2. When a low logic level is applied to the blanking input (forced condition) all segment outputs go to a low logic level regardless of the state of any other input condition.
- 3. When ripple-blanking input (RBI) is at a low logic level, lamp test input is at high logic level and A = B = C = D = low logic level, all segment outputs go to a low logic level and the ripple-blanking output goes to a low logic level (response condition).
- 4. When blanking input/ripple-blanking output is open or held at a high logic level, and a low logic level is applied to lamp test input, all segment outputs go to a high logic level.

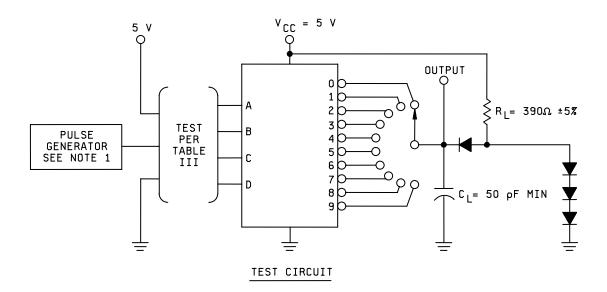
FIGURE 3. Truth tables - Continued.

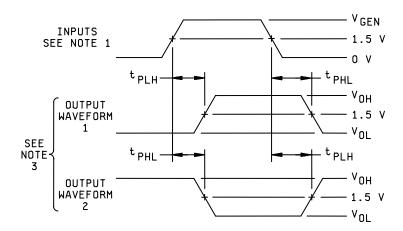
Device type 09

DECIMAL			INPUT	S				Ol	JTPUT	S			
OR FUNCTION	D	С	В	Α	ВІ	Α	В	С	D	Е	F	G	NOTE
0	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	L	1
1	L	L	L	Н	Н	L	Н	Н	L	L	L	L	
2	L	L	Н	L	Н	Н	Η	L	Ι	Η	L	Ι	
3	L	L	Н	Н	Н	Н	Н	Н	Н	L	L	Н	
4	L	Н	L	L	Н	L	Н	Н	L	L	Н	Н	
5	L	Н	L	Н	Н	Н	L	Н	Н	L	Н	Н	
6	L	Н	Н	L	Н	L	L	Н	Н	Н	Н	Н	
7	L	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	
8	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	
9	Н	L	L	Н	Н	Н	Н	Н	L	L	Н	Н	
10	Н	L	Н	L	Н	L	L	L	Н	Н	L	Н	
11	Н	L	Н	Н	Н	L	L	Н	Н	L	L	Н	
12	Н	Н	L	L	Н	L	Н	L	L	L	Н	Н	
13	Н	Н	L	Н	Н	Н	L	L	Н	L	Н	Н	
14	Н	Н	Н	L	Н	L	L	L	Н	Н	Н	Н	
15	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	
BI	Χ	Χ	Х	Х	L	L	L	L	L	L	L	L	2

- 1. The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired.
- 2. When a low logic level is applied to the blanking input all segment outputs go to a low logic level regardless of the state of any other input condition. X = input may be high or low.

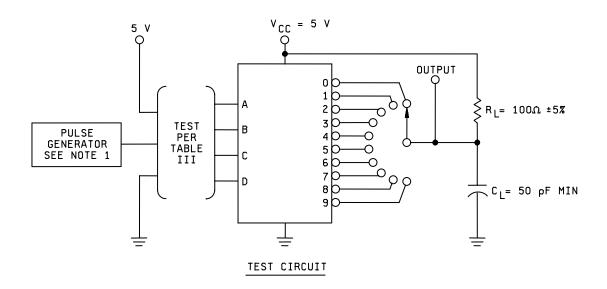
FIGURE 3. <u>Truth tables</u> - Continued.

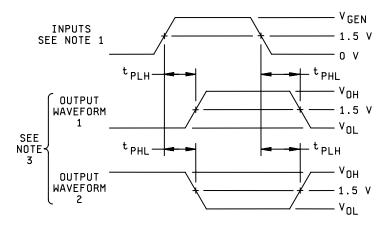




- 1. The pulse generator has the following characteristics: $V_{GEN} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) \leq 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
- 2. C_L includes probe and jig capacitance.
- 3. Input output waveform combination in accordance with the truth tables (see figure 3).
- 4. All diodes are 1N3064 or equivalent.

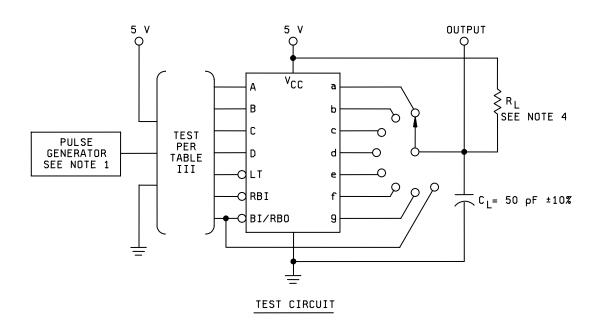
FIGURE 4. Switching times for device types 01, 02, and 03.

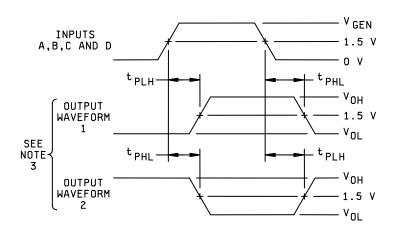




- 1. The pulse generator has the following characteristics: $V_{GEN} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) \leq 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
- 2. C_L includes probe and jig capacitance.
- 3. Input output waveform combination in accordance with the truth tables (see figure 3).

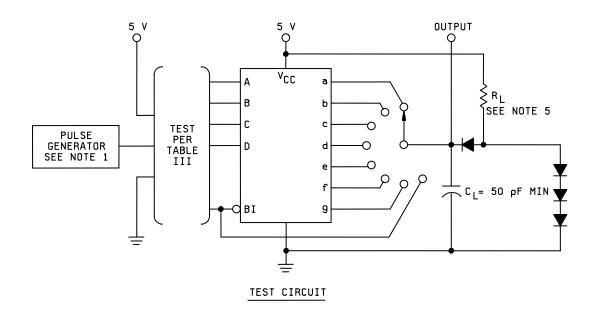
FIGURE 5. Switching times for device types 04 and 05.

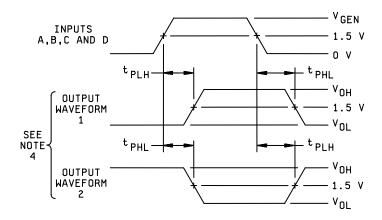




- 1. The pulse generator has the following characteristics: $V_{GEN} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) \leq 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
- 2. C_L includes probe and jig capacitance.
- 3. Input output waveform combination in accordance with the truth tables (see figure 3).
- 4. $R_L = 120~\Omega~\pm 5\%$ for outputs A thru G; $R_L = 560~\Omega~\pm 5\%$ for output BI/RBO.

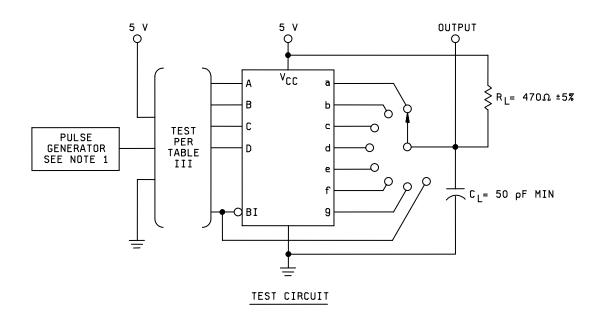
FIGURE 6. Switching times for device types 06 and 07.

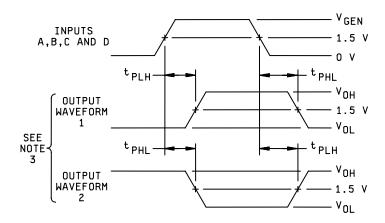




- 1. The pulse generator has the following characteristics: $V_{GEN} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) \leq 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
- 2. C_L includes probe and jig capacitance.
- 3. All diodes are 1N3064 or equivalent.
- 4. Input output waveform combination in accordance with the truth tables (see figure 3).
- 5. $R_L = 750~\Omega~\pm 5\%$ for outputs A thru G; $R_L = 560~\Omega~\pm 5\%$ for output BI/RBO.

FIGURE 7. Switching times for device type 08.





- 1. The pulse generator has the following characteristics: $V_{GEN} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) \leq 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
- 2. C_L includes probe and jig capacitance.
- 3. Input output waveform combination in accordance with the truth tables (see figure 3).

FIGURE 8. Switching times for device type 09.

TABLE III. Group A inspection for device type 01. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	nits	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}	1	Min	Max	
1	V _{OH}	3006	1	-0.8 mA							GND				2.0 V	2.0 V	2.0 V	2.0 V	4.5 V	0	2.4 V		V
$Tc = 25^{\circ}C$			2		-0.8 mA										"		"	"		1	"		"
			3			-0.8 mA													:	2			
			4				-0.8 mA	0.0 4												3			
			5 6					-0.8 mA	-0.8 mA											5			
			7						-U.8 IIIA	-0.8 mA										_			
			8							-0.6 IIIA		-0.8 mA								6			
			9									-0.0 IIIA	-0.8 mA							8			
			10										0.0 1111	-0.8 mA						9			
	OL	3007	11	16 mA										0.0	0.8 V	0.8 V	0.8 V	0.8 V		0		0.4 V	V
	OL	"	12	1011111	16 mA										0.0 .	"	0.8 V	2.0 V		ĭ		"	
			13		-	16 mA											2.0 V	0.8 V		2			
V			14				16 mA										2.0 V	2.0 V		3			
			15					16 mA								2.0 V	0.8 V	0.8 V		4			
			16						16 mA								0.8 V	2.0 V		5			
			17							16 mA						:	2.0 V	0.8 V		6			
			18									16 mA					2.0 V	2.0 V		7			
			19										16 mA	40 4	2.0 V	0.8 V	0.8 V	0.8 V	:	8			
			20											16 mA	2.0 V	0.8 V	0.8 V	2.0 V		9			
	IC		21														10 1	-12 mA		A B		-1.5	V
			22 23													-12 mA	-12 mA			C			
V			23												-12 mA	-12 IIIA				D			
V		3010	25												GND	GND	GND	2.4 V	5.5 V	A		40	μА
	IH1	0010	26												UND "	GND	2.4 V	GND	0.0 V	В		"	μ/\
			27													2.4 V	GND	OI,		Č			
1			28												2.4 V	GND	"	ar .		Ď			
	IH2	3010	29												GND	"		5.5 V		Α		100	μА
	1112		30														5.5 V	GND		В			,,,
			31													5.5 V	GND	"		С			
1			32								"				5.5 V	GND	GND	"		D		"	
	IL	3009	33								"				"	5.5 V	5.5 V	0.4 V		Α	-0.7	-1.6	mA
		"	34													5.5 V	0.4 V	5.5 V		В	"	"	
		"	35								"					0.4 V	5.5 V	"		C	"		
1			36	0.115											0.4 V	5.5 V	5.5 V	"		D			
	os	3011	37	GND	ONE										5.5 V	5.5 V	5.5 V	5.5 V	:	0	-20	-55	mA "
			38		GND	OND														1			
			39			GND	GND													2			
I			40 41				GIND	GND												3			
			42					GIND	GND											5			
			43						GND	GND										6			
		"	44							0110		GND								7			
			45									0	GND							8	"		
		"	46								"		-	GND				"		9	"		
	СС	3005	47								"				GND	GND	GND	GND	"	V _{CC}		41	mA
2		ests, termina	al conditions	s, and limit	s as subgi	roup 1, exc	ept T _C = +	125°C and	V _{IC} tests	are omitte	ed.			•							•		
3		ests, termina																					

See footnotes at end of device type 01.

TABLE III. Group A inspection for device type 01.

Terminal conditions (pins not designated may be high > 2.4 V or low < 0.8 V; or open).

						ı eri	minai co	nditions	s (pins r	iot desig	gnated n	nay be r	nigh ≥ 2.	.4 V or I	$0.8 \ge 0.8$	v; or op	pen).						
		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	iits	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	
7	Truth		48	L	Н	Н	Н	Н	Н	Н	GND	Н	Н	Н	GND	GND	GND	GND	5.0 V				
Tc = 25°C	table test		49 50	H	H	H											GND 5.0 V	5.0 V GND					
	เยรเ		51	"	"	Ь	L	"	"	"				"			5.0 V	5.0 V					
			52			"	Н	L			"			"		5.0 V	GND	GND					
			53	"	"	"	"	Н	L		"		"	"	"		GND	5.0 V	:		See		
			54 55						H	L H	·	ï					5.0 V 5.0 V	GND 5.0 V			note		
			56							\ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		н	L	"	5.0 V	GND	GND	GND					
			57		"		"		"				Н	L			GND	5.0 V	:				
			58 59								i i			H			5.0 V 5.0 V	GND					
			60													5.0 V	GND	5.0 V GND					
			61		"									"			GND	5.0 V					
			62											"			5.0 V	GND	:				
8	Truth		63 64 thru 79	Sama ta	ete ac au-	group 7 a	voont T	±12F°C	l					l "			5.0 V	5.0 V			I	l	
0	table					group 7, e																	
9	test	3003	80 thru 95 96 & 97	Same te OUT	sts as sub	group 7, e	xcept T _C =	-55°C.	1	1	GND		1	1	GND	GND	GND	IN	5.0 V	A to 0	5	32	ne
Tc = 25°C	t _{PHL} t _{PLH}	(Fig. 4)	98 & 99	001	OUT						GND "				GND "	GND	GND	IIN "	5.0 V	A to 1	5 "	37	ns "
.0-200	4 61	(1.19. 1)	100 & 101								"	OUT				5.0 V	5.0 V			A to 7		37	
			102 & 103		OUT											GND	IN	5.0 V	:	B to 1		32	
			104 & 105 106 & 107			OUT		OUT								5.0 V GND		GND GND		B to 4 B to 2		32 37	
			108 & 107			OUT										IN	5.0 V	GND		C to 2		32	
			110 & 111			00.	OUT										5.0 V	5.0 V		C to 3			
			112 & 113										OUT	OUT	5.0 V	:	GND	GND	:	C to 8	:		
			114 & 115 116 & 117					OUT						OUT	5.0 V GND			5.0 V GND		C to 9 C to 4		37	
			118 & 119					001	OUT						UND "			5.0 V		C to 5		37	
		"	120 & 121							OUT							5.0 V	GND		C to 6			
			122 & 123				OUT					OUT				OND		5.0 V	:	C to 7		"	
			124 & 125 126 & 127				OUT	OUT							IN "	GND 5.0 V	GND	5.0 V GND		D to 3 D to 4		32	
			128 & 129					001	OUT							0.0 1	GND	5.0 V		D to 5			
		"	130 & 131							OUT							5.0 V	GND		D to 6			
			132 & 133								:	OUT	OUT			CND	5.0 V	5.0 V	:	D to 7		27	:
			134 & 135 136 & 137										OUT	OUT	"	GND "	GND "	GND 5.0 V		D to 8 D to 9		37 37	
10	t _{PHL}	3003	138 & 139	OUT							"				GND		"	IN		A to 0		39	
Tc = 125°C	t _{PLH}	(Fig. 4)	140 & 141		OUT							OUT				- "	= "		:	A to 1		46	
			142 & 143 144 & 145		OUT							OUT				5.0 V GND	5.0 V IN	5.0 V		A to 7 B to 1	i i	46 39	
			146 & 145		001			OUT								5.0 V	"	GND		B to 4		39	
		"	148 & 149			OUT									"	GND	"	GND		B to 2		46	
			150 & 151			OUT	OUT									IN	5.0 V	GND	:	C to 2		39	
			152 & 153 154 & 155				OUT						OUT		5.0 V		5.0 V GND	5.0 V GND		C to 3 C to 8			
1			156 & 157										001	OUT	5.0 V		UND.	5.0 V		C to 9			
		"	158 & 159					OUT							GND			GND		C to 4		46	
			160 & 161 162 & 163						OUT	OUT							5.0 V	5.0 V GND	:	C to 5			
			164 & 165							OUT		OUT					5.U V	5.0 V		C to 6 C to 7			
		"	166 & 167				OUT				"	551			IN	GND	"	5.0 V		D to 3		39	
			168 & 169					OUT								5.0 V	GND	GND	:	D to 4			
			170 & 171 172 & 173						OUT	OUT						i i	GND 5.0 V	5.0 V GND		D to 5 D to 6	i i		
			174 & 175							001		OUT			"		5.0 V	5.0 V		D to 7			
		"	176 & 177								"		OUT		"	GND	GND	GND		D to 8		46	
<u></u>			178 & 179	L	L	L	L				"			OUT		GND	GND	5.0 V		D to 9		46	
11	Same t	ests, termina	al conditions	, and limit	s as subgr	roup 10, ex	$cept T_c =$	-55°C.															

NOTE: Output voltages shall be either: (a) H = 2.4 V minimum and L = 0.4 V maximum when using a high speed checker double comparator, or (b) H ≥ 1.5 V and L ≤ 1.5 V when using a high speed checker single comparator.

TABLE III. Group A inspection for device type 02. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	İ
1	V _{OH}	3006	1	-0.8 mA							GND				2.0 V	2.0 V	2.0 V	2.0 V	4.5 V	0	2.4 V		V
Tc = 25°C			2		-0.8 mA										"	"	"			1	"		. "
			3			-0.8 mA														2			
			4				-0.8 mA													3			
			5					-0.8 mA	0.0 4											4			
			6 7						-0.8 mA	0.0 1										5			
			8							-0.8 mA		-0.8 mA								6			
			9									-0.6 IIIA	-0.8 mA							8			
			10										-0.0 IIIA	-0.8 mA						9			
	OL	3007	11	16 mA										0.0 1117 (0.8 V	0.8 V	2.0 V	2.0 V		0		0.4 V	V
	OL	0007	12	1011111	16 mA										0.0 1	0.0 1	0.8 V	0.8 V		1		0.4 0	, i
			13			16 mA											0.8 V	2.0 V		2			
V			14				16 mA										2.0 V	0.8 V		3			
•			15				-	16 mA								2.0 V	2.0 V	2.0 V		4			
			16						16 mA								0.8 V	0.8 V		5			
			17							16 mA							0.8 V	2.0 V		6		"	
			18									16 mA			"	"	2.0 V	0.8 V		7		"	. "
		"	19										16 mA		2.0 V	0.8 V	2.0 V	2.0 V		8		"	. "
		"	20								-			16 mA	2.0 V	0.8 V	0.8 V	0.8 V		9		-	
	I C		21								:							-12 mA		A		-1.5	V
			22													40 4	-12 mA			В			
			23												40 4	-12 mA				С			
V		0040	24								-				-12 mA	OND	OND	0.41/	5 5 V	D		40	
	IH1	3010	25												GND	GND	GND	2.4 V GND	5.5 V	A		40	μA
			26 27													GND 2.4 V	2.4 V GND	GŅD		B C			
1			28												2.4 V	GND	GIND	ar .		D			
		3010	29												GND	UND "		5.5 V		A		100	μА
	IH2	3010	30												OND "		5.5 V	GND		В		"	μ
			31													5.5 V	GND	GIVD		Č			
			32												5.5 V	GND	GND	"		Ď			
'	IL	3009	33												"	5.5 V	5.5 V	0.4 V		A	-0.7	-1.6	mA
	IL.	"	34													5.5 V	0.4 V	5.5 V		В	"	"	
		"	35													0.4 V	5.5 V	"		Č	"	"	
		"	36												0.4 V	5.5 V	5.5 V	"		D	"	"	
'	os	3011	37	GND											5.5 V	5.5 V	5.5 V	5.5 V		0	-20	-55	mA
		"	38		GND											"	"	"		1	"		
		"	39			GND											"	"		2	"	"	
1		. "	40				GND													3			
		"	41					GND	OND											4			
			42						GND	GND										5			
			43 44							GND		GND								7			
			44									GIND	GND							8			
			46										GIND	GND						9			
	CC	3005	47											OND	GND	GND	GND	GND		V _{CC}		41	mA
2			al conditions	and limite	s as subar	nun 1 evo	ent To - +	125°C and	V. a tests	are omitte	ed.				0.10	0.10	0.10	0.10		* CC	l	71	
			al conditions																				

See footnotes at end of device type 01.

TABLE III. Group A inspection for device type 02. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	
7 Tc = 25°C	Truth table test	metrod	48 49 50 51 52 53 54 55 56 57 58 59 60 61 62	L H	H	H H L H	H	H	H	H	GND	H	I	H	GND " " 5.0 V " " " " " " " " " " " " " " " " " " "	GND 5.0 V " " GND " 5.0 V	5.0 V GND GND 5.0 V 5.0 V GND GND 5.0 V GND GND 5.0 V 5.0 V GND GND GND GND GND	5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V	5.0 V		See note		
			63		"	"	"			"					"	"	5.0 V	GND					
8	Truth table		64 thru 79	Same te	sts as sub	group 7, e	xcept T _C =	+125°C.															
	test		80 thru 95	Same te	sts as sub	aroup 7. e	xcept T _C =	-55°C.															
9 Tc = 25°C	трн теµн	3003 (Fig. 4)	96 & 97 98 & 99 100 & 101 102 & 103 104 & 105 106 & 107 108 & 109 110 & 111 112 & 113 114 & 115 116 & 117 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 313 136 & 313 140 & 141 142 & 143	OUT	ОИТ	OUT	OUT	OUT	OUT	OUT	GND	OUT	OUT	OUT	GND 5.0 V GND 5.0 V GND 5.0 V S.0 V	5.0 V GND 5.0 V GND 5.0 V GND 5.0 V 5.0 V GND W """""""""""""""""""""""""""""""""""	GND 5.0 V GND 5.0 V GND IN " " 5.0 V GND GND GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND	IN " " " " " " " " " " " " " " " " " " "	5.0 V	A to 1 A to 9 A to 0 A to 0 B to 6 B to 9 B to 7 B to 8 C to 5 C to 5 C to 5 C to 5 C to 2 C to 4 C to 9 D to 3 D to 6 D to 6 D to 7 D to 8	5	32 " 37 37 32 32 32 37 37 32 32 32 32 37 " " "	ns

See notes at end of device type 02.

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TABLE III. <u>Group A inspection for device type 02</u>. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

Subgroup Symbol 883			MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
10	Subgroup	Symbol		Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{cc}		Min	Max	1
Tc = 125°C	10	tou		144 & 145		OUT						GND				GND	5.0 V	GND	IN	5.0 V	A to 1	5	39	ns
148 & 149	_												OUT										"	
150 & 151 OUT	10 - 120 0	TEN	(1.3.1)												OUT		_							
152 & 153 OUT 156 & 157 OUT 156 & 157 OUT 158 & 159 OUT 160 & 161 OUT 162 & 163 OUT 164 & 165 OUT 166 & 167 OUT					OUT																		46	
156 & 157				152 & 153							OUT					5.0 V	GND		"		A to 6		46	
158 & 159				154 & 155			OUT									GND	5.0 V	IN	5.0 V		B to 2		39	
160 & 161 162 & 163 162 & 163 164 & 165 164 & 165 166 & 167 OUT											OUT													
" 162 & 163				158 & 159											OUT	5.0 V	5.0 V	"	GND		B to 9		39	
164 & 165 166 & 167 OUT				160 & 161				OUT								GND	5.0 V	"	GND	"	B to 3		46	
" 166 & 167 OUT				162 & 163									OUT			5.0 V	GND	"	GND	"	B to 7		46	
" 168 & 169 " 170 & 171 " OUT " OUT " OUT " S.0 V " GND GND " C to 5 " 39 39 170 & 171 & 172 & 173 OUT " OUT " OUT " GND GND " GND " C to 2 " " GND GND " GND " GND " C to 2 " " GND GND " GND " GND " GND " C to 2 " " GND GND " G														OUT			GND	"			B to 8	"	46	
" 170 & 171				166 & 167	OUT											GND	IN	5.0 V		"	C to 0		39	
172 & 173										OUT												"		
" 174 & 175			-									"		OUT						-		"		
" 176 & 177 " 176 & 177 " 178 & 179 " 180 & 181 " 182 & 183 " OUT " OUT " OUT " S.0 V S.0 V " C to 4 " " " OUT S.0 V " GND GND " C to 9 " " OUT S.0 V " GND GND " D to 3 " 34 OUT GND GND " D to 4 " 34						OUT						-					"					"	-	
" 178 & 179 " 180 & 181 " " OUT 5.0 V " GND GND " C to 9 " " 34 OUT 182 & 183 OUT OUT " 5.0 V 5.0 V 5.0 V 5.0 V T D to 4 T 34 OUT T T T T T T T T T			-				OUT					"					"					"	"	
" 180 & 181									OUT								"					"		
" 182 & 183 OUT " 5.0 V 5.0 V 5.0 V " Dto 4 " 34															OUT									
" 182 & 183								OUT												-				: 1
I I " 1184 & 1851 I I I I I OUII I " I I I I GND I GND I GND I " I Dto 5 I " I 46 I									OUT														-	: 1
										OUT	0.17									-			46	: 1
											OUT		OUT				l :	-						
" 188 & 189													OUT	OUT						-				i i
" 190 & 191					10.0	· .	40		5500	l				UUT				5.U V	5.0 V		שוט ט			

NOTE: Output voltages shall be either: (a) H = 2.4 V minimum and L = 0.4 V maximum when using a high speed checker double comparator, or (b) H ≥ 1.5 V and L ≤ 1.5 V when using a high speed checker single comparator.

TABLE III. Group A inspection for device type 03. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	nits	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}	1	Min	Max	i
1	V _{OH}	3006	1	-0.8 mA							GND				0.8 V	0.8 V	0.8 V	0.8 V	4.5 V	0	2.4 V		V
c = 25°C		"	2		-0.8 mA										"			"		1	"		"
		:	3			-0.8 mA	0.0 4												:	2			
			4 5				-0.8 mA	-0.8 mA												3			
			6					-0.6 IIIA	-0.8 mA											5			
			7						-0.0 IIIA	-0.8 mA										6			
			8									-0.8 mA								7			
		"	9										-0.8 mA		"			"		8	"		
		-	10											-0.8 mA		"		"		9			
	OL	3007	11	16 mA	40 4										0.8 V	0.8 V	2.0 V	0.8 V	:	0		0.4 V	V
			12 13		16 mA	16 mA										2.0 V		0.8 V 2.0 V		1 2			
V			14			16 IIIA	16 mA										0.8 V	2.0 V 2.0 V		3			
V			15				10 1117	16 mA								"	U.U V	0.8 V		4			
			16						16 mA						2.0 V	"		0.8 V		5			
		"	17							16 mA					"	"		2.0 V		6		"	
			18									16 mA			"		2.0 V	2.0 V		7		"	
			19										16 mA					0.8 V		8			
			20											16 mA		0.8 V	"	0.8 V	<u> </u>	9			<u>"</u>
	IC		21 22														-12 mA	-12 mA		A B		-1.5	V
			23													-12 mA	-12 IIIA			Č			
V			24												-12 mA	121117				Ď			
•	IH1	3010	25								"				GND	GND	GND	2.4 V	5.5 V	Α		40	μА
			26													GND	2.4 V	GŅD		В			- "
			27												"	2.4 V	GND	,		С		"	
ı		"	28								- "				2.4 V	GND				D			
	IH2	3010	29												GND		551/	5.5 V		A		100	
			30 31													5.5 V	5.5 V GND	GND "		B C			
1			32												5.5 V	GND	GND	"		D			
1	IL	3009	33												U.U V	5.5 V	5.5 V	0.4 V		A	-0.7	-1.6	mA
	IL.	"	34													5.5 V	0.4 V	5.5 V		В	"	"	
		"	35													0.4 V	5.5 V	"		С	"	"	
1			36								"				0.4 V	5.5 V	5.5 V	"	"	D	"	"	"
•	os	3011	37	GND	ONE										GND	GND	GND	GND		0	-20	-55	mA
			38 39		GND	GND														1 2			
			39 40			GND	GND													2 3			
1			41				GIVE	GND												4			
			42					0	GND											5			
			43							GND								"		6			
		"	44									GND				"	"	"		7	"	"	"
			45										GND							8			
		0005	46											GND	OND	OND	OND.	OND	-:-	9		"	
	CC	3005	47				T	10500	-1.1/ 4						GND	GND	GND	GND		V _{CC}	l	41	mA
3			al conditions			roup 1, exc roup 1, exc																	

See footnotes at end of device type 03.

TABLE III. Group A inspection for device type 03. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	
7 Tc = 25°C	Truth table		48 49	L H	H L	H H	H	H	H	H	GND "	Η.	H	H	GND "	GND 5.0 V	5.0 V 5.0 V	GND GND	5.0 V				
. 5 - 25 6	test		50 51		H	L	"	:	:	:	:	:		:			5.0 V GND	5.0 V 5.0 V					
			52 53				H .	L H	" L						" 5.0 V	"	"	GND GND			See		
			54 55						H .	L H					0.0 V	"	5.0 V	5.0 V 5.0 V			note		
			56 57									H	L H	"		" GND	3.0 V	GND GND	"				
			58 59	:	:		:	:	:		:			H		"	" GND	5.0 V 5.0 V	"				
			60 61	:	:		:	:	:		:			:	" GND		"	GND GND					
			62 63												"		5.0 V	5.0 V 5.0 V					
8	Truth table		64 thru 79	Same te	sts as sub	group 7, e	xcept T _C =	+125°C.	I	I	I		ı	I	I		0.0 1	0.0 1	1		I		
	test		80 thru 95	Same te	sts as sub	group 7, e	xcept T _C =	-55°C.															
9 Tc = 25°C	t _{PHL}	3003 (Fig. 4)	96 & 97 98 & 99	OUT							GND "		OUT		GND 5.0 V	GND 5.0 V	5.0 V 5.0 V	IN IN	5.0 V	A to 0 A to 8	5	32 32	ns "
	1 21	"	100 & 101 102 & 103			OUT	OUT								GND GND	"	5.0 V IN	IN 5.0 V		A to 2 B to 3		37 32	
			104 & 105 106 & 107				00.	OUT	OUT						GND 5.0 V	"		GND GND		B to 4 B to 5		"	
			108 & 109	OUT						OUT					5.0 V GND	" GND		5.0 V GND		B to 6 B to 0		" 37	:
			112 & 113 114 & 115	001	OUT	OUT									GND GND	5.0 V 5.0 V		GND 5.0 V		B to 1 B to 2		"	
			116 & 117 118 & 119								"		OUT	OUT	5.0 V 5.0 V	5.0 V IN	5.0 V	GND GND		B to 8 C to 9		32	"
			120 & 121 122 & 123		OUT		OUT								GND GND		5.0 V GND	GND 5.0 V		C to 1 C to 3	"	37	"
			124 & 125 126 & 127					OUT	OUT						GND 5.0 V	"	"	GND GND		C to 4 C to 5			"
			128 & 129 130 & 131							OUT		OUT			5.0 V 5.0 V	"	5.0 V	5.0 V 5.0 V		C to 6 C to 7	"		
			132 & 133 134 & 135		OUT	OUT									IN "	5.0 V	5.0 V 5.0 V	GND 5.0 V	:	D to 1 D to 2		32 32	
			136 &137 138 & 139					OUT	OUT								GND GND	5.0 V GND		D to 4 D to 5		32 37	
			140 & 141 142 & 143							OUT		OUT					GND 5.0 V	5.0 V 5.0 V		D to 6 D to 7		:	
											"	OUT		OUT		" GND			:				

See notes at end of device type 03.

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TABLE III. Group A inspection for device type 03. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	nits	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{cc}		Min	Max	
10	t _{PHL}	3003	146 & 147	OUT							GND				GND	GND	5.0 V	IN	5.0 V	A to 0	5	39	ns
Tc = 125°C	t _{PLH}	(Fig. 4)	148 & 149										OUT		5.0 V	5.0 V	5.0 V	IN		A to 8		39	
		` " ′	150 & 151			OUT									GND		5.0 V	IN		A to 2		46	
			152 & 153				OUT								GND		IN	5.0 V		B to 3		39	
			154 & 155					OUT							GND	"	"	GND		B to 4			
			156 & 157						OUT						5.0 V	"	"	GND		B to 5			
			158 & 159							OUT	"				5.0 V	"	"	5.0 V		B to 6		"	
			160 & 161	OUT							"				GND	GND	"	GND		B to 0		46	
			162 & 163		OUT										GND	5.0 V	"	GND		B to 1			
			164 & 165			OUT					"				GND	5.0 V		5.0 V	"	B to 2	"		
			166 & 167										OUT		5.0 V	5.0 V	"	GND		B to 8			
			168 & 169								"			OUT	5.0 V	IN	5.0 V	GND	"	C to 9	"	39	
			170 & 171		OUT						"				GND	"	5.0 V	GND	"	C to 1	"	46	
			172 & 173				OUT				"				GND	"	GND	5.0 V	"	C to 3	"	"	
			174 & 175					OUT			"				GND	"	"	GND		C to 4			
			176 & 177						OUT		"				5.0 V	"	"	GND		C to 5			
			178 & 179							OUT	"				5.0 V	"		5.0 V		C to 6			
			180 & 181									OUT			5.0 V		5.0 V	5.0 V		C to 7			
		:	182 & 183		OUT						"				IN.	5.0 V	5.0 V	GND		D to 1		39	
			184 & 185			OUT									:		5.0 V	5.0 V		D to 2		39	
	1	:	186 &187					OUT							:		GND	5.0 V		D to 4		39	
			188 & 189						OUT						:		GND	GND		D to 5		46	
	1	:	190 & 191							OUT					:	"	GND	5.0 V		D to 6		"	:
		i i	192 & 193									OUT		0117	:		5.0 V	5.0 V		D to 7		l ".	
11			194 & 195								"			OUT		GND	5.0 V	GND		D to 9	.,		

NOTE: Output voltages shall be either: (a) H = 2.4 V minimum and L = 0.4 V maximum when using a high speed checker double comparator, or (b) H ≥ 1.5 V and L ≤ 1.5 V when using a high speed checker single comparator.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	
1	V _{OL}	3007	1	80 mA							GND				0.8 V	0.8 V	0.8 V	0.8 V	4.5 V	0		0.9	V
Γc = 25°C			2		mA												0.8 V	2.0 V	"	1		"	
			3			80 mA											2.0 V	0.8 V		2			
			4	80			80 mA										2.0 V	2.0 V	"	3		"	
			5	00				80 mA								2.0 V	0.8 V	0.8 V		4			
			6						80 mA								0.8 V	2.0 V		5			"
			7							80 mA		80 mA					2.0 V 2.0 V	0.8 V 2.0 V		6			
			8 9									80 IIIA	80 mA		2.0 V	0.8 V	0.8 V	2.0 V 0.8 V		8			
			10										OU IIIA	80 mA	2.0 V 2.0 V	0.8 V	0.8 V	2.0 V		9			
		3007	11	20 mA										OU IIIA	0.8 V		0.8 V	0.8 V		0		0.41/	V
	OL2	3007	12	20 MA	20 mA										0.8 V	0.8 V	0.8 V 0.8 V	0.8 V 2.0 V		1		0.4 V	V
			13		201117	20 mA											2.0 V	0.8 V		2			
V			14			20 1117	20 mA										2.0 V	2.0 V		3			
*			15				20 1117	20 mA								2.0 V	0.8 V	0.8 V		4			
			16						20 mA								0.8 V	2.0 V		5			
			17							20 mA							2.0 V	0.8 V		6			
			18									20 mA					2.0 V	2.0 V	"	7		"	
			19										20 mA		2.0 V	0.8 V	0.8 V	0.8 V	"	8		"	
			20											20 mA	2.0 V	0.8 V	0.8 V	2.0 V		9			
	CEX		21	Υ											2.0 V	2.0 V	2.0 V	2.0 V		0		250	μΑ
	_/		22		Υ														"	1			"
			23			Υ													"	2		"	"
I			24				Υ													3			
1			25					Y	.,											4			
			26						Υ											5			
			27 28							Υ		Υ								6			
			28 29									Y	Υ							, 8			
			30										ı	Υ						9			
			31											- '				-12 mA		A		-1.5	V
	IC		32														-12 mA	-121117		В		"	
			33													-12 mA	-1211174			C			
V			34												-12 mA					Ď			
· ·	IL	3009	35												5.5 V	5.5 V	5.5 V	0.4 V	5.5 V	A	-0.7	-1.6	mA
			36													5.5 V	0.4 V	5.5 V		В	"		
			37													0.4 V	5.5 V	ar.		С			
1		"	38			<u> </u>		<u> </u>			-				0.4 V	5.5 V	5.5 V	·P		D	-		"
	IH1	3010	39												GND	GND	GND	2.4 V	"	Α		40	μА
			40													GND	2.4 V	GND	"	В		"	
		"	41													2.4 V	GND	"		С			
ı		"	42												2.4 V	GND		"		D			"
	IH2	3010	43												GND	"		5.5 V	"	Α		100	μА
			44														5.5 V	GND	"	В		"	"
			45												_ "	5.5 V	GND			C			
ı			46												5.5 V	GND	GND		-	D			
	CC	3005	47			İ		İ	İ						GND	GND	GND	GND		V _{CC}		62	mA
2	Samo to	acte tarmins	al conditions	and limit	s as suhar	oun 1 exc	cent To - 4	-125°C and	d Vic tests	are omitte	d												

See footnotes at end of device types 04 and 05.

MIL-M-38510/10D

TABLE III. Group A inspection for device type 04 and 05. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{CC}		Min	Max	
7 Tc = 25°C	Truth table test		48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63	L H	H L H:	H H L H	H	H	H	H	GND	H	T	H	GND " " " " " " " " " " " " " " " " " " "	GND " " " " " " " " " " " " " " " " " " "	GND GND 5.0 V 5.0 V GND 5.0 V 5.0 V GND GND 5.0 V GND GND 5.0 V 5.0 V	GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V	5.0 V		2/		
8	Truth		64 thru 79	Same te	sts as sub	group 7, e	xcept T _C =	+125°C.	ı								0.0 1	0.0 7	"	ı			
	table test		80 thru 95	Camata	sts as sub	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	vaant T	FF°C															
9	t _{PHL}	3003	96 & 97	OUT	SIS AS SUD	group 7, e	xcept r _c =	: -55°C.			GND				GND	GND	GND	IN	5.0 V	A to 0	5	56	ns
Tc = 25°C		(Fig. 5)	98 & 99 100 & 101 102 & 103 104 & 105 106 & 107 108 & 109 110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137		ОПТ	OUT	OUT	OUT	OUT	OUT		OUT	OUT	OUT	GND GND 5.0 V GND " " " 5.0 V 5.0 V	GND 5.0 V GND GND GND 5.0 V IN " " " " " " " " " " " " " " " " " "	" " " IN IN IN 5.0 V 5.0 V GND GND 5.0 V GND GND 5.0 V GND GND GND GND GND GND GND GND GND GND	5.0 V GND GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V GND 5.0 V		A to 1 A to 8 B to 1 B to 6 C to 5 C to 5 C to 5 C to 6 C to 7 C to 8 D to 6 D to 7 D to 6 D to 7 D to 9 D to 9			

See notes at end of device types 04 and 05.

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TABLE III. Group A inspection for device type 04 and 05. Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	nits	Unit
Subgroup	Symbol	883 method	Test no.	0	1	2	3	4	5	6	GND	7	8	9	D	С	В	Α	V _{cc}		Min	Max	
10	t _{PHL}	3003	140 & 141	OUT							GND				GND	GND	GND	IN	5.0 V	A to 0	5	73	ns
Tc = 125°C	t _{PLH}	(Fig. 5)	142 & 143		OUT						"				GND	GND				A to 1			"
		"	144 & 145						OUT		"				GND	5.0 V				A to 5			"
			146 & 147								"		OUT		5.0 V	GND				A to 8			"
			148 & 149		OUT						"				GND	GND	IN	5.0 V		B to 1	"	"	"
			150 & 151			OUT					"				"	GND	IN	GND		B to 2		"	"
		"	152 & 153							OUT	"				"	5.0 V	IN	GND		B to 6			"
		"	154 & 155			OUT					"				"	IN	5.0 V	GND		C to 2			"
		"	156 & 157				OUT				"				"	"	5.0 V	5.0 V		C to 3		"	"
		"	158 & 159					OUT			"				"		GND	GND		C to 4		"	. "
		"	160 & 161						OUT		"				"		GND	5.0 V		C to 5			. "
			162 & 163							OUT	"				"		5.0 V	GND	"	C to 6		"	. "
		"	164 & 165									OUT			"		5.0 V	5.0 V		C to 7	"	"	"
			166 & 167								"		OUT		5.0 V		GND	GND	"	C to 8	"	"	. "
			168 & 169											OUT	5.0 V		GND	5.0 V		C to 9	"	"	. "
			170 & 171				OUT								IN	GND	5.0 V	5.0 V		D to 3	"	"	. "
			172 & 173					OUT							"	5.0 V	GND	GND		D to 4	"		. "
			174 & 175						OUT						"		GND	5.0 V		D to 5			
			176 & 177							OUT					"		5.0 V	GND		D to 6			
			178 & 179									OUT					5.0 V	5.0 V		D to 7			. "
			180 & 181										OUT	~		GND	GND	GND		D to 8			
11		"	182 & 183 al conditions,								"			OUT	"	GND	GND	5.0 V		D to 9	"	"	

 $[\]underline{1}$ / Y = 30 volts for device type 04 and 15 volts for device type 05. $\underline{2}$ / Output voltages shall be either:

⁽a) H = 2.4 V minimum and L = 0.4 V maximum when using a high speed checker double comparator, or

⁽b) $H \ge 1.5 \text{ V}$ and $L \le 1.5 \text{ V}$ when using a high speed checker single comparator.

Subgroup Symbox Set Subgroup Symbox Set Subgroup Symbox Set Subgroup Symbox Set Subgroup Symbox Set Subgroup Symbox Set Subgroup Symbox Symbox Subgroup Symbox Sy			MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Tr = 25°C 37	Subgroup	Symbol		Test no.	IN B	IN C	LT	RBO	RBI	IN D	IN A	GND	OUT E	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{CC}		Min	Max	
	1	V _{OL1}	3007	1	Х		0.8 V					GND	40 mA							4.5 V	OUT E		0.4	V
1	Tc = 25°C		"			"	"			"		"		40 mA										
S S S S S S S S S S															40 mA									
1					".X				"X	"X						40 mA	40 4						:	
1				_													40 mA	40 m A						
Col.																		40 IIIA	40 mA					
CS		OL 2	"		0.8 V	0.8 V	2.0 V	8 mA	0.8 V	0.8 V	0.8 V								40 IIIA				0.4	
V										"	"		Υ							"				μА
V				10										Υ							OUT D			
2	V	_													Υ									
Text	1															Υ	.,							
Section Sect	2																Y	V						
ON 3006 16								"										r	v					
1		OH	3006		0.8 V	0.8 V	2.0 V	2 mA	2.0 V	0.8 V	0.8 V	"										2.4		V
V V 20 20 21 20 20																							-1.5	
V						-12 mA						"												
1								-12 mA	40 4														:	:
1	V								-12 mA	10 m 1														
1 3009 23 4/2 0.4 V 5.5 V 5.5 V 5.5 V 5.5 V 5.5 V 1.6 V 1.0 V 1.6 V										-12 MA	-12 mA													
1		11.4	3009		0.4 V	5.5 V	5.5 V		5.5 V	5.5 V		"								5.5 V		-0.7	-1.6	mA
		ILI	"						"	"	"								"	"				
				24 <u>4</u> /					"			"										-0.7		
	1				5.5 V																			
1																								
						5.5 V			0.41/															
																						-		
27 CKT C							3.5 V			0.4 V														
28 CKTC				27 CKT C	"	"	"		"															
1			"	28 <u>4</u> /		"	"		"			"												
H11 3010 30 2.4 V GND GND GND GND GND GND " " " IN B 40 40 40 40 40 40 40					"	"	"					"												-
		IL2				"	"	0.4 V												5.5 V		-1.7		mA
		IH1	3010							GND	GND												40	μA
					GND																			
1	1 : 1					GIND																		
1	'									2.4 V														
			"			"					2.4 V													
1 37 GND 5.5 V GND GND " " " LT " RBI " IN D "		IH2	3010							GND	GND	"								"			100	μΑ
			"		GND					"	"	"												
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$:	GND																		:
" 41 " " " GND" GND 5.5 V " " IN A "	'						GND			5 5 V														
OS 3011 42 " " GND											55V													
CC 3005 43 5.5 V 9.5 V<		OS	3011			"	"	GND															-4	mA
2 Same tests, terminal conditions, and limits as subgroup 1, except T _C = +125°C and V _{I C} tests are omitted.					5.5 V	5.5 V	5.5 V					"												mA
	2											ed.					•							
Todano todo, terminar conditiono, and innite ac cabyroup 1, except 10 = 30 O and vig tests are critical.	3																							

See footnotes at end of device types 06 and 07.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lin	nits	Unit
ubgroup	Symbol	883 method	Test no.	IN B	IN C	LT	RBO	RBI	IN D	IN A	GND	OUT E	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{CC}		Min	Max	
7	Truth		44	GND	GND	5.0 V		5.0 V	GND	GND	GND	L	L	L	L	L	Н	L	5.0 V				
= 25°C	table		45	GND				X	"	5.0 V		Н	Н	L	"	Н	Н	Н					
	test		46	5.0 V						GND		L	L	H		L	L	H					
			47	5.0 V						5.0 V		H	L	Ļ		L		H			0/		
			48 49	GND GND	5.0 V					GND 5.0 V		H H	H		н	H L		L L			<u>3</u> /		
			50	5.0 V						GND		Ë	Ĺ		H	H		Ĺ					
			51	5.0 V						5.0 V		H	н		L L	Ľ	н	H					
			52	GND	GND				5.0 V	GND		Ë	L ii		Ĺ	Ĺ	l ï	Ë					
			53	GND	UI I				0.0 0	5.0 V		H	H		ī	ΙĒ	-	Ī					
			54	5.0 V						GND		Ë	L.	Н	H	H	"	H					
			55	5.0 V						5.0 V		Ĥ	Ē	Ľ	H	H	"	H					
			56	GND	5.0 V					GND		Н	H	Н	Ĺ	Н		Ĺ					
			57	GND						5.0 V		Н	L		Н	L		L					
			58	5.0 V						GND		L	L			Н	"	L	"				
			59	5.0 V	"	"	۱ .		"	5.0 V		Н	Н		"	"	Н	Н	"		1		l
			60	X	Х	X	GND	X	X	X	"	Н	Н			"	Н	Н					
			61	GND	GND	5.0 V	L	GND	GND	GND		Н	Н				Н	Н					
			62	Χ	X	GND		X	X	X	"	L	L	L	L	L	L	L	"				
8	Truth		63 - 81	Same te	sts as sub	group 7, e	xcept T _C =	125°C.															
	table		82 -100	Same te	sts as sub	group 7, e	xcept T _C =	-55°C.															
	test																						
9	t _{PHL}	3003	101 & 102	GND	GND	5.0 V		5.0 V	GND	IN	GND					OUT			5.0 V	IN A to A	8	104	ns
= 25°C	t_{PLH}	(Fig. 6)	103 & 104	GND	5.0 V				5.0 V							OUT				IN A to A	"	"	
			105 & 106	5.0 V					GND							OUT				IN A to A			
			107 & 108	GND											OUT					IN A to B			
			109 & 110	5.0 V											OUT					IN A to B			
			111 & 112	5.0 V	GND								01.17	OUT					1 :	IN A to C			
			113 & 114	GND	GND								OUT						1 :	IN A to D			
			115 & 116	GND	5.0 V								OUT							IN A to D			
			117 & 118	5.0 V	5.0 V							OUT	001							IN A to D			
			119 & 120	GND	GND GND							OUT								IN A to E			
			121 & 122 123 & 124	5.0 V GND	GND							001						OUT		IN A to E IN A to F			
			125 & 124	5.0 V	5.0 V				5.0 V									OUT		IN A to F			
			127 & 128	5.0 V	5.0 V				5.0 V								OUT	001		IN A to G			
			129 & 130	GND	GND		OUT	GND	GND								001			IN A to RBO			
			131 & 132	IN	GND		001	5.0 V	5.0 V	GND						OUT				IN B to A			
			133 & 134	,,,	GND			0.0 1	GND	5.0 V						OUT				IN B to A			
			135 & 136		5.0 V				GND	GND					OUT					IN B to B			
		"	137 & 138		GND				5.0 V	GND					OUT					IN B to B	"		"
		"	139 & 140		5.0 V				GND	5.0 V					OUT					IN B to B			
		"	141 & 142		GND	"			5.0 V	5.0 V					OUT					IN B to B	"	"	"
			143 & 144		GND	"			GND	GND				OUT					"	IN B to C	"	"	. "
					GND					5.0 V			OUT						"	IN B to D	"	"	. "
		"	145 & 146							GND			OUT				1	1	"	IN B to D	"	"	"
			147 & 148		5.0 V					0110	"	OUT						l		INDAGE			"
			147 & 148 149 & 150		5.0 V 5.0 V					GND					1	1				IN B to E		1	
			147 & 148 149 & 150 151 & 152		5.0 V 5.0 V 5.0 V				"	5.0 V								OUT		IN B to F			"
			147 & 148 149 & 150 151 & 152 153 & 154	"	5.0 V 5.0 V 5.0 V GND						"							OUT		IN B to F IN B to F			
			147 & 148 149 & 150 151 & 152 153 & 154 155 & 156	" " " " " " " " " " " " " " " " " " " "	5.0 V 5.0 V 5.0 V GND GND				5.0 V	5.0 V GND										IN B to F IN B to F IN B to F			
			147 & 148 149 & 150 151 & 152 153 & 154 155 & 156 157 & 158	" " " " " " " " " " " " " " " " " " " "	5.0 V 5.0 V 5.0 V GND GND GND				GND	5.0 V GND							OUT	OUT		IN B to F IN B to F IN B to F IN B to G			
			147 & 148 149 & 150 151 & 152 153 & 154 155 & 156 157 & 158 159 & 160	" " " GND	5.0 V 5.0 V 5.0 V GND GND				GND 5.0 V	5.0 V GND				0.17		OUT	OUT	OUT		IN B to F IN B to F IN B to F IN B to G IN C to A			
			147 & 148 149 & 150 151 & 152 153 & 154 155 & 156 157 & 158 159 & 160 161 & 162	5.0 V	5.0 V 5.0 V 5.0 V GND GND GND				GND 5.0 V GND	5.0 V GND "				OUT		OUT	OUT	OUT		IN B to F IN B to F IN B to F IN B to G IN C to A IN C to C			
			147 & 148 149 & 150 151 & 152 153 & 154 155 & 156 157 & 158 159 & 160 161 & 162 163 & 164	5.0 V GND	5.0 V 5.0 V 5.0 V GND GND GND				GND 5.0 V GND 5.0 V	5.0 V GND " " " 5.0 V			OUT	OUT OUT		ОИТ	OUT	OUT		IN B to F IN B to F IN B to F IN B to G IN C to A IN C to C IN C to C			
			147 & 148 149 & 150 151 & 152 153 & 154 155 & 156 157 & 158 159 & 160 161 & 162	5.0 V	5.0 V 5.0 V 5.0 V GND GND GND				GND 5.0 V GND	5.0 V GND "		OUT	OUT			ОИТ	OUT	OUT		IN B to F IN B to F IN B to F IN B to G IN C to A IN C to C			

See footnotes at end of device types 06 and 07.

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TABLE III. Group A inspection for device type 06 and 07. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	nits	Unit
Subgroup	Symbol	883 method	Test no.	IN B	IN C	LT	RBO	RBI	IN D	IN A	GND	OUT E	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{cc}	-	Min	Max	
9 Tc = 25°C	t _{PHL}	3003 (Fig. 6)	171 & 172 173 & 174	GND 5.0 V	IN 5.0 V	5.0 V		5.0 V	GND IN	5.0 V 5.0 V	GND "					OUT		OUT	5.0 V	IN C to F IN D to A	8	104	ns "
			175 & 176	GND	5.0 V GND				IN IN	GND				OUT			OUT			IN D to C		:	
			177 & 178 179 & 180		GND "	IN		GND	GND							OUT	001			IN D to G LT to A			
			181 & 182			IN	OUT	GND			"									LT to RBO			
			183 & 184	"	"	5.0 V	IN	5.0 V		"	"					OUT			"	RBO to A		"	
			185 & 186 187 & 188			i i	OUT	IN IN								OUT				RBI to A RBI to RBO			:
10	t _{PHL}	3003	189 & 190	"	"	"	001	5.0 V	"	IN	"					OUT				IN A to A	8	144	
Tc = 125°C	t _{PLH}	(Fig. 6)	191 & 192		5.0 V			0.0 .	5.0 V	"						OUT			"	IN A to A	ï		
			193 & 194	5.0 V					GND	"						OUT			"	IN A to A		"	
			195 & 196	GND						. "					OUT					IN A to B			
			197 & 198 199 & 200	5.0 V 5.0 V	GND					"				OUT	OUT					IN A to B IN A to C			
			201 & 202	GND	GND			"	"	"	"		OUT	001						IN A to D			
		"	203 & 204	GND	5.0 V			"	"	"	"		OUT						"	IN A to D		"	
		"	205 & 206	5.0 V	5.0 V				"	"	"		OUT						"	IN A to D		"	
			207 & 208	GND	GND							OUT								IN A to E			
			209 & 210 211 & 212	5.0 V GND	GND GND					"		OUT						OUT		IN A to E IN A to F			
			213 & 214	5.0 V	5.0 V				5.0 V	"	"							OUT		IN A to F			
		"	215 & 216	5.0 V	5.0 V	"		"	5.0 V	"	"						OUT	00.	"	IN A to G			
		"	217 & 218	GND	GND	"	OUT	GND	GND	"									"	IN A to RBO		"	
			219 & 220	IN.	GND			5.0 V	5.0 V	GND						OUT				IN B to A			:
			221 & 222 223 & 224		GND 5.0 V				GND GND	5.0 V GND					OUT	OUT				IN B to A IN B to B			
			225 & 224	"	GND				5.0 V	GND					OUT					IN B to B			
		"	227 & 228	"	5.0 V	"		"	GND	5.0 V	"				OUT					IN B to B			
		"	229 & 230	"	GND	"		"	5.0 V	5.0 V	"				OUT					IN B to B			
			231 & 232		GND				GND	GND				OUT						IN B to C			
			233 & 234 235 & 236	"	GND 5.0 V	i i				5.0 V GND			OUT							IN B to D IN B to D			
			237 & 238	"	5.0 V				"	GND	"	OUT	001							IN B to E			
			239 & 240	"	5.0 V	"		"	"	5.0 V	"	001						OUT		IN B to F			
		"	241 & 242	"	GND	"		"	"	GND	"							OUT		IN B to F		"	
			243 & 244		GND				5.0 V								OUT	OUT		IN B to F			:
			245 & 246	" GND	GND				GND 5.0 V							OUT	OUT			IN B to G			
			247 & 248 249 & 250	5.0 V	IN "				GND					OUT		001				IN C to A IN C to C			
		"	251 & 252	GND					5.0 V	5.0 V				OUT					"	IN C to C		"	
		"	253 & 254	GND	"	"		"	GND	GND	"		OUT						"	IN C to D		"	"
			255 & 256	GND					GND	GND	"	OUT								IN C to E			
			257 & 258 259 & 260	5.0 V GND	501/				5.0 V GND	GND 5.0 V								OUT		IN C to F IN C to F			
			259 & 260 261 & 262	5.0 V	5.0 V 5.0 V				IN	5.0 V 5.0 V						OUT		001		IN C to F			
			263 & 264	GND	5.0 V				IN	GND				OUT		001				IN D to C			
		"	265 & 266		GND			"	IN	"	"						OUT			IN D to G		"	
			267 & 268	"	"	IN		GND	GND		"					OUT			"	LT to A		"	"
			269 & 270			IN FOV	OUT	GND								OUT				LT to RBO	:		
			271 & 272 273 & 274			5.0 V	IN	5.0 V IN								OUT				RBO to A RBI to A			
			275 & 274		"		OUT	IN			"					001				RBI to RBO			
11	Same to	ests, termin	al conditions	and limit	s as subar	oup 10 ex																	

 ^{1/} X = Input may be high level or low level.
 2/ Y = 30 volts for device type 05 and 15 volts for device type 07.
 3/ Output voltages shall be either:

⁽a) H = 2.4 volts minimum and L = 0.4 volts minimum when using high speed checker double comparator, or

⁽b) $H \ge 1.5$ volts and $L \le 1.5$ volts when using a high speed checker single comparator.

^{4/} CKT except C.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	IN B	IN C	LT	RBO	RBI	IN D	IN A	GND	OUT E	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{CC}		Min	Max	
1	V _{OH1}	3006	1	Х	X	0.8 V		X	X	Х	GND	-0.4 mA							4.5 V	OUT E	2.4 V		V
Tc = 25°C	<u>1</u> /		2 3										-0.4 mA	0.4 1						OUT D OUT C			
			3 4											-0.4 mA	-0.4 mA					OUT B			
		"	5						"						01111111	-0.4 mA				OUT A			
			6														-0.4 mA			OUT G			
		- "	7 8	0.8 V	0.01/	2.0 V	0 1	0.01/	0.01/	0.01/								-0.4 mA		OUT F RBO			
	OH2 OL1	3007	9	0.8 V	0.8 V	2.0 V	2 mA	0.8 V	0.8 V	0.8 V		6.4 mA								OUT E		0.4	
	OLI	"	10									0.4 1117	6.4 mA							OUT D		"	
V		"	11						"					6.4 mA						OUT C			
V			12	:											6.4 mA	0.4 4				OUT B		:	
			13 14													6.4 mA	6.4 mA			OUT A OUT G			
		"	15				"										0.41117	6.4 mA		OUT F			
	OL2	3007	16	0.8 V	0.8 V	2.0 V	8 mA	0.8 V	0.8 V	0.8 V									"	RBO		0.4	-
	I C		17	-12 mA	40 4															IN B		-1.5	
V			18 19		-12 mA		-12 mA													IN C RBO			
V			20				121117	-12 mA												RBI			
			21						-12 mA											IN D			
		0000	22	0.41/	5.5.7	5.5.7		5.5.1/	5 5 V	-12 mA	-								5.5.7	IN A	0.7	1.0	" A
	IL1	3009	23 <u>2</u> / 23 CKT B	0.4 V 0.4 V	5.5 V 5.5 V	5.5 V 5.5 V		5.5 V	5.5 V	5.5 V									5.5 V	IN B IN B	-0.7 -0.4	-1.6 -1.3	mA "
		"	24 <u>2</u> /	5.5 V	0.4 V	5.5 V														IN C	-0.7	-1.6	
1			24 CKT B		0.4 V	5.5 V			"											IN C	-0.4	-1.3	
			25 <u>2</u> / 25 CKT B		5.5 V	0.4 V 0.4 V														LT LT	-0.7 -0.4	-1.6	
						5.5 V		0.4 V												RBI	-0.4 -0.7	-1.3 -1.6	
			26 <u>2</u> / 26 CKT B			0.0 .		0.4 V												RBI	-0.4	-1.3	
		"	27 <u>2</u> /					5.5 V	0.4 V											IN D	-0.7	-1.6	V
			27 CKT B 28 <u>2</u> /						0.4 V 5.5 V	0.4 V	·									IN D IN A	-0.4 -0.7	-1.3 -1.6	
			28 CKT B						5.5 V	0.4 V										IN A	-0.7	-1.3	
	IL2	3009	29		"	"	0.4 V	5.5 V	5.5 V	5.5 V										RBO	-1.7	-4.2	mA
	IH1	3010	30	2.4 V	GND	GND		GND	GND	GND	"									IN B		40	μА
			31	GND	2.4 V	GND		GND	"											IN C		:	
!			32 33		GND "	2.4 V GND		GND 2.4 V												LT RBI			
'		"	34			GND		GND	2.4 V											IN D			
		"	35		"	GND		GND"	GND	2.4 V									"	IN A			
	IH2	3010	36	5.5 V	GND	GND		GND	GND	GND "										IN B		100	μA "
			37 38	GND "	5.5 V GND	GND 5.5 V		GND GND	"											IN C LT			
			39		"	GND		5.5 V												RBI			
			40	:		:		GND	5.5 V	"	:								:	IN D			:
			41		- "			GND"	GND	5.5 V						CND				IN A		"	-
	os	3011	42 43	X	X "	GND "		X	X "	X "					GND	GND				OUT A OUT B		-4 "	mA "
			44											GND	0.10					OUT C			
1		"	45						"				GND							OUT D			
			46 47									GND						GND		OUT E OUT F			
			47				"										GND	GND		OUT G			
		"	49				GŅD		"										"	RBO			
	CC	3005	50	"	"	"	- "	"	"	"	"								"	V _{CC}	•	76	mA
2			al conditions																				
3	Same to	ests, termina	al conditions	, and limit	s as subgr	oup 1, exc	ept I _C = -	oo ℃ and `	v _{IC} tests a	re omitted													

See footnotes at end of device types 08.

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TABLE III. Group A inspection for device type 08. Terminal conditions (pins not designated may be high \geq 2.4 V or low \leq 0.8 V; or open).

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	nits	Unit
Subgroup	Symbol	883	Test no.	IN B	IN C	LT	RBO	RBI	IN D	IN A	GND	OUT E	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{cc}		Min	Max	
		method																					
7	Truth		51	GND	GND	5.0 V		5.0 V	GND	GND	GND	Н	Н	Н	Н	Н	L	Н	5.0 V				
Tc = 25°C	table		52	GND	"			Х	"	5.0 V		L	L	Н	"	L	L	L	"				
	test		53	5.0 V						GND		Н	Н	L	"	Н	Н	"	"				
			54	5.0 V						5.0 V		Ļ	H	H		H		l .".	l :		0.1		
			55	GND	5.0 V					GND		· ·	L.			<u>L</u>		H	l :		<u>3</u> /		
			56 57	GND 5.0 V						5.0 V GND		Н	H H		<u> </u>	H							
			57 58	5.0 V 5.0 V						5.0 V		L	L		H	H	L						
			59	GND	GND				5.0 V	GND		H	H		7		н	Ь					
			60	GND	GIND				3.0 V	5.0 V		Ë	''			"	';'	Н					
			61	5.0 V						GND		H	H	L	L	L		l ï					
			62	5.0 V						5.0 V		Ĺ	H	H	L	"	"	L					
			63	GND	5.0 V		"			GND			L	L	н	"	"	Н					
			64	GND	"			"	"	5.0 V			Н	"	L	Н	"	"					
			65	5.0 V						GND		Н	Н			L	"	"					
			66	5.0 V	"			"	"	5.0 V		L	L			"	L	L					
			67	X	X	X	GND	X	X	X													
			68	GND	GND	5.0 V GND	L	GND	GND	GND							l						
8	Truth		69 70 - 89	X	_ X	group 7, e		X 40500	Х	Х		Н	Н	Н	Н	Н	Н	Н	l				
0			70 - 69	Same les	SIS as Sub	group 7, e	xcept ic =	125-0.															
	table		00 -100	Como to	oto oo oub																		
	table test		90 -109	Same te	sts as sub	group 7, e																	
9	test	3003				group 7, e		-55°C.	GND	IN	GND					OUT			5.0 V	IN A to A	8	104	ns
9 Tc = 25°C	test t _{PHL}	3003 (Fig. 7)	90 -109 110 & 111 112 & 113	Same tes	sts as sub GND				GND "	IN "	GND "		OUT			OUT			5.0 V	IN A to A IN A to D	8 "	104	ns "
9 Tc = 25°C	test t _{PHL}	3003 (Fig. 7)	110 & 111			group 7, e		-55°C.	GND "			OUT	OUT			OUT			5.0 V		8 "	104	ns "
	test t _{PHL}		110 & 111 112 & 113		GND "	group 7, e		-55°C.	GND " "			OUT	OUT					OUT		IN A to D	8 "	104	ns "
	test t _{PHL}		110 & 111 112 & 113 114 & 115 116 & 117 118 & 119	GND " " 5.0 V	GND " " " 5.0 V	group 7, e		5.0 V	GND " "	"	"	OUT	OUT			OUT		OUT		IN A to D IN A to E IN A to F IN A to A	8 "	104	ns "
	test t _{PHL}		110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121	GND " " 5.0 V GND	GND " " 5.0 V 5.0 V	group 7, e		-55°C.	GND " " "			OUT			OUT			OUT		IN A to D IN A to E IN A to F IN A to A IN A to B	8	104	ns "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123	GND " " 5.0 V GND 5.0 V	GND " " 5.0 V 5.0 V 5.0 V	group 7, e	xcept T _C =	-55°C.	GND			OUT	OUT		OUT			OUT		IN A to D IN A to E IN A to F IN A to A IN A to B IN A to D	8	104	ns " "
	test t _{PHL}		110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125	GND " " 5.0 V GND 5.0 V GND	GND " " 5.0 V 5.0 V	group 7, e		5.0 V	GND "			OUT		QUIT	OUT			OUT		IN A to D IN A to E IN A to F IN A to A IN A to B IN A to D IN A to RBO	8	104	ns "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127	GND " " 5.0 V GND 5.0 V	GND " " 5.0 V 5.0 V 5.0 V	group 7, e	xcept T _C =	-55°C.	GND "			OUT		OUT	OUT					IN A to D IN A to E IN A to F IN A to A IN A to B IN A to D IN A to RBO IN B to C	8	104	ns "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129	GND " " 5.0 V GND 5.0 V GND	GND " " 5.0 V 5.0 V 5.0 V GND "	group 7, e	xcept T _C =	5.0 V	GND			OUT		OUT	OUT		OUT	OUT		IN A to D IN A to E IN A to F IN A to A IN A to B IN A to B IN A to RBO IN B to C IN B to F	8	104	ns " " " " " " " " " " " " " " " " " " "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131	GND " " 5.0 V GND 5.0 V GND	GND " " 5.0 V 5.0 V 5.0 V GND "	group 7, e	xcept T _C =	5.0 V " " " " GND 5.0 V		" " " " " GND		OUT		ОПТ	OUT	OUT	OUT			IN A to D IN A to E IN A to E IN A to A IN A to B IN A to B IN A to BO IN B to C IN B to F IN B to G	8	104	ns "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133	GND " " 5.0 V GND 5.0 V GND	GND " " 5.0 V 5.0 V 5.0 V GND " "	group 7, e	xcept T _C =	5.0 V " " " GND 5.0 V	5.0 V	GND		OUT		OUT	OUT		OUT	OUT		IN A to D IN A to E IN A to F IN A to B IN A to B IN A to B IN A to B IN B to C IN B to G IN B to A	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131	GND " " 5.0 V GND 5.0 V GND	GND " " 5.0 V 5.0 V 5.0 V GND "	group 7, e	xcept T _C =	5.0 V		GND		OUT		OUT	OUT	OUT	OUT			IN A to D IN A to E IN A to E IN A to A IN A to B IN A to B IN A to BO IN B to C IN B to F IN B to G	8	104	ns "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135	GND " " 5.0 V GND 5.0 V GND	GND " " 5.0 V 5.0 V GND " " 5.0 V	group 7, e	xcept T _C =	-55°C. 5.0 V	5.0 V	GND "		OUT		OUT	ОИТ	OUT		OUT		IN A to D IN A to E IN A to A IN A to A IN A to B IN A to B IN A to B IN B to B IN B to F IN B to A IN B to F IN B to F IN B to F IN B to F	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139	GND " " 5.0 V GND 5.0 V GND IN "	GND " 5.0 V 5.0 V 5.0 V GND " 5.0 V 5.0 V 5.0 V	group 7, e	xcept T _C =	5.0 V GND 5.0 V	5.0 V	GND " 5.0 V 5.0 V				OUT		OUT		OUT		IN A to D IN A to E IN A to F IN A to A IN A to B IN A to B IN A to B IN A to B IN B to F IN B to F IN B to G IN B to F IN B to G IN B to E IN B to E IN B to E IN B to E IN B to E	8	104	ns "
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141	GND " " 5.0 V GND 5.0 V GND	GND " 5.0 V 5.0 V GND " 5.0 V 5.0 V GND " IN	group 7, e	xcept T _C =	5.0 V GND 5.0 V	5.0 V GND	GND " 5.0 V 5.0 V GND			OUT	OUT	OUT	OUT		OUT		IN A to D E IN A to D F IN A A to F IN A A to B IN A A to B IN A to B IN A to B IN A to B IN A to B IN B B to C IN B B to C IN B B to C IN B B To C IN B B TO C TO C IN B TO C TO C IN B TO C TO C IN C TO C TO C TO C TO C	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143	GND " " " " " " " " " " " " " " " " " " "	GND " " 5.0 V 5.0 V GND " " 5.0 V GND " IN IN IN GND	group 7, e	xcept T _C =	5.0 V GND 5.0 V	5.0 V GND	GND			OUT	OUT	OUT	OUT		OUT		IN A to B IN A to F A IN A to B O IN A to B O IN A to B O IN A to B O IN A to B O IN A to B O IN B B to G IN B B to G IN B B to G IN B B to G IN B B to G IN B B to C TO TO TO TO TO TO TO TO TO TO TO TO TO	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145	GND 5.0 V GND 5.0 V GND IN 	GND " 5.0 V 5.0 V 5.0 V GND " 5.0 V 5.0 V IN IN GND GND	group 7, e	xcept T _C =	5.0 V GND 5.0 V	5.0 V GND "	GND			OUT			OUT		OUT		IN A to D E IN A A to F IN A A to F IN A A to B D IN A A to B D IN A A to B D IN A A to B D IN A to B to F IN B B to F IN B B to F IN B B to B D IN B B TO T IN B B TO T T T T T T T T T T T T T T T T	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147	GND 5.0 V GND 5.0 V GND IN 5.0 V	GND " " 5.0 V 5.0 V GND " " 5.0 V GND " IN GND GND GND GND GND	900 7, e	xcept T _C =	5.0 V GND 5.0 V	5.0 V GND "	GND " 5.0 V 5.0 V GND "			OUT	OUT	OUT	OUT		OUT		IN A to D E IN A A to F IN A A to B IN A A to B IN A A to B IN A A to B IN A to B IN A to B IN A to B IN A to B IN B to C IN B B to C IN B B to C IN B B TO C IN B B TO C IN B B TO C IN B B TO C IN B TO T IN C IN C IN C IN C IN C IN C IN C IN C	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147 148 & 149	GND	GND " " 5.0 V 5.0 V 5.0 V GND " " 5.0 V 5.0 V IN IN GND GND 5.0 V GND	5.0 V	out	-55°C. 5.0 V GND 5.0 V	5.0 V GND IN IN IN IN GND	5.0 V GND 5.0 V GND			OUT		OUT	OUT OUT OUT		OUT		IN A to B IN A to B IN A to B IN A to B IN A to B IN A to B IN A to B IN A to B IN A to B to B IN A to B to B IN A to B to B IN B B to F G IN B B to TO B IN B B TO TO TO B IN B B TO TO TO B IN B B TO TO TO TO TO TO TO TO	8	104	ns
	test t _{PHL}	(Fig. 7)	110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145 146 & 147	GND	GND " " 5.0 V 5.0 V GND " " 5.0 V GND " IN GND GND GND GND GND	900 7, e	xcept T _C =	5.0 V GND 5.0 V	5.0 V GND "	GND " 5.0 V 5.0 V GND "			OUT		OUT	OUT		OUT		IN A to D E IN A A to F IN A A to B IN A A to B IN A A to B IN A A to B IN A to B IN A to B IN A to B IN A to B IN B to C IN B B to C IN B B to C IN B B TO C IN B B TO C IN B B TO C IN B B TO C IN B TO T IN C IN C IN C IN C IN C IN C IN C IN C	8	104	ns

See footnotes at end of device types 08.

		MIL-STD-	Cases E, F	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Lim	its	Unit
Subgroup	Symbol	883 method	Test no.	IN B	IN C	LT	RBO	RBI	IN D	IN A	GND	OUT E	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{CC}	1	Min	Max	
10	t _{PHI}	3003	156 & 157	GND	GND	5.0 V		5.0 V	GND	IN	GND					OUT			5.0 V	IN A to A	8	144	ns
Tc = 125°C	t _{PLH}	(Fig. 7)	158 & 159	"	"	"		"	"	"	"		OUT			00.			"	IN A to D	"		"
10 - 120 0	TEN	(1.5.1)	160 & 161									OUT							"	IN A to E	"	"	
			162 & 163									00.						OUT	"	IN A to F	"	"	
			164 & 165	5.0 V	5.0 V											OUT			"	IN A to A	"	"	"
			166 & 167	GND	5.0 V										OUT				"	IN A to B	"	"	
			168 & 169	5.0 V	5.0 V								OUT						"	IN A to D	"	"	"
			170 & 171	GND	GND		OUT	GND											"	IN A to RBO	"	"	"
			172 & 173	IN				5.0 V		GND				OUT					"	IN B to C	"	"	"
			174 & 175															OUT	"	IN B to F	"	"	
			176 & 177														OUT		"	IN B to G	"	"	
			178 & 179						5.0 V	"						OUT			"	IN B to A	"	"	"
			180 & 181		5.0 V				GND	5.0 V								OUT	"	IN B to F	"	"	"
			182 & 183		5.0 V			"		5.0 V							OUT		"	IN B to G	"	"	"
			184 & 185		5.0 V					GND		OUT							"	IN B to E	"	"	"
			186 & 187		IN								OUT						"	IN C to D	"	"	
			188 & 189	5.0 V	IN	"		"		"					OUT				"	IN C to B	"	"	
			190 & 191		GND	"		"	IN	"						OUT			"	IN D to A	"	"	
			192 & 193		GND	"		"	IN	"					OUT				"	IN D to B	"	"	
			194 & 195	GND	5.0 V	"		"	IN					OUT					"	IN D to C	"	"	
			196 & 197	GND	GND	IN		GND	GND	"						OUT			"	LT to A	"	"	
			198 & 199	Χ	Х	GND	IN	Х	Х	Х						OUT			"	RBO to A	"	"	"
			200 & 201	GND	GND	5.0 V		IN	GND	GND						OUT			"	RBI to A	"	"	"
11	Same to	ests, termina	al conditions,	and limits	s as subgr	oup 10 exc	cept $T_C = -$	-55°C.															

- X = Input may be high level or low level.

4

- 2 CKT except B.
 Output voltages shall be either:
 (a) H = 2.4 volts minimum and L = 0.4 volts minimum when using high speed checker double comparator, or
 (b) H ≥ 1.5 volts and L ≤ 1.5 volts when using a high speed checker single comparator.

TABLE III. Group A inspection for device type 09. Terminal conditions (pins not designated may be high \geq 2.0 V or low \leq 0.8 V or open).

	1								_						- 10	- 10					
0	0	MIL-STD- 883	Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured	Lim		Unit
Subgroup	Symbol		A,B,C,D	IN D	11.1.0		11.15	18.1.4	OUT E	GND	OUTD	OUT C	OUTD	OUT A	OUT O	OUT F	\ /	ivieasured			Unit
	\ /	method	Test no.	IN B	IN C	BI	IN D	IN A			0010	0010	OUT B	OUT A	OUT G	0011	V _{CC}	_	Min	Max	
1	V _{OL}	3007	1	X "	X	0.8 V	X	X	10 mA	GND	10 mA						4.5 V	E minal _D		0.4	V
$Tc = 25^{\circ}C$	<u>1</u> /		2	"	"	"	"	"			10 MA	40 4					" (6)	_			
			3 4	"	"	"	66	"		44		10 mA	A					C B			
			5	"	"	44	66	"					mA	10 mA							
			6	"	"	"	"	"		"				10 MA	10 mA			A G			
		"	7	"	"	"	"	"	**	"		10			10 IIIA	10 mA		F			
			8	0.8 V	0.8 V	2.0 V	2.0 V	0.8 V	5.5,V	"						TOTILA		E		250	μА
	CEX		9	0.6 V	0.6 V	2.0 V	2.0 V	0.6 V	J.Q, V		5.5 V							D		230	μΑ "
			10	"	"	"	"	"		"	5.5 V	5.5 V						C			
			11	"	44	44	66	**		66		3.5 V	5.5 V				"	В			
l l			12	"	"	"	"	"		"			3.5 V	5.5 V			"	A			
			13	"	"	"	"	"		"				0.0 V	5.5 V		"	G		"	
			14	"	"	44	66	"		44					0.0 V	5.5 V	"	F		"	
	I C		15					-12 mA		"						0.0 1	"	IN A		-1.5	"
	10		16	-12 mA						"							"	IN B		"	"
			17		-12 mA					44							"	IN C		"	
V			18				-12 mA			"							"	IN D		"	"
V			19			-12 mA				"							"	BI		"	"
	IL	3009	20 <u>2</u> /	5.5 V	5.5 V	5.5 V	5.5 V	0.4 V		"							5.5 V	IN A	-0.7	-1.6	mA
	· -	"	20 CKT C	5.5 V	"	"	"	0.4 V		"							"	IN A	-0.4	-1.3	"
		"	21 <u>2</u> /	0.4 V	"	"	"	5.5 V		"							"	IN B	-0.7	-1.6	
		"	21 CKT C	0.4 V	"	"	"	"		"							"	IN B	-0.4	-1.3	
l l		"	22 <u>2</u> /	5.5 V	0.4 V	"	"	"		"							"	IN C	-0.7	-1.6	"
		"	22 CKT C	"	0.4 V	"	"	"		"							"	IN C	-0.4	-1.3	"
		"	23 <u>2</u> /	"	5.5 V	"	0.4 V	"		"							"	IN D	-0.7	-1.6	. "
		"	23 CKT C	"	"	"	0.4 V	"		"							"	IN D	-0.4	-1.3	. "
			24 <u>2</u> /		"	0.4 V	5.5 V			"								BI	-0.7	-1.6	
			24 CKT C		"	0.4 V	5.5 V										"	BI	-0.4	-1.3	
	IH1	3010	25	GND	GND	GND	GND	2.4 V										IN A		40	μΑ
			26	2.4 V	GND	"		GND		"								IN B			
			27	GND	2.4 V	"		"		"							"	IN C			. ".
I			28		GND		2.4 V			"							"	IN D		"	
			29			2.4 V	GND	551/		"								BI		100	
	IH2	3010	30		"	GND	l	5.5 V		"							l	IN A		100	μA
			31	5.5 V			"	GND									"	IN B		"	
			32	GND	5.5 V		·			"							"	IN C		"	
I			33		GND		5.5 V										"	IN D		"	
		2005	34	CND	CND	5.5 V	GND	OND										BI		47	
	CC	3005	35	GND	GND	GND	GND	GND			l	l	l	l				V_{CC}		47	mA
2		ts, terminal cor																			
3	same tes	ts, terminal cor	naitions, and li	imits as sub	group 1, ex	cept I _C = -	oo C and V	_C tests are	omitted.												

See footnotes at end of device type 09.

TABLE III. <u>Group A inspection for device type 09</u> – Continued. Terminal conditions (pins not designated may be high \geq 2.0 V or low \leq 0.8 V or open).

Subgroup	Symbol	MIL-STD- 883	Cases A,B,C,D	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Measured	Lir	nits	Unit
ouzg.oup	Cy	method	Test no.	IN B	IN C	BI	IN D	IN A	OUT E	GND	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V _{CC}	terminal	Min	Max	0
7	Truth		36	GND	GND		GND	GND	Н	GND	Н	Н	Н	Н	1	Н	5.5 V				
Tc = 25°C	table		37	"	"		"	5.0 V	L	"	L	H		Ë	Ĺ	Ľ	0.0 0				
10 - 20 0	labio		38	5.0 V			"	GND	Н	"	H	L	"	H	H	-					
			39	5.0 V			"	5.0 V	L	"	н	Н		H	"						
			40	GND	5.0 V		"	GND	Ĺ	"	L			Ĺ	"	Н					
test			41	GND	"		"	5.0 V	Ĺ	"	H	"	L	Н	"						
1031			42	5.0 V			"	GND	H	"	H		L	Ĺ	"	"			<u>3</u> /		
			43	5.0 V			"	5.0 V	L	"	L		H	H	L	L			<u> </u>		
			44	GND	GND		5.0 V	GND	H	"	H	"	"	"	Н	Н					
			45	GND	"		"	5.0 V	i	44	L				"	Н					
			46	5.0 V	"		"	GND	H	"	Н	L	L	L	"	L					
			47	5.0 V	"		"	5.0 V	L	"	Н	Н	L	"	"	L					
			48	GND	5.0 V		"	GND	L	"	L	L	Н	"	"	Н					
			49	GND	"		"	5.0 V	L	"	Н	"	L	Н	"	"	"				
			50	5.0 V	"		"	GND	Н	"	Н	"	"	L	"	"	"				
			51	5.0 V	"		"	5.0 V	L	"	L	"	"	"	L	L					
			52	Χ	Χ	GND	X	X	L	"	L	"	"	"	L	L	"				
8	Truth		53 to 69	Same te	sts as sul	ogroup 7,	except T _c	= 125°C.													
	table		70 to 76	Same te	sts as sul	ogroup 7,	except T _C	= -55°C.													
	test					0 1 /	, ,														
9	t _{PHL}	3003	77 & 78	GND	GND	5.0 V	GND	IN		GND				OUT			5.0 V	IN A to A	8	104	ns
Tc = 25°C	t _{PLH}	(Fig 8)	79 & 80	"	"	"	"	"			OUT						"	IN A to D	"	"	"
			81 & 82	"	"	"	"	"	OUT	"							"	IN A to E	"	"	"
			83 & 84	"	"	"	"	"		"						OUT	"	IN A to F	"	"	"
			85 & 86	5.0 V	5.0 V	"	"	"	"	"				OUT			"	IN A to A	"	"	"
			87 & 88	GND	5.0 V	"	"	"		"			OUT				"	IN A to B	"	"	"
			89 & 90	5.0 V	5.0 V	"	"	"		"	OUT						"	IN A to D	"	"	"
			91 & 92	IN	GND	"	"	GND		"		OUT					"	IN B to C	"	"	"
			93 & 94	"	"	"	"	"		"						OUT	"	IN B to F	"	"	"
			95 & 96	"	"	"	"	"		"					OUT		"	IN B to G	"	"	"
			97 & 98	"	"	"	5.0 V	"		"				OUT			"	IN B to A	"	"	"
			99 & 100	"	5.0 V	"	GND	5.0 V		"						OUT	"	IN B to F	"	"	"
1			101 & 102	"	5.0 V	"	"	GND	OUT	"							"	IN B to E	"	"	"
			103 & 104	"	5.0 V	"	"	5.0 V		"					OUT		"	IN B to G	"	"	"
			105 & 106	GND	IN	"	"	GND		"	OUT						"	IN C to D	"	"	
			107 & 108	5.0 V	IN	"	"	GND		"			OUT				"	IN C to B	"	"	"
			109 & 110	GND	GND	"	IN	GND		"					OUT		"	IN D to G	"	"	"
			111 & 112	5.0 V	GND	"	IN	5.0 V		"			OUT				"	IN D to B	"	"	"
			113 & 114	GND	5.0 V	"	IN	GND		"		OUT					"	IN D to C	"	"	"
			115 & 116	GND	GND	IN	GND	GND		"	1			OUT	l		"	BI to A	"	"	"

See notes at end of device type 09.

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TABLE III. Group A inspection for device type 09 – Continued. Terminal conditions (pins not designated may be high $\geq 2.0 \text{ V}$ or low $\leq 0.8 \text{ V}$ or open).

		MIL-STD-	Cases	1	2	3	4	5	6	7	8	9	10	11	12	13	14				
Subgroup	Symbol	883	A,B,C,D															Measured	Lin	nits	Unit
		method	Test no.	IN B	IN C	BI	IN D	IN A	OUT E	GND	OUT D	OUT C	OUT B	OUT A	OUT G	OUT F	V_{CC}		Min	Max	
10	t _{PHL}	3003	117 & 118	GND	GND	5.0 V	GND	IN		GND				OUT			5.0 V	IN A to A	8	144	ns
Tc = 125°C	t _{PLH}	(Fig 8)	119 & 120	"	"	"	"	"			OUT						" ter	minal to D	"	"	"
			121 & 122	"	"	"	"	"	OUT	"							"	IN A to E		"	"
			123 & 124	"	"	"	"	"		"						OUT	"	IN A to F		"	"
			125 & 126	5.0 V	5.0 V	"	"	"	"	"				OUT			"	IN A to A	"	"	"
			127 & 128	GND	5.0 V	"	"	"		"			OUT				"	IN A to B		"	"
			129 & 130	5.0 V	5.0 V	"	"	"		"	OUT						"	IN A to D		"	"
			131 & 132	IN	GND	"	"	GND		"		OUT					"	IN B to C	"	"	"
			133 & 134	"	"	"	"	"		"						OUT	"	IN B to F	"	"	"
			135 & 136	"	"	"	"	"		"					OUT		"	IN B to G	"	"	"
			137 & 138	"	"	"	5.0 V	"		"				OUT			"	IN B to A	"	"	"
			139 & 140	"	5.0 V	"	GND	5.0 V		"						OUT	"	IN B to F	"	"	"
			141 & 142	"	5.0 V	"	"	GND	OUT	"							"	IN B to E	"	"	"
			143 & 144	"	5.0 V	"	"	5.0 V		"					OUT		"	IN B to G	"	"	"
			145 & 146	GND	IN	"	"	GND		"	OUT						"	IN C to D	"	"	"
			147 & 148	5.0 V	IN	"	"	GND		"			OUT				"	IN C to B	"	"	"
			149 & 150	GND	GND	"	IN	GND		"					OUT		"	IN D to G	"	"	"
			151 & 152	5.0 V	GND	"	IN	5.0 V		"			OUT				"	IN D to B	"	"	"
			153 & 154	GND	5.0 V	"	IN	GND		"		OUT					"	IN D to C	"	"	"
			155 & 156	GND	GND	IN	GND	GND						OUT				BI to A	"	-	"
11	Same to	ests, termina	al conditions	and limits	s as for su	bgroup 10), except	$\Gamma_{\rm C} = -55^{\circ}{\rm C}$).												

- $\underline{1}$ / X = Input may be high level or low level.

- CKT except B.

 Output voltages shall be either:

 (a) H = 2.4 volts minimum and L = 0.4 volts minimum when using high speed checker double comparator, or

 (b) H ≥ 1.5 volts and L ≤ 1.5 volts when using a high speed checker single comparator.

5. PACKAGING

5.1 <u>Packaging requirements.</u> For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When packaging of materiel is to be performed by DoD or in-house contractor personnel, these personnel need to contact the responsible packaging activity to ascertain packaging requirements. Packaging requirements are maintained by the Inventory Control Point's packaging activity within the Military Service or Defense Agency, or within the military service's system command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but it is not mandatory)

- 6.1 <u>Intended use.</u> Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.
 - 6.2 Acquisition requirements. Acquisition documents should specify the following:
 - a. Title, number, and date of the specification.
 - b. PIN and compliance identifier, if applicable (see 1.2).
 - c. Requirements for delivery of one copy of the conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
 - d. Requirements for certificate of compliance, if applicable.
 - Requirements for notification of change of product or process to contracting activity in addition to notification to the qualifying activity, if applicable.
 - Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
 - g. Requirements for product assurance options.
 - h. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements should not affect the part number. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
 - i. Requirements for "JAN" marking.
 - j. Packaging requirements (see 5.1).
- 6.3 <u>Qualification</u>. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-38535 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from DSCC-VQ, 3990 E. Broad Street, Columbus, Ohio 43123-1199.
- 6.4 <u>Superseding information</u>. The requirements of MIL-M-38510 have been superseded to take advantage of the available Qualified Manufacturer Listing (QML) system provided by MIL-PRF-38535. Previous references to MIL-M-38510 in this document have been replaced by appropriate references to MIL-PRF-38535. All technical requirements now consist of this specification and MIL-PRF-38535. The MIL-M-38510 specification sheet number and PIN have been retained to avoid adversely impacting existing government logistics systems and contractor's parts lists.

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6.5 <u>Abbreviations, symbols, and definitions.</u> The abbreviations, symbols, and definitions used herein are defined in MIL-PRF-38535, MIL-HDBK-1331, and as follows:

GND	Ground zero voltage potential		
V _{IN}	Voltage level at an input terminal		
l _{IN}	Current flowing into an input terminal		

- 6.6 <u>Logistic support.</u> Lead materials and finishes (see 3.4) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish A (see 3.4). Longer length leads and lead forming should not affect the part number.
- 6.7 <u>Substitutability.</u> The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-35810 device types and may have slight physical variations in relation to case size. The presence of this information should not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-PRF-38535.

Military device type	Generic-industry type	
01	5442	
02	5443	
03	5444	
04	5445	
05	54145	
06	5446	
07	5447	
08	5448	
09	5449	

6.8 <u>Manufacturers' designation.</u> Manufacturers' circuits which form a part of this specification are designated with an "X" as shown in table IV herein.

TABLE IV. Manufacturers' designations.

Device	Circuit				
type	Texas Instruments	Signetics	National Semiconductor/ Fairchild Semiconductor	Motorola Inc.	Fairchild
01	D	С	E	В	Α
04	С	Α	E	В	D
05	С	Α	E	В	D
06	С	В	D		
07	С	В	D	_	
80	В	С	E	Α	

6.9 <u>Changes from previous issue.</u> Marginal notations are not used in this revision to identify changes with respect to the previous issue due to the extensiveness of the changes.

MIL-M-38510/10D

Preparing activity: DLA - CC Custodians:

Army - CR Navy - EC Air Force - 11 (Project 5962-2093)

DLA - CC

Review activities:

Army - MI, SM Navy - AS, CG, MC, SH, TD Air Force - 03, 19, 99

NOTE: The activities listed above were interested in this document as of the date of this document. Since organizations and responsibilities can change, you should verify the currency of the information above using the ASSIST Online database at http://assist.daps.dla.mil.